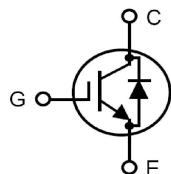


High Voltage, High Gain BiMOSFET™

IXBK64N250 IXBX64N250

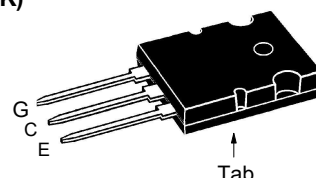
Monolithic Bipolar MOS Transistor



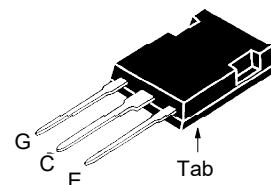
$$\begin{aligned} V_{CES} &= 2500V \\ I_{C110} &= 64A \\ V_{CE(sat)} &\leq 3.0V \end{aligned}$$

Symbol	Test Conditions	Maximum Ratings	
V_{CES}	$T_J = 25^\circ\text{C}$ to 150°C	2500	V
V_{CGR}	$T_J = 25^\circ\text{C}$ to 150°C , $R_{GE} = 1M\Omega$	2500	V
V_{GES}	Continuous	± 25	V
V_{GEM}	Transient	± 35	V
I_{C25}	$T_C = 25^\circ\text{C}$ (Chip Capability)	156	A
I_{LRMS}	Lead Current Limit, RMS	120	A
I_{C100}	$T_C = 110^\circ\text{C}$	64	A
I_{CM}	$T_C = 25^\circ\text{C}$, 1ms	800	A
SSOA (RBSOA)	$V_{GE} = 15V$, $T_{VJ} = 125^\circ\text{C}$, $R_G = 1\Omega$ Clamped Inductive Load	$I_{CM} = 160$ $V_{CE} \leq 0.8 \cdot V_{CES}$	A
T_{SC} (SCSOA)	$V_{GE} = 15V$, $T_J = 125^\circ\text{C}$, $R_G = 5\Omega$, $V_{CE} = 1250V$, Non-Repetitive	10	μs
P_C	$T_C = 25^\circ\text{C}$	735	W
T_J		-55 ... +150	$^\circ\text{C}$
T_{JM}		150	$^\circ\text{C}$
T_{stg}		-55 ... +150	$^\circ\text{C}$
T_L	Maximum Lead Temperature for Soldering 1.6 mm (0.062 in.) from Case for 10	300	$^\circ\text{C}$
M_d F_C	Mounting Torque (TO-264) Mounting Force (PLUS247)	1.13/10 20..120/4.5..27	Nm/lb.in. N/lb.
Weight	TO-264	10	g
	PLUS247	6	g

TO-264
(IXBK)



PLUS247™
(IXBX)



G = Gate C = Collector
E = Emitter Tab = Collector

Features

- High Blocking Voltage
- Low Switching Losses
- High Current Handling Capability
- Anti-Parallel Diode

Advantages

- High Power Density
- Low Gate Drive Requirement

Applications

- Switch-Mode and Resonant-Mode Power Supplies
- Uninterrupted Power Supplies (UPS)
- Capacitor Discharge Circuits
- Laser Generators

Symbol	Test Conditions ($T_J = 25^\circ\text{C}$, Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
BV_{CES}	$I_C = 1\text{mA}$, $V_{GE} = 0V$	2500		V
$V_{GE(th)}$	$I_C = 4\text{mA}$, $V_{CE} = V_{GE}$	3.0		5.0 V
I_{CES}	$V_{CE} = 0.8 \cdot V_{CES}$, $V_{GE} = 0V$ $T_J = 125^\circ\text{C}$			50 μA 6 mA
I_{GES}	$V_{CE} = 0V$, $V_{GE} = \pm 25V$			± 200 nA
$V_{CE(sat)}$	$I_C = I_{C110}$, $V_{GE} = 15V$, Note 1 $T_J = 125^\circ\text{C}$		2.5 3.1	V V

Symbol Test Conditions
 $(T_J = 25^\circ\text{C}$ Unless Otherwise Specified)

Characteristic Values
Min. Typ. Max.

g_{fs}	$I_C = I_{C110}, V_{CE} = 10V, \text{Note 1}$	40	72	S
C_{ies}	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		8900	pF
C_{oes}			345	pF
C_{res}			118	pF
Q_g	$I_C = I_{C110}, V_{GE} = 15V, V_{CE} = 600V$		400	nC
Q_{ge}			46	nC
Q_{gc}			155	nC
$t_{d(on)}$	Resistive Switching Times, $T_J = 25^\circ C$ $I_C = 128A, V_{GE} = 15V, tp = 1\mu s$ $V_{CE} = 1250V, R_G = 1\Omega$		49	ns
t_r			318	ns
$t_{d(off)}$			232	ns
t_f			170	ns
$t_{d(on)}$	Resistive Switching Times, $T_J = 125^\circ C$ $I_C = 128A, V_{GE} = 15V, tp = 1\mu s$ $V_{CE} = 1250V, R_G = 1\Omega$		54	ns
t_r			578	ns
$t_{d(off)}$			222	ns
t_f			175	ns
R_{thJC}			0.15	0.17 °C/W
R_{thCS}				°C/W

Reverse Diode
Symbol Test Conditions
 $(T_J = 25^\circ\text{C}$ Unless Otherwise Specified)

Characteristic Values
Min. Typ. Max

V_F	$I_F = I_{C110}, V_{GE} = 0V, \text{Note 1}$		3.0	V
t_{rr}	$I_F = I_{C110}, V_{GE} = 0V, -di_F/dt = 650A/\mu s$ $V_R = 600V, V_{GE} = 0V$		160	ns
I_{RM}			480	A

Note 1: Pulse test, $t \leq 300\mu s$, duty cycle, $d \leq 2\%$.

Additional provisions for lead-to-lead isolation are required at $V_{CE} > 1200V$.

Littelfuse reserves the right to change limits, test conditions, and dimensions.

IXYS MOSFETs and IGBTs are covered by one or more of the following U.S. patents:

4,835,592	4,931,844	5,049,961	5,237,481	6,162,665	6,404,065 B1	6,683,344	6,727,585	7,005,734 B2	7,157,338 B2
4,850,072	5,017,508	5,063,307	5,381,025	6,259,123 B1	6,534,343	6,710,405 B2	6,759,692	7,063,975 B2	
4,881,106	5,034,796	5,187,117	5,486,715	6,306,728 B1	6,583,505	6,710,463	6,771,478 B2	7,071,537	

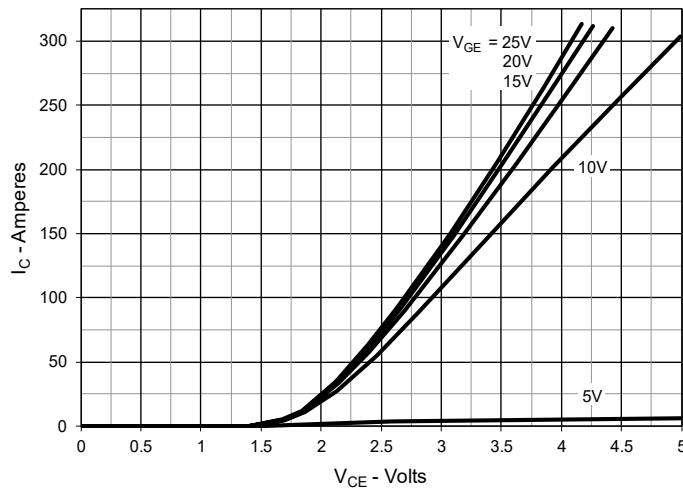
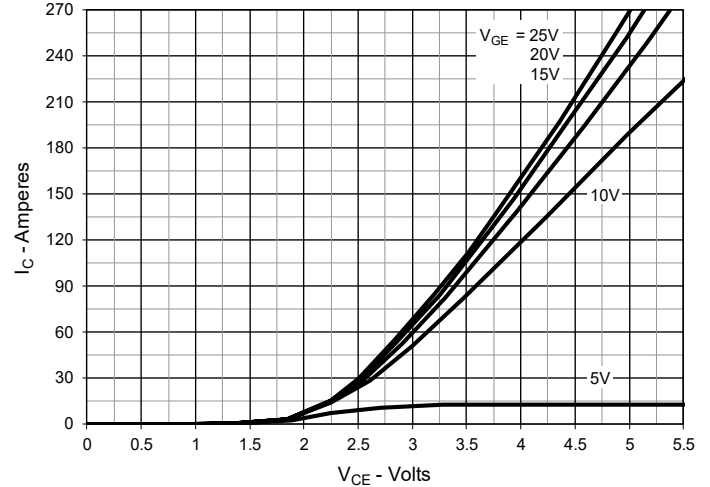
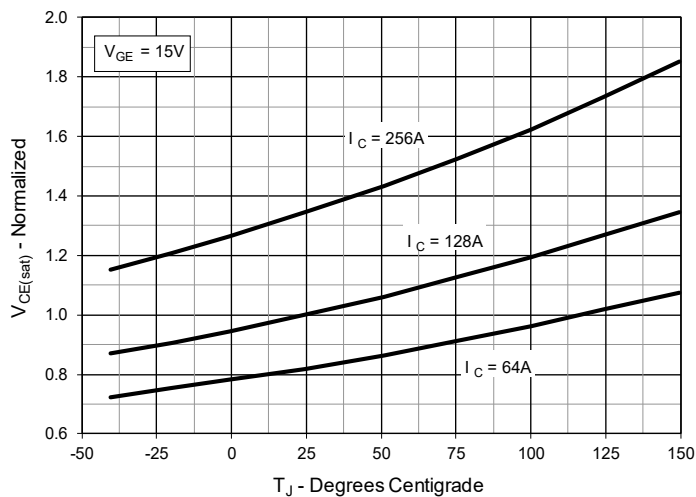
Fig. 1. Output Characteristics @ $T_J = 25^\circ\text{C}$

Fig. 2. Output Characteristics @ $T_J = 125^\circ\text{C}$

Fig. 3. Dependence of $V_{CE(sat)}$ on Junction Temperature


Fig. 4. Collector-to-Emitter Voltage vs. Gate-to-Emitter Voltage

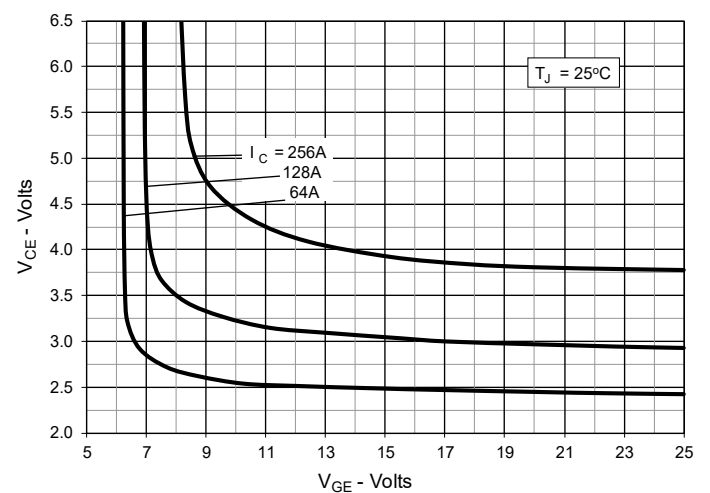


Fig. 5. Breakdown & Threshold Voltages vs. Junction Temperature

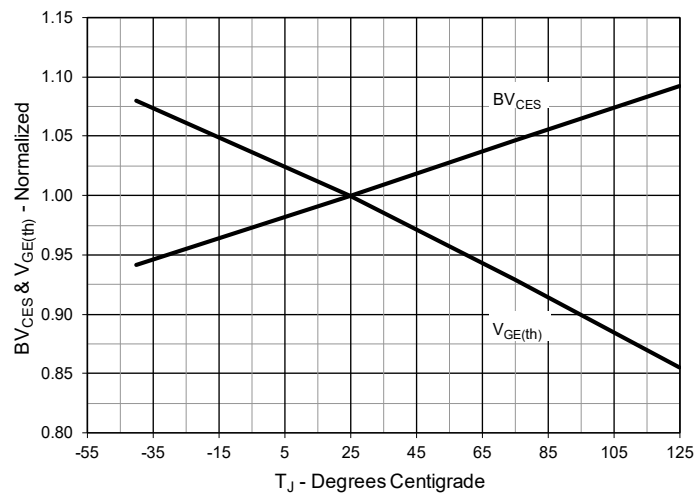


Fig. 6. Input Admittance

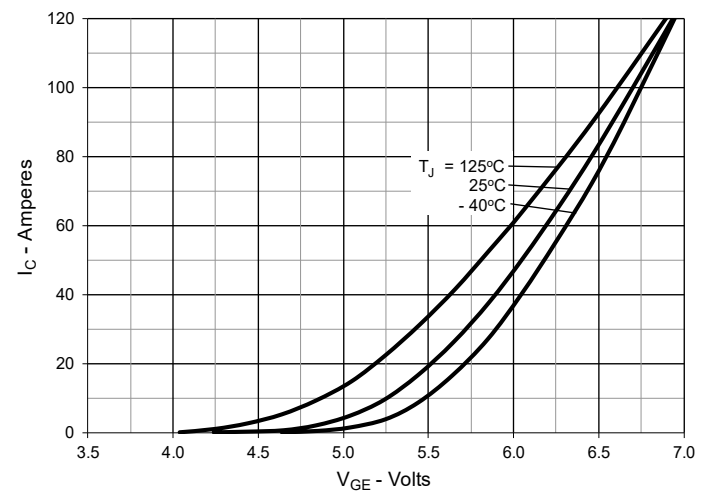


Fig. 7. Transconductance

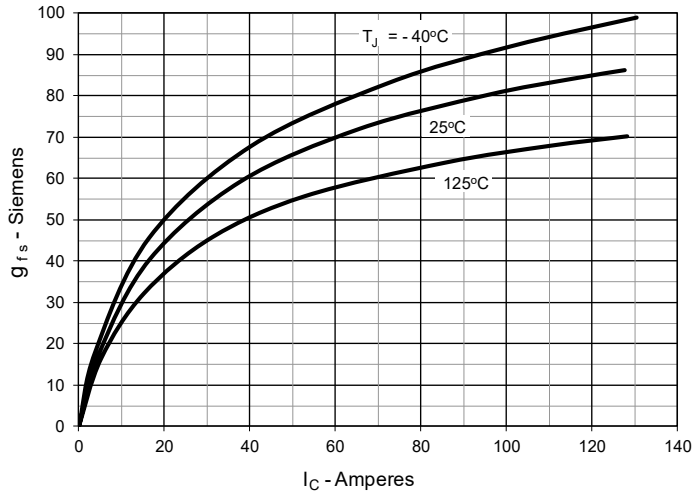


Fig. 8. Forward Voltage Drop of Intrinsic Diode

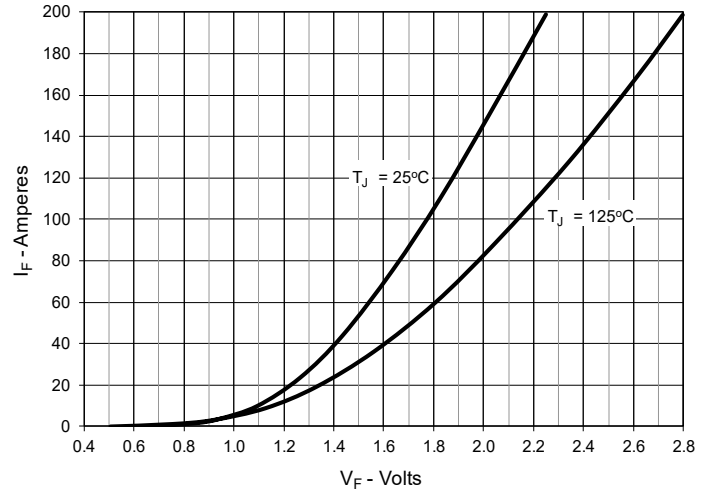


Fig. 9. Gate Charge

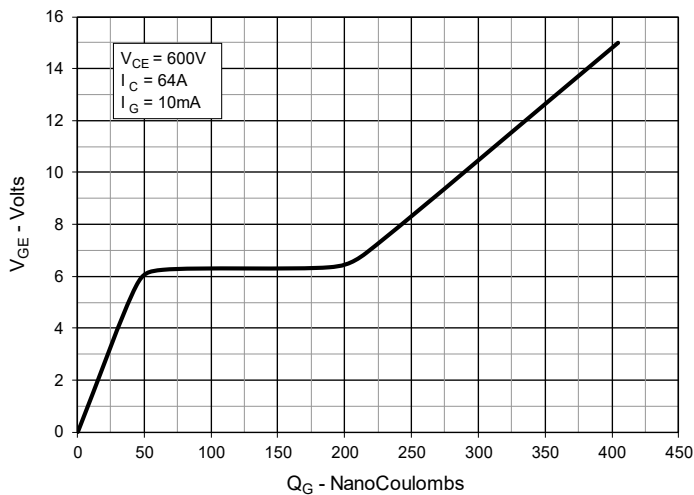


Fig. 10. Capacitance

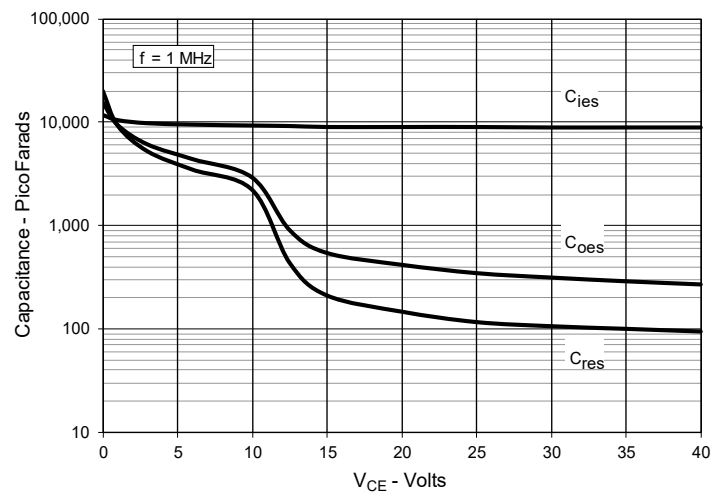


Fig. 11. Reverse-Bias Safe Operating Area

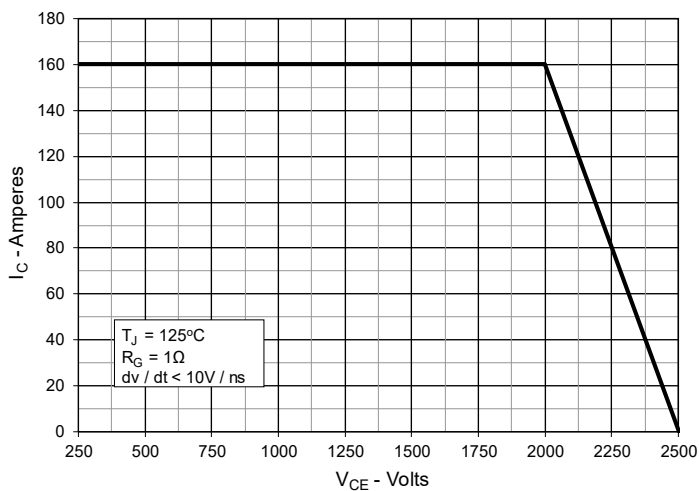


Fig. 12. Forward-Bias Safe Operating Area

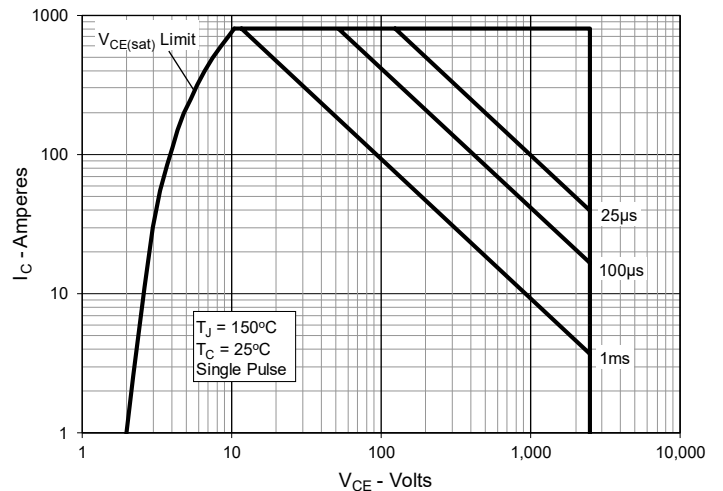


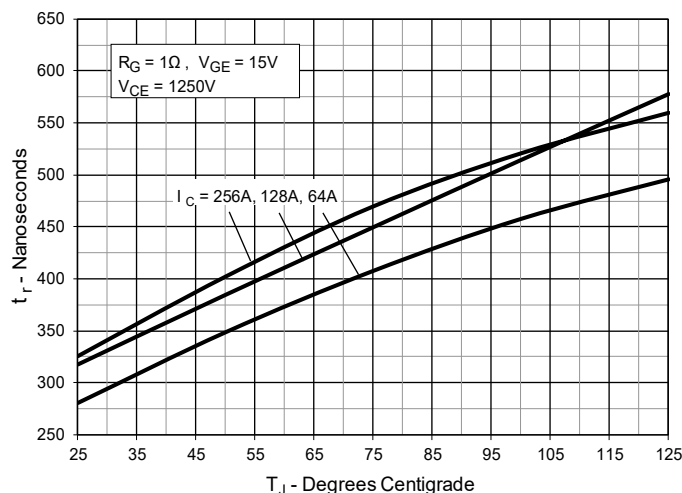
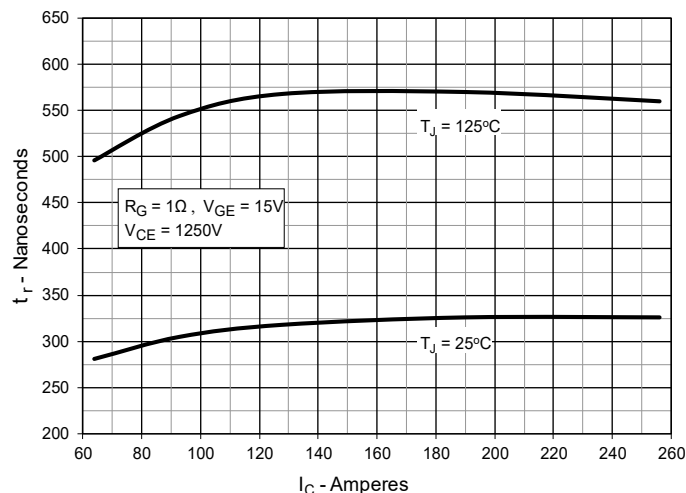
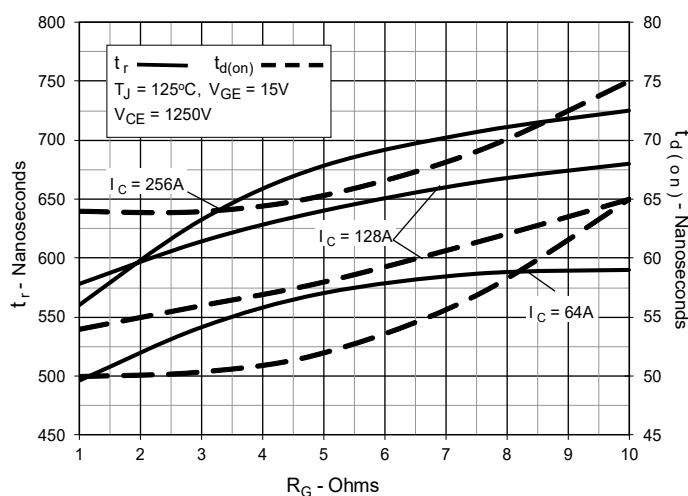
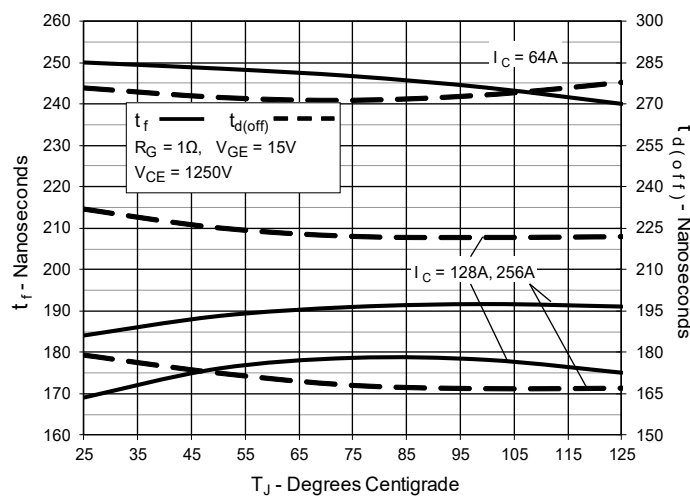
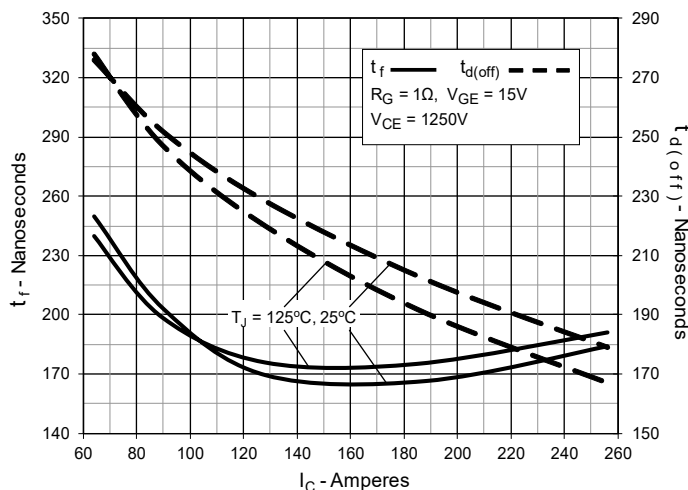
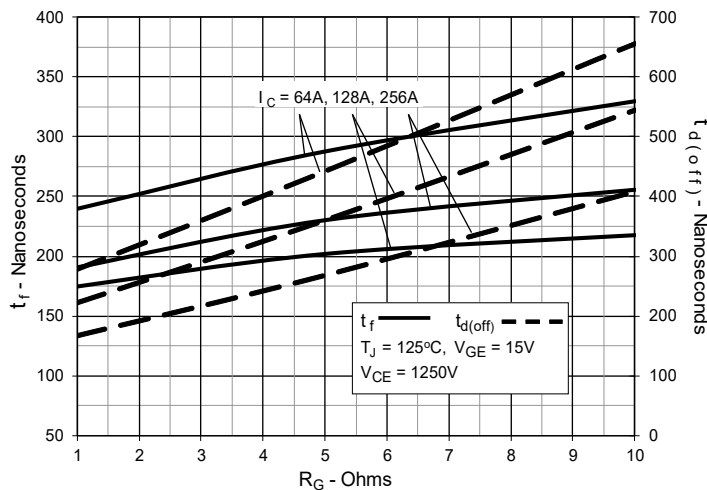
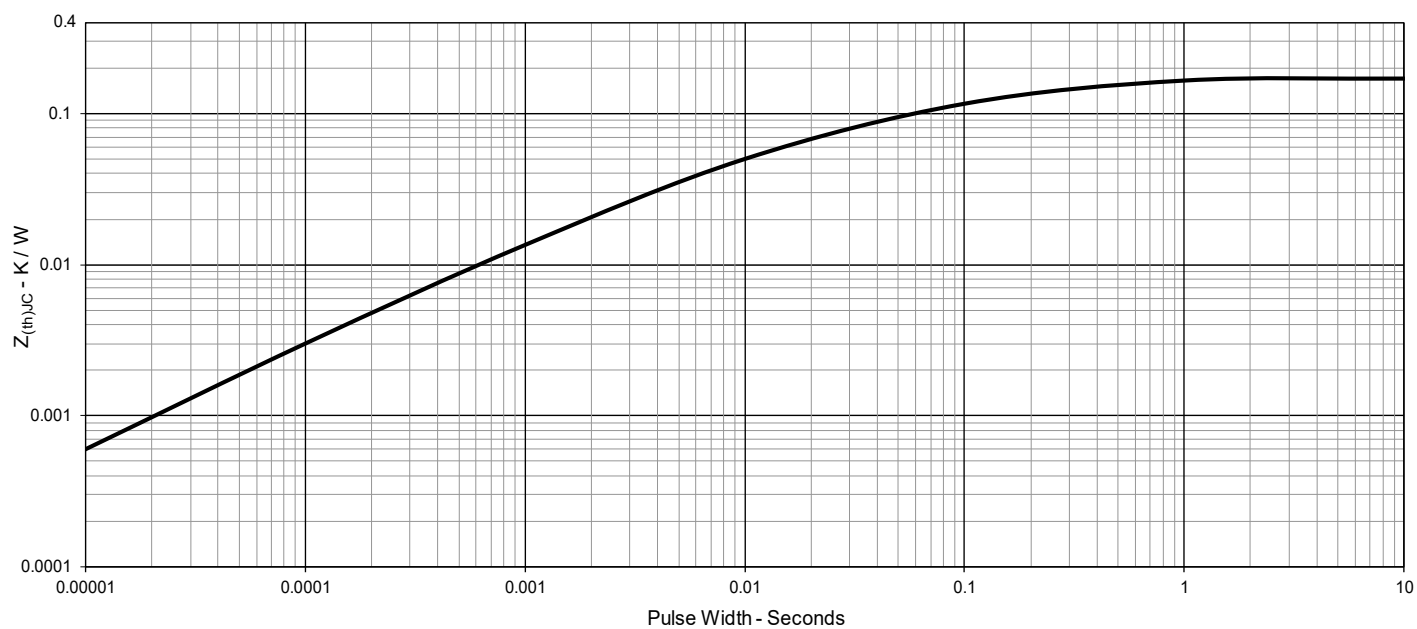
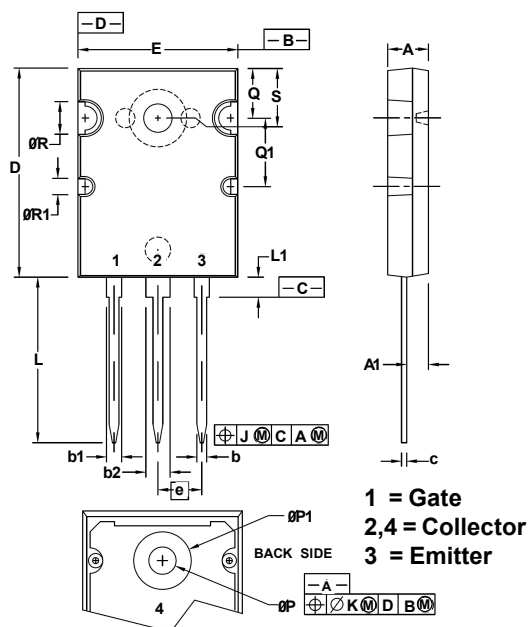
Fig. 13. Resistive Turn-on Rise Time vs. Junction Temperature

Fig. 14. Resistive Turn-on Rise Time vs. Drain Current

Fig. 15. Resistive Turn-on Switching Times vs. Gate Resistance

Fig. 16. Resistive Turn-off Switching Times vs. Junction Temperature

Fig. 17. Resistive Turn-off Switching Times vs. Drain Current

Fig. 18. Resistive Turn-off Switching Times vs. Gate Resistance


Fig. 19. Maximum Transient Thermal Impedance

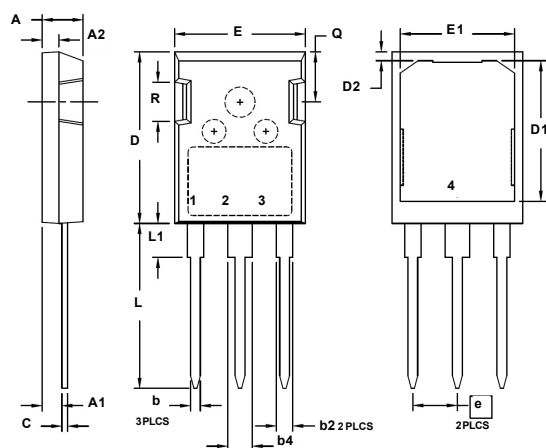


TO-264 Outline



SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.185	.209	4.70	5.31
A1	.102	.118	2.59	3.00
b	.037	.055	0.94	1.40
b1	.087	.102	2.21	2.59
b2	.110	.126	2.79	3.20
c	.017	.029	0.43	0.74
D	1.007	1.047	25.58	26.59
E	.760	.799	19.30	20.29
e	.215 BSC		5.46 BSC	
J	.000	.010	0.00	0.25
K	.000	.010	0.00	0.25
L	.779	.842	19.79	21.39
L1	.087	.102	2.21	2.59
øP	.122	.138	3.10	3.51
øP1	.270	.290	6.86	7.37
Q	.240	.256	6.10	6.50
Q1	.330	.346	8.38	8.79
øR	.155	.187	3.94	4.75
øR1	.085	.093	2.16	2.36
S	.243	.253	6.17	6.43

PLUS247™ Outline



SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.190	.205	4.83	5.21
A1	.090	.100	2.29	2.54
A2	.075	.085	1.91	2.16
b	.045	.055	1.14	1.40
b2	.075	.087	1.91	2.20
b4	.115	.126	2.92	3.20
C	.024	.031	0.61	0.80
D	.819	.840	20.80	21.34
D1	.650	.690	16.51	17.53
D2	.035	.050	0.89	1.27
E	.620	.635	15.75	16.13
E1	.520	.560	13.08	14.22
e	.215 BSC		5.45 BSC	
L	.780	.810	19.81	20.57
L1	.150	.170	3.81	4.32
Q	.220	.244	5.59	6.20
R	.170	.190	4.32	4.83



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Littelfuse reserves the right to change limits, test conditions, and dimensions.
