FEATURES AND BENEFITS

• No external sense resistor required; single package solution
• Reduced power loss:
  □ 0.6 mΩ internal conductor resistance
• Economical low- and high-side current sensing
• Output voltage proportional to AC or DC currents
• ±15.5 A and ±31 A full-scale sensing ranges
• Overcurrent FAULT trips and latches at 100% of full-scale current
• Low-noise analog signal path
• 100 kHz bandwidth
• Small footprint, low-profile SOIC8 and QFN packages
• New automotive-qualified wettable flank QFN package
• 3 to 5.5 V single supply operation
• Integrated electrostatic shield for output stability
• Factory-trimmed for accuracy
• Extremely stable output offset voltage
• Zero magnetic hysteresis
• Ratiometric output from supply voltage

DESCRIPTION

The Allegro™ ACS711 provides economical and precise solutions for AC or DC current sensing in <100 V audio, communications systems, and white goods. The device package allows for easy implementation by the customer. Typical applications include circuit protection, current monitoring, and motor and inverter control. The new wettable flank QFN package offering is ideally suited for in-cabin automotive applications and is automotive qualified.

The device consists of a linear Hall sensor circuit with a copper conduction path located near the surface of the die. Applied current flowing through this copper conduction path generates a magnetic field which is sensed by the integrated Hall IC and converted into a proportional voltage. Device accuracy is optimized through the close proximity of the magnetic signal to the Hall transducer.

The output of the device has a positive slope proportional to the current flow from IP+ to IP– (pins 1 and 2, to pins 3 and 4). The internal resistance of this conductive path is 0.6 mΩ, providing a non-intrusive measurement interface that saves power in applications that require energy efficiency.

The ACS711 is optimized for low-side current sensing applications, although the terminals of the conductive path are electrically isolated from the sensor IC leads, providing sufficient internal creepage and clearance dimensions for a low AC or DC working voltage applications. The thickness of the copper conductor allows survival of the device at up to 5× overcurrent conditions.

Typical Application

Application 1. The ACS711 outputs an analog signal, V_{OUT}, that varies linearly with the bi-directional AC or DC primary current, IP, within the range specified. The FAULT pin trips when IP reaches ±100% of its full-scale current.
Hall-Effect Linear Current Sensor with Overcurrent Fault Output for < 100 V Isolation Applications

ACS711

DESCRIPTION (CONTINUED)
The ACS711 is provided in a small, surface-mount packages: QFN12. The leadframe is plated with 100% matte tin, which is compatible with standard lead (Pb) free printed circuit board assembly processes. Internally, the device is Pb-free, except for flip-chip high-temperature Pb-based solder balls, currently exempt from RoHS. The device is fully calibrated prior to shipment from the factory.

SELECTION GUIDE

<table>
<thead>
<tr>
<th>Part Number</th>
<th>$T_A$ (°C)</th>
<th>Optimized Accuracy Range, $I_P$ (A)</th>
<th>Sensitivity$^{[1]}$, Sens (Typ) (mV/A)</th>
<th>Package</th>
<th>Packing$^{[2]}$</th>
</tr>
</thead>
<tbody>
<tr>
<td>ACS711KEXLT-15AB-J</td>
<td>–40 to 125</td>
<td>±15.5</td>
<td>90</td>
<td>12-contact QFN with fused current loop and wettable flank</td>
<td>1500 pieces/reel</td>
</tr>
<tr>
<td>ACS711KEXLT-31AB-J</td>
<td>–40 to 125</td>
<td>±31</td>
<td>45</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

$^{[1]}$ Sensitivity measured with $V_{CC} = 3.3$ V.

$^{[2]}$ Contact Allegro for additional packing options.

ABSOLUTE MAXIMUM RATINGS

<table>
<thead>
<tr>
<th>Characteristic</th>
<th>Symbol</th>
<th>Notes</th>
<th>Rating</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supply Voltage</td>
<td>$V_{CC}$</td>
<td></td>
<td>7</td>
<td>V</td>
</tr>
<tr>
<td>Reverse Supply Voltage</td>
<td>$V_{RCC}$</td>
<td></td>
<td>–0.1</td>
<td>V</td>
</tr>
<tr>
<td>Output Voltage</td>
<td>$V_{OUT}$</td>
<td></td>
<td>7</td>
<td>V</td>
</tr>
<tr>
<td>Reverse Output Voltage</td>
<td>$V_{RIOUT}$</td>
<td></td>
<td>–0.1</td>
<td>V</td>
</tr>
<tr>
<td>Working Voltage for Basic Isolation</td>
<td>$V_{WORKING}$</td>
<td>Voltage applied between pins 1-4 and 5-8</td>
<td>100</td>
<td>VAC peak or VDC</td>
</tr>
<tr>
<td>FAULT Pin Voltage</td>
<td>$V_{FAULT}$</td>
<td></td>
<td>7</td>
<td>V</td>
</tr>
<tr>
<td>Overcurrent Transient Tolerance</td>
<td>$I_{POC}$</td>
<td>1 pulse, 100 ms</td>
<td>100</td>
<td>A</td>
</tr>
<tr>
<td>Nominal Operating Ambient Temperature</td>
<td>$T_A$</td>
<td>Range K</td>
<td>–40 to 125</td>
<td>°C</td>
</tr>
<tr>
<td>Maximum Junction Temperature</td>
<td>$T_{J(max)}$</td>
<td></td>
<td>165</td>
<td>°C</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>$T_{stg}$</td>
<td></td>
<td>–65 to 170</td>
<td>°C</td>
</tr>
</tbody>
</table>
ACS711

Hall-Effect Linear Current Sensor with Overcurrent Fault Output for < 100 V Isolation Applications

Functional Block Diagram

PINOUT DIAGRAM

TERMINAL LIST TABLE

<table>
<thead>
<tr>
<th>Name</th>
<th>Number</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>GND</td>
<td>5</td>
<td>Signal ground terminal</td>
</tr>
<tr>
<td>FAULT</td>
<td>6</td>
<td>Overcurrent fault; active low</td>
</tr>
<tr>
<td>IP–</td>
<td>3 and 4</td>
<td>Terminals for current being sensed; fused internally</td>
</tr>
<tr>
<td>IP+</td>
<td>1 and 2</td>
<td>Terminals for current being sensed; fused internally</td>
</tr>
<tr>
<td>NC</td>
<td>7, 8, 9, 10</td>
<td>No connection; connect to GND for optimal ESD performance.</td>
</tr>
<tr>
<td>VCC</td>
<td>12</td>
<td>Device power supply terminal</td>
</tr>
<tr>
<td>VIOUT</td>
<td>11</td>
<td>Analog output signal</td>
</tr>
</tbody>
</table>
Hall-Effect Linear Current Sensor with Overcurrent Fault Output for < 100 V Isolation Applications

ACS711

COMMON OPERATING CHARACTERISTICS: Valid at $T_A = 25^\circ C$, $V_{CC} = 3.3\, V$, unless otherwise specified

<table>
<thead>
<tr>
<th>Characteristic</th>
<th>Symbol</th>
<th>Test Conditions</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>ELECTRICAL CHARACTERISTICS</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Supply Voltage</td>
<td>$V_{CC}$</td>
<td>VCC = 3.3 V, output open</td>
<td>3</td>
<td>3.3</td>
<td>5.5</td>
<td>V</td>
</tr>
<tr>
<td>Supply Current</td>
<td>$I_{CC}$</td>
<td></td>
<td>–</td>
<td>4</td>
<td>5.5</td>
<td>mA</td>
</tr>
<tr>
<td>Output Capacitance Load</td>
<td>$C_{LOAD}$</td>
<td>VIOUT to GND</td>
<td>–</td>
<td>–</td>
<td>1</td>
<td>nF</td>
</tr>
<tr>
<td>Output Resistive Load</td>
<td>$R_{LOAD}$</td>
<td>VIOUT to GND</td>
<td>15</td>
<td>–</td>
<td>–</td>
<td>kΩ</td>
</tr>
<tr>
<td>Primary Conductor Resistance</td>
<td>$R_{IP}$</td>
<td></td>
<td>–</td>
<td>0.6</td>
<td>–</td>
<td>–</td>
</tr>
<tr>
<td>VIOUT Rise Time</td>
<td>$t_r$</td>
<td>$I_p = I_{PMAX}$, $T_A = 25^\circ C$, COUT = open</td>
<td>–</td>
<td>3.5</td>
<td>–</td>
<td>µs</td>
</tr>
<tr>
<td>Propagation Delay Time</td>
<td>$t_{PROP}$</td>
<td>$I_p = I_{P(max)}$, $T_A = 25^\circ C$, COUT = open</td>
<td>–</td>
<td>1.2</td>
<td>–</td>
<td>µs</td>
</tr>
<tr>
<td>Response Time</td>
<td>$t_{RESPONSE}$</td>
<td>$I_p = I_{P(max)}$, $T_A = 25^\circ C$, COUT = open</td>
<td>–</td>
<td>4.6</td>
<td>–</td>
<td>µs</td>
</tr>
<tr>
<td>Internal Bandwidth</td>
<td>$BW_I$</td>
<td>3 dB, $T_A = 25^\circ C$</td>
<td>–</td>
<td>100</td>
<td>–</td>
<td>kHz</td>
</tr>
<tr>
<td>Nonlinearity</td>
<td>$E_{LIN}$</td>
<td>Over full range of $I_p$</td>
<td>–</td>
<td>±1</td>
<td>–</td>
<td>%</td>
</tr>
<tr>
<td>Symmetry</td>
<td>$E_{SYM}$</td>
<td>Apply full scale $I_p$</td>
<td>–</td>
<td>100</td>
<td>–</td>
<td>%</td>
</tr>
<tr>
<td>VIOUT Saturation Voltages</td>
<td>$V_{IOH}$</td>
<td>$VCC - 0.3$</td>
<td>–</td>
<td>–</td>
<td>–</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td>$V_{IOL}$</td>
<td></td>
<td>–</td>
<td>–</td>
<td>0.3</td>
<td>V</td>
</tr>
<tr>
<td>Quiescent Output Voltage</td>
<td>$V_{IOUT(0)}$</td>
<td>$I_p = 0, A$, $T_A = 25^\circ C$</td>
<td>–</td>
<td>$V_{CC} / 2$</td>
<td>–</td>
<td>V</td>
</tr>
<tr>
<td>Power-On Time</td>
<td>$t_{PO}$</td>
<td>Output reaches 90% of steady-state level, $T_A = 25^\circ C$, 20 A present on primary conductor</td>
<td>–</td>
<td>35</td>
<td>–</td>
<td>µs</td>
</tr>
</tbody>
</table>

**FAULT PIN CHARACTERISTICS**

| FAULT Operating Point | $I_{FAULT}$ | –   | ±1 × $I_p$ | –   | A     |
| FAULT Output Pullup Resistor | $R_{PU}$ | 1   | –   | –   | kΩ    |
| FAULT Output Voltage    | $V_{OH}$ | $V_{CC} - 0.3$ | –   | –   | V     |
|                        | $V_{OL}$ | $R_{PU} = 1\, k\Omega$ | –   | 0.3 | –   | V     |
| FAULT Response Time     | $t_{FAULT}$ | Measured from $|I_p| > |I_{FAULT}|$ to $V_{FAULT} \leq V_{OL}$ | –   | 1.3  | – | µs |
| $V_{CC}$ Off Voltage Level for Fault Reset[3] | $V_{CCFR}$ | –   | –   | 200 | mV    |
| $V_{CC}$ Off Duration for Fault Reset[3] | $t_{CCFR}$ | 100 | –   | –   | µs    |

[1] Devices are programmed for maximum accuracy at 3.3 V $V_{CC}$ levels. The device contains ratiometry circuits that accurately alter the 0 A Output Voltage and Sensitivity level of the device in proportion to the applied $V_{CC}$ level. However, as a result of minor nonlinearities in the ratiometry circuit additional output error will result when $V_{CC}$ varies from the 3.3 V $V_{CC}$ level. Customers that plan to operate the device from a 5 V regulated supply should contact their local Allegro sales representative regarding expected device accuracy levels under these bias conditions.

[2] Calculated using the formula $BW_I = 0.35 / t_r$.

[3] After the FAULT pin is latched low, the only way to reset it is through a power-off and power-on cycle on the VCC pin. For fault reset, $V_{CC}$ must stay below $V_{CCFR}$ for a period greater than $t_{CCFR}$ before settling to the normal operation voltage (3 to 5.5 V).
ACS711

Hall-Effect Linear Current Sensor with Overcurrent Fault Output for < 100 V Isolation Applications

### x15AB PERFORMANCE CHARACTERISTICS \(^\text{[1]}\)

\(T_A = 25^\circ\text{C} and V_{CC} = 3.3 \text{ V}, \text{unless otherwise specified}\)

<table>
<thead>
<tr>
<th>Characteristic</th>
<th>Symbol</th>
<th>Test Conditions</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Optimized Accuracy Range</td>
<td>(I_P)</td>
<td></td>
<td>–15.5</td>
<td>–</td>
<td>15.5</td>
<td>A</td>
</tr>
<tr>
<td>Sensitivity</td>
<td>Sens</td>
<td>Across full range of (I_P)</td>
<td>–</td>
<td>90</td>
<td>–</td>
<td>mV/A</td>
</tr>
<tr>
<td>Noise (^\text{[2]})</td>
<td>(V_{\text{NOISE}})</td>
<td>No external low pass filter on (V_{\text{OUT}})</td>
<td>–</td>
<td>11</td>
<td>–</td>
<td>mV</td>
</tr>
<tr>
<td>Electrical Offset Voltage</td>
<td>(V_{OE(TA)}) (I_P = 0 \text{ A})</td>
<td></td>
<td>–</td>
<td>±5</td>
<td>–</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td>(V_{OE(TOP)HT}) (I_P = 0 \text{ A, } T_A = 25^\circ\text{C to } T_A(\text{max}))</td>
<td></td>
<td>–</td>
<td>±40</td>
<td>–</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td>(V_{OE(TOP)LT}) (I_P = 0 \text{ A, } T_A = –40^\circ\text{C to } 25^\circ\text{C})</td>
<td></td>
<td>–</td>
<td>±50</td>
<td>–</td>
<td>mV</td>
</tr>
<tr>
<td>Total Output Error (^\text{[3]})</td>
<td>(E_{TOT}) (I_P = ±12.5 \text{ A, } T_A = –40^\circ\text{C to } T_A(\text{max}))</td>
<td></td>
<td>–</td>
<td>±5</td>
<td>–</td>
<td>%</td>
</tr>
</tbody>
</table>

\(^{[1]}\) See Characteristic Performance Data for parameter distributions across the full temperature range.

\(^{[2]}\) ±3 sigma noise voltage.

\(^{[3]}\) Percentage of \(I_P\) with \(I_P = ±15.5 \text{ A}\).

### x31AB PERFORMANCE CHARACTERISTICS \(^\text{[1]}\)

\(T_A = 25^\circ\text{C} and V_{CC} = 3.3 \text{ V}, \text{unless otherwise specified}\)

<table>
<thead>
<tr>
<th>Characteristic</th>
<th>Symbol</th>
<th>Test Conditions</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Optimized Accuracy Range</td>
<td>(I_P)</td>
<td></td>
<td>–31</td>
<td>–</td>
<td>31</td>
<td>A</td>
</tr>
<tr>
<td>Sensitivity</td>
<td>Sens</td>
<td>Across full range of (I_P)</td>
<td>–</td>
<td>45</td>
<td>–</td>
<td>mV/A</td>
</tr>
<tr>
<td>Noise (^\text{[2]})</td>
<td>(V_{\text{NOISE}})</td>
<td>No external low pass filter on (V_{\text{OUT}})</td>
<td>–</td>
<td>8</td>
<td>–</td>
<td>mV</td>
</tr>
<tr>
<td>Electrical Offset Voltage</td>
<td>(V_{OE(TA)}) (I_P = 0 \text{ A})</td>
<td></td>
<td>–</td>
<td>±5</td>
<td>–</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td>(V_{OE(TOP)HT}) (I_P = 0 \text{ A, } T_A = 25^\circ\text{C to } T_A(\text{max}))</td>
<td></td>
<td>–</td>
<td>±30</td>
<td>–</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td>(V_{OE(TOP)LT}) (I_P = 0 \text{ A, } T_A = –40^\circ\text{C to } 25^\circ\text{C})</td>
<td></td>
<td>–</td>
<td>±35</td>
<td>–</td>
<td>mV</td>
</tr>
<tr>
<td>Total Output Error (^\text{[3]})</td>
<td>(E_{TOT}) (I_P = ±12.5 \text{ A, } T_A = 25^\circ\text{C to } 125^\circ\text{C})</td>
<td></td>
<td>–</td>
<td>±5</td>
<td>–</td>
<td>%</td>
</tr>
<tr>
<td></td>
<td>(I_P = ±12.5 \text{ A, } T_A = –40^\circ\text{C to } 25^\circ\text{C})</td>
<td></td>
<td>–</td>
<td>±7</td>
<td>–</td>
<td>%</td>
</tr>
</tbody>
</table>

\(^{[1]}\) See Characteristic Performance Data for parameter distributions across the full temperature range.

\(^{[2]}\) ±3 sigma noise voltage.

\(^{[3]}\) Percentage of \(I_P\) with \(I_P = ±31 \text{ A}\).
## THERMAL CHARACTERISTICS

<table>
<thead>
<tr>
<th>Characteristic</th>
<th>Symbol</th>
<th>Test Conditions</th>
<th>Value</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Package Thermal Resistance, Junction to Lead</td>
<td>$R_{JL}$</td>
<td></td>
<td>5</td>
<td>°C/W</td>
</tr>
<tr>
<td>Package Thermal Resistance, Junction to Ambient</td>
<td>$R_{JA}$</td>
<td>Mounted on Allegro 85-0528 evaluation board, includes the power consumed by the board</td>
<td>24</td>
<td>°C/W</td>
</tr>
</tbody>
</table>

---


[2] The Allegro evaluation board has 1500 mm² of 2 oz. copper on each side, connected to pins 1 and 2, and to pins 3 and 4, with thermal vias connecting the layers. Performance values include the power consumed by the PCB. Further details on the board are available from the Frequently Asked Questions document on our website.
Hall-Effect Linear Current Sensor with Overcurrent Fault Output for < 100 V Isolation Applications

ACS711

Characteristic Performance Data
Data taken using the ACS711KEX-15A, $V_{CC} = 3.3$ V

QVO versus Ambient Temperature

-60 -40 -20 0 20 40 60 80 100 120 140

$T_A (^\circ C)$

1720
1700
1680
1660
1640
1620
1600
1580

QVO (mV)

QVO Error versus Ambient Temperature

-60 -40 -20 0 20 40 60 80 100 120 140

$T_A (^\circ C)$

60
40
20
0
-20
-40
-60

$V_{OE}$ (mV)

Sensitivity versus Ambient Temperature

-60 -40 -20 0 20 40 60 80 100 120 140

$T_A (^\circ C)$

94
92
90
88
86
84
82

Sensitivity (mV/A)

Total Error versus Ambient Temperature

-60 -40 -20 0 20 40 60 80 100 120 140

![](https://www.allegromicro.com/)

Symmetry versus Ambient Temperature

-60 -40 -20 0 20 40 60 80 100 120 140

$T_A (^\circ C)$

0.6
0.5
0.4
0.3
0.2
0.1
0
-0.1
-0.2

$E_{LN}$ (%)

Nonlinearity versus Ambient Temperature

-60 -40 -20 0 20 40 60 80 100 120 140

$T_A (^\circ C)$

0.6
0.5
0.4
0.3
0.2
0.1
0
-0.1
-0.2

$E_{SN}$ (%)

Mean
Typical Maximum Limit
Typical Minimum Limit

Allegro MicroSystems, LLC
955 Perimeter Road
Manchester, NH 03103-3353 U.S.A.
www.allegromicro.com

Downloaded from Arrow.com.
CHARACTERISTIC PERFORMANCE DATA
Data taken using the ACS711KEX-31AB, $V_{CC} = 3.3$ V

- **QVO versus Ambient Temperature**
- **QVO Error versus Ambient Temperature**
- **Sensitivity versus Ambient Temperature**
- **Total Error versus Ambient Temperature**
- **Symmetry versus Ambient Temperature**
- **Nonlinearity versus Ambient Temperature**
CHARACTERISTIC PERFORMANCE DATA

**Timing Data**

**Rise Time**

![Graph showing rise time data with Ip (10 A/div.) and VOUT (0.5 V/div.)](image)

- Rise Time: 3.47 µs

**Propagation Delay Time**

![Graph showing propagation delay time data with Ip (10 A/div.) and VOUT (0.5 V/div.)](image)

- Propagation Delay Time: 1.24 µs

**Response Time**

![Graph showing response time data with Ip (10 A/div.) and VOUT (0.5 V/div.)](image)

- Response Time: 4.62 µs

**Fault Response**

![Graph showing fault response data with Ip (10 A/div.) and Fault (2 V/div.)](image)

- Fault Response: 1.28 µs
DEFINITIONS OF ACCURACY CHARACTERISTICS

**Sensitivity (Sens).** The change in sensor output in response to a 1 A change through the primary conductor. The sensitivity is the product of the magnetic circuit sensitivity (G/Ω) and the linear IC amplifier gain (mV/G). The linear IC amplifier gain is programmed at the factory to optimize the sensitivity (mV/A) for the full-scale current of the device.

**Noise (V_{NOISE}).** The product of the linear IC amplifier gain (mV) and the noise floor for the Allegro Hall effect linear IC. The noise floor is derived from the thermal and shot noise observed in Hall elements. Dividing the noise (mV) by the sensitivity (mV/A) provides the smallest current that the device is able to resolve.

**Linearity (E_{LIN}).** The degree to which the voltage output from the sensor varies in direct proportion to the primary current through its full-scale amplitude. Nonlinearity in the output can be attributed to the saturation of the flux concentrator approaching the full-scale current. The following equation is used to derive the linearity:

\[
100 \left(1 - \frac{\Delta \text{gain} \times \% \text{sat} \left(V_{\text{OUT full-scale amperes}} - V_{\text{OUT(Q)}}\right)}{2 \left(V_{\text{OUT half-scale amperes}} - V_{\text{OUT(Q)}}\right)}\right)
\]

where \(V_{\text{OUT full-scale amperes}}\) = the output voltage (V) when the sensed current approximates full-scale \(\pm I_p\).

**Symmetry (E_{SYM}).** The degree to which the absolute voltage output from the sensor varies in proportion to either a positive or negative full-scale primary current. The following formula is used to derive symmetry:

\[
100 \left(\frac{V_{\text{OUT + full-scale amperes}} - V_{\text{OUT(Q)}}}{V_{\text{OUT(Q)}} - V_{\text{OUT - full-scale amperes}}}\right)
\]

**Quiescent output voltage (V_{\text{OUT(Q)}}).** The output of the sensor when the primary current is zero. For a unipolar supply voltage, it nominally remains at \(V_{CC}/2\). Thus, \(V_{CC} = 3.3 \text{ V}\) translates into \(V_{\text{OUT(Q)}} = 1.65 \text{ V}\). Variation in \(V_{\text{OUT(Q)}}\) can be attributed to the resolution of the Allegro linear IC quiescent voltage trim and thermal drift.

**Electrical offset voltage (V_{OE}).** The deviation of the device output from its ideal quiescent value of \(V_{CC}/2\) due to nonmagnetic causes. To convert this voltage to amperes, divide by the device sensitivity, Sens.

**Accuracy (E_{TOT}).** The accuracy represents the maximum deviation of the actual output from its ideal value. This is also known as the total output error. The accuracy is illustrated graphically in the output voltage versus current chart below.

**Ratiometry.** The ratiometric feature means that its 0 A output, \(V_{\text{OUT(Q)}}\) (nominally equal to \(V_{CC}/2\)) and sensitivity, Sens, are proportional to its supply voltage, \(V_{CC}\). The following formula is used to derive the ratiometric change in 0 A output voltage, \(\Delta V_{\text{OUT(Q)RAT}}\) (%):

\[
100 \left(\frac{V_{\text{OUT(Q)CC}} / V_{\text{OUT(Q)3.3V}}}{V_{CC} / 3.3 \text{ V}}\right)
\]

\[
100 \left(\frac{V_{\text{OUT(Q)Sens}} / V_{\text{OUT(Q)Sens3.3V}}}{V_{CC} / 3.3 \text{ V}}\right)
\]

**Output Voltage versus Sensed Current**

**Accuracy at 0 A and at Full-Scale Current**

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DEFINITIONS OF DYNAMIC RESPONSE CHARACTERISTICS

Power-On Time ($t_{PO}$). When the supply is ramped to its operating voltage, the device requires a finite time to power its internal components before responding to an input magnetic field. Power-On Time, $t_{PO}$, is defined as the time it takes for the output voltage to settle within ±10% of its steady state value under an applied magnetic field, after the power supply has reached its minimum specified operating voltage, $V_{CC\text{(min)}}$, as shown in the chart at right.

Rise time ($t_r$). The time interval between a) when the sensor reaches 10% of its full scale value, and b) when it reaches 90% of its full scale value. The rise time to a step response is used to derive the bandwidth of the current sensor, in which $f(-3\,\text{dB}) = 0.35/t_r$. Both $t_r$ and $t_{RESPONSE}$ are detrimentally affected by eddy current losses observed in the conductive IC ground plane.
APPLICATION INFORMATION

Layout
To optimize thermal and electrical performance, the following features should be included in the printed circuit board:

• The primary leads should be connected to as much copper area as is available.
• The copper should be 2 oz. or heavier.
• Additional layers of the board should be used for conducting the primary current if possible, and should be connected using the arrangement of vias shown below.

• The two solder pads at the ends of the exposed pad loop should be placed directly on the copper trace that conducts the primary current.
• When using vias under exposed pads, such as with the EX package, using plugged vias prevents wicking of the solder from the pad into the via during reflow. Whether or not to use plugged vias should be evaluated in the application.
Hall-Effect Linear Current Sensor with Overcurrent Fault Output for < 100 V Isolation Applications

Package EX, 12-Contact QFN
With Fused Sensed Current Loop and Wettable Flank

For reference only, not for tooling use (reference JEDEC MO-220WEED except for fused current path and wettable flank)
Dimensions in millimeters
Exact case and lead configuration at supplier discretion within limits shown
Terminal #1 mark area
Fused sensed current path
Reference land pattern layout (reference IPC7351 QFN05P300X300X61-17W4M); All pads a minimum of 0.20 mm from all adjacent pads; adjust as necessary to meet application process requirements and PCB layout tolerances; when mounting on a multilayer PCB, thermal vias at the exposed thermal pad land can improve thermal dissipation (reference EIA/JEDEC Standard JESD91-5)
Coplanarity includes exposed current path and terminals
Branding scale and appearance at supplier discretion
Hall elements (F1); not to scale

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