

HFB50HC20C

PD-94238C

Ultrafast, Soft Recovery Diode Thru-Hole (TO-258AA) 200V, 50A

Features

- Dual common cathode configuration
- Reduced RFI and EMI
- · Reduced snubbing
- Extensive characterization of recovery parameters
- Hermetic package
- ESD Rating: Class 3B per MIL-STD-750, Method 1020

Product Summary

V_R (per leg): 200V

• **V**_F: 1.49V

t_{rr}: 50ns
 di_{(rec)M}/dt: 530A/μs

Potential Applications

- DC-DC converter
- Motor drives

Product Validation

Qualified according to MIL-PRF-19500 for space applications



Description

HEXFRED™ diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems. An extensive characterization of the recovery behavior for different values of current, temperature and di/dt simplifies the calculations of losses in the operating conditions. The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for power converters, motor drives and other applications where switching losses are significant portion of the total losses.

Ordering Information

Table 1 Ordering options

Part number	Package	Screening Level
HFB50HC20C	TO-258AA	COTS
HFB50HC20CCSCV	TO-258AA	JANTXV-equivalent
HFB50HC20CSCS	TO-258AA	S-level

HFB50HC20C

FRED Ultrafast, Soft Recovery Diode



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Absolute Maximum Ratings

1 Absolute Maximum Ratings

Table 2 Absolute Maximum Ratings

Symbol	Parameter	Value	Unit
V_R	Cathode to anode voltage (per leg)	200	V
I _{F(AV)}	Continuous forward current, T _C = 107°C ¹	50	Α
I _{FSM}	Single pulse forward current, T _c = 25°C (per leg) ²	300	Α
$P_D @ T_C = 25^{\circ}C$	Maximum power dissipation	130	W
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to 150	°C
Wt	Weight	10.9 (Typical)	g

¹ DC = 50% rect. wave

 $^{^2}$ ½ sine wave, 60 Hz, Pulse width = 8.33 ms



Device Characteristics

2 Device Characteristics

2.1 Electrical Characteristics

Table 3 Electrical Characteristics (Per Leg) @ T_J = 25°C (unless otherwise specified)

Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Conditions
V_{BR}	Cathode Anode Breakdown Voltage	200	_	_	V	Ι _R = 100μΑ
		_	_	1.36	V	$I_F = 25A, T_J = -55^{\circ}C$
V	Forward Voltage Drop (Per Leg) See Fig. 1	_	_	1.20	V	$I_F = 25A, T_J = 25^{\circ}C$
V _F		_	_	1.49	V	$I_F = 50A, T_J = 25^{\circ}C$
		_	_	0.99	V	I _F = 25A, T _J = 125°C
	Reverse Leakage Current	_	_	10	μΑ	$V_R = V_R$ Rated
l _R	(Per Leg) See Fig. 2	_	_	1	mA	$V_R = V_R$ Rated, $T_J = 125$ °C
CJ	Junction Capacitance (Per Leg) See Fig. 3	_	_	65	pF	V _R = 200V
Ls	Series Inductance (Per Leg)	_	8.7	_	nH	Measured from anode lead to cathode lead, 6mm (0.025 in) from package

2.2 Dynamic Recovery Characteristics

Table 4 Dynamic Recovery Characteristics (Per Leg) @ T_J = 25°C (unless otherwise specified)

Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Condition	S
t _{rr1}	Reverse Recovery Time	_	46	50		T _J = 25°C	
t _{rr2}	(Per Leg) See Fig. 5	_	84	_	ns	T _J = 125°C	I _F = 50A
I _{RRM1}	Peak Recovery Current	_	5.7	_		T _J = 25°C	
I _{RRM2}	(Per Leg) See Fig. 6	_	12.5	_	A	T _J = 125°C	V _R = 160V
Q _{rr1}	Reverse Recovery Charge	_	150	_		T _J = 25°C	
Q _{rr2}	(Per Leg) See Fig. 7	_	595	_	nC	T _J = 125°C	d _{if} /dt = 200 A/ μs
$\overline{di_{(rec)M}/dt_1}$	Peak Rate of Fall of Recovery	_	530	_		T _J = 25°C	
di _{(rec)M} /dt ₂	Current During t₀ (Per Leg) See Fig. 8	_	1130	_	A/ μs	T _J = 125°C	

2.3 Thermal-Mechanical Characteristics

Table 5 Thermal-Mechanical Characteristics

Symbol	Parameter		Max.	Unit
$R_{ heta JC}$	Junction to Case, Single Leg Conducting		0.96	°C/W

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Electrical Characteristics Curves

3 Electrical Characteristics Curves

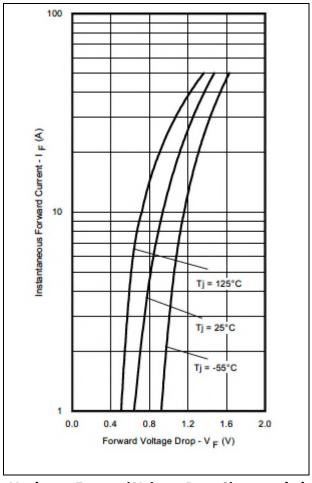


Figure 1 Maximum Forward Voltage Drop Characteristics (Per Leg)

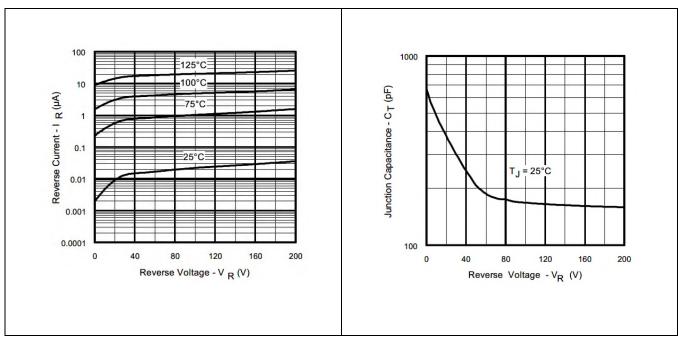


Figure 2 Typical Values of Reverse Current Vs. Reverse Voltage (Per Leg)

Figure 3

Typical Junction Capacitance Vs. Reverse Voltage (Per Leg)



Electrical Characteristics Curves

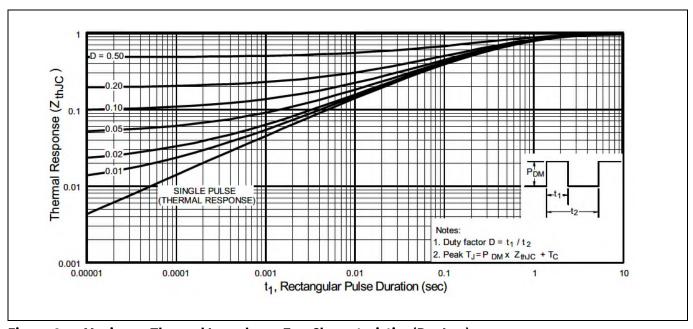


Figure 4 Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

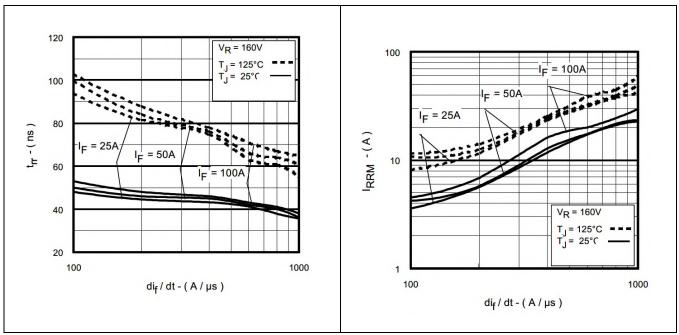


Figure 5 Typical Reverse Recovery Vs. di_f/dt (Per Leg)

Figure 6 Typical Recovery Current Vs. di_f/dt (Per Leg)



Electrical Characteristics Curves

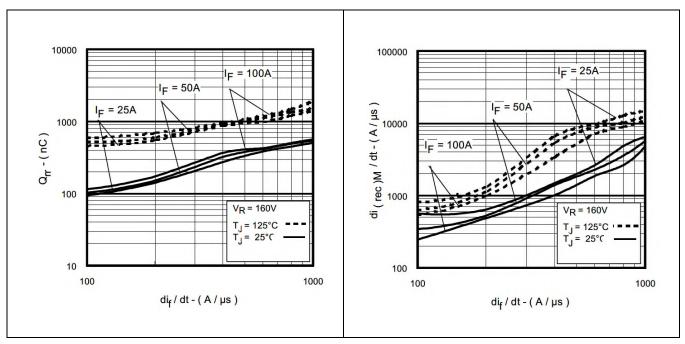


Figure 7 Typical Stored Charge Vs. di_f/dt (Per Leg)

Figure 8 Typical $di_{(rec)M}/dt$ Vs. di_f/dt (Per Leg)



Test Circuit

4 Test Circuit

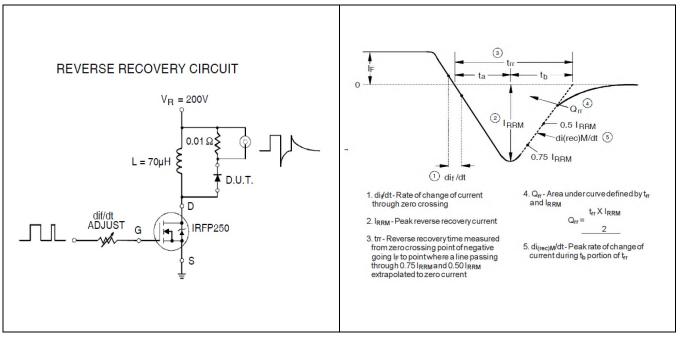


Figure 9 Reverse Recovery Parameter Test
Circuit

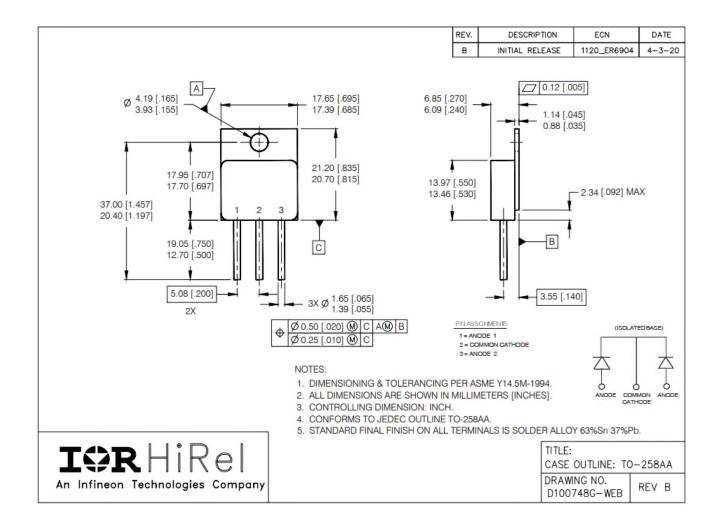
Figure 10 Reverse Recovery Waveform and Definitions



Package Outline

5 Package Outline

Note: For the most updated package outline, please see the website: TO-258AA



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Revision history

Revision history

Document version	Date of release	Description of changes			
	07/06/2001	Final datasheet (PD-94238)			
Rev A	08/02/2001	Updated per DR2			
Rev B	02/20/2006	Updated per ECN-13811			
Rev C	05/02/2023	Updated per ECN-1120-09532			

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