Power line chokes

Current-compensated ring core double chokes
250 V AC / 800 V DC, 0.5 … 47 mH, 1.6 … 10 A, +70 °C

Series/Type: B82724J8*N
Date: May 2018
Power line chokes B82724J8*N

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Rated voltage 250 V AC / 800 V DC
Rated inductance 0.5 ... 47 mH
Rated current 1.6 ... 10 A / +70 °C

Construction
- Current-compensated ring core double chokes
- Ferrite core with epoxy coating (UL 94 V-0)
- Plastic case with in-molded pins (UL 94 V-0)\(^1\)
- Full potting (UL 94 V-0, CTI600)
- Sector winding
- Insulation distances inside potting >2.5 mm

Features
- High resonance frequency due to special winding technique
- Approx. 0.5% stray inductance for symmetrical interference suppression
- Significantly increased nominal inductance and current values at high rated temperature
- Completely potted for local reduction of pollution degree (micro environment) and thermal stability
- Suitable for wave soldering
- Design complies with EN 60938-2 (VDE 0565-2)\(^2\)
- RoHS-compatible

Applications
- Suppression of common-mode interferences
- Switch-mode power applications
- High voltage applications
- Frequency converters

Terminals
- Base material CuNi18Zn290
- Layer composition Ni, Sn
- Hot-dipped
- Pins 0.7 mm x 0.7 mm
- Lead spacing 15 mm x 12.5 mm

Marking
Product brand, ordering code, graphic symbol, rated current, rated DC voltage, rated inductance, date of manufacture (YYWWDD.internal ID code), production place identification code

Delivery mode
- Blister tray in cardboard box

1) Additionally certified values:
   - Glow wire flammability index (GWFI to IEC 60695-2-12): +850 °C
   - Glow wire ignition temperature (GWIT to IEC 60695-2-13): +775 °C
   - Comparative tracking index (CTI to IEC 60112): 175 V
   - Ball pressure test (BP to IEC 60695-10-2): +125 °C

2) For AC power-line applications only.

Please read Cautions and warnings and Important notes at the end of this document.
Dimensional drawings and pin configurations

Part tolerances to ISO 2768-cL / ISO 8015.
Size ISO 14405
All dimensions in mm

Please read Cautions and warnings and Important notes at the end of this document.
**Technical data and measuring conditions**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rated voltage $V_R$</td>
<td>250 V AC (50 / 60 Hz) / 800 V DC</td>
</tr>
<tr>
<td>Test voltage $V_{test}$</td>
<td>2000 V AC, 2 s (line/line)</td>
</tr>
<tr>
<td>Rated temperature $T_R$</td>
<td>+70 °C</td>
</tr>
<tr>
<td>Rated current $I_R$</td>
<td>Referred to 50 Hz and rated temperature</td>
</tr>
<tr>
<td>Rated inductance $L_R$</td>
<td>Measured with Agilent 4284A at 0.1 mA, +20 °C</td>
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<tr>
<td>Inductance tolerance</td>
<td>±30% at +20 °C</td>
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<tr>
<td>Inductance decrease $\Delta L/L_0$</td>
<td>&lt; 10% at DC magnetic bias with $I_R$, +20 °C</td>
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<tr>
<td>Stray inductance $L_{stray,typ}$</td>
<td>Measured with Agilent 4284A at 5 mA, +20 °C, typical values</td>
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<tr>
<td>DC resistance $R_{typ}$</td>
<td>Measured at +20 °C, typical values, specified per winding</td>
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<tr>
<td>Solderability (lead-free)</td>
<td>Sn96.5Ag3.0Cu0.5: +(245 ± 5) °C, (3 ± 0.3) s</td>
</tr>
<tr>
<td>Resistance to soldering heat</td>
<td>+(260 ± 5) °C, (10 ± 1) s</td>
</tr>
<tr>
<td>(wave soldering)</td>
<td>(to IEC 60068-2-20, test Ta)</td>
</tr>
<tr>
<td>Climatic category</td>
<td>40/125/56 (to IEC 60068-1)</td>
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<tr>
<td>Storage conditions (packaged)</td>
<td>−25 °C … +40 °C, ≤ 75% RH</td>
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<tr>
<td>Weight</td>
<td>Approx. 35 g</td>
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</table>
# Current-compensated ring core double chokes

## Characteristics and ordering codes

<table>
<thead>
<tr>
<th>I&lt;sub&gt;R&lt;/sub&gt; A</th>
<th>L&lt;sub&gt;R&lt;/sub&gt; mH</th>
<th>L&lt;sub&gt;stray,typ&lt;/sub&gt; μH</th>
<th>R&lt;sub&gt;typ&lt;/sub&gt; mΩ</th>
<th>Ordering code</th>
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<tbody>
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<td>6.7</td>
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<td>8.0</td>
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<td>7.4</td>
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<td>5.0</td>
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<tr>
<td>10.0</td>
<td>0.5</td>
<td>3.0</td>
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<td>B82724J8103N040</td>
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</table>

Please read **Cautions and warnings** and **Important notes** at the end of this document.
Power line chokes

Current-compensated ring core double chokes

Impedance $|Z|$ versus frequency $f$
measured with windings in parallel at $+20$ °C,
typical values

Current derating $I_{op}/I_R$
versus ambient temperature $T_A$
rated temperature $T_R=+70$ °C

Please read Cautions and warnings and
Important notes at the end of this document.
Cautions and warnings

- Please note the recommendations in our Inductors data book (latest edition) and in the data sheets.
  - Particular attention should be paid to the derating curves given there.
  - The soldering conditions should also be observed. Temperatures quoted in relation to wave soldering refer to the pin, not the housing.

- If the components are to be washed varnished it is necessary to check whether the washing varnish agent that is used has a negative effect on the wire insulation, any plastics that are used, or on glued joints. In particular, it is possible for washing varnish agent residues to have a negative effect in the long-term on wire insulation.

Washing processes may damage the product due to the possible static or cyclic mechanical loads (e.g. ultrasonic cleaning). They may cause cracks to develop on the product and its parts, which might lead to reduced reliability or lifetime.

- The following points must be observed if the components are potted in customer applications:
  - Many potting materials shrink as they harden. They therefore exert a pressure on the plastic housing or core. This pressure can have a deleterious effect on electrical properties, and in extreme cases can damage the core or plastic housing mechanically.
  - It is necessary to check whether the potting material used attacks or destroys the wire insulation, plastics or glue.
  - The effect of the potting material can change the high-frequency behaviour of the components.

- Ferrites are sensitive to direct impact. This can cause the core material to flake, or lead to breakage of the core.

- Even for customer-specific products, conclusive validation of the component in the circuit can only be carried out by the customer.

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