

RF360 Europe GmbH

A Qualcomm – TDK Joint Venture

SAW components

SAW 2in1 filter

Series/type:	B4236
Ordering code:	B39811B4236H410
Date:	August 17, 2016
Version:	2.2

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SAW 2in1 filter	769.0 / 809.5

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1 Application

- Low-loss 2in1 RF filter for Trunked Radio
- Device with two integrated Rx filters
- Low amplitude ripple
- Usable pass band 31 & 14 MHz
- No matching required for operation at 50Ω

2 Features

- Package code QCC8E
- Package size $3.0 \pm 0.08 \text{ mm} \times 2.5 \pm 0.08 \text{ mm}$
- Package height $0.98 \pm 0.115 \text{ mm}$
- Approximate weight 0.04 g
- RoHS compatible
- Package for Surface Mount Technology (SMT)
- Lead free soldering compatible with J-STD20C
- Electrostatic Sensitive Device (ESD)
- Moisture Sensitivity Level 1 (MSL1)

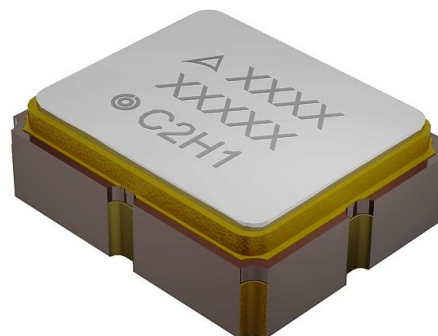


Figure 1: Picture of component with example of product marking.

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5 Matching circuit

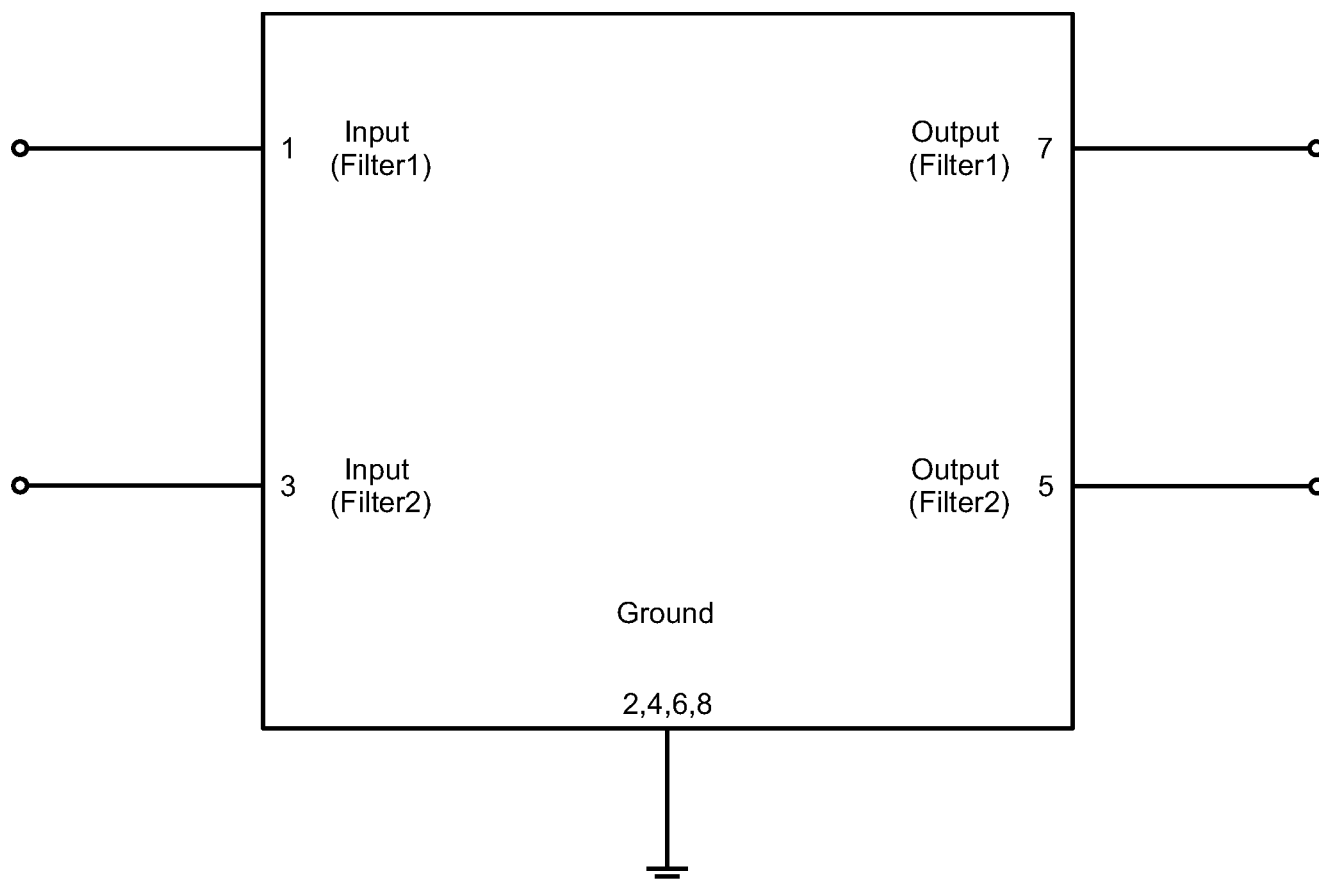


Figure 3: Schematic of matching circuit. No external matching components required.

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6 Characteristics Filter1

Temperature range for specification	$T_{SPEC} = -30\text{ °C} \dots +85\text{ °C}$
Filter1 input terminating impedance	$Z_{Filter1\ IN} = 50\ \Omega$
Filter1 output terminating impedance	$Z_{Filter1\ OUT} = 50\ \Omega$

Characteristics Filter1			min. for T_{SPEC}	typ. @+25 °C	max. for T_{SPEC}	
Center frequency		f_C	—	809.5	—	MHz
Maximum insertion attenuation		α_{max}	—	2.3	3.3 ¹⁾	dB
	794... 825	MHz	—	2.3	3.3 ¹⁾	dB
Amplitude ripple (p-p)		$\Delta\alpha$	—	0.9	1.9 ²⁾	dB
	794... 825	MHz	—	0.9	1.9 ²⁾	dB
Maximum VSWR		$VSWR_{max}$	—	2.1	2.4	
@ Filter1 input port	794... 825	MHz	—	2.1	2.4	
@ Filter1 output port	794... 825	MHz	—	2.1	2.4	
Minimum attenuation		α_{min}				
	10... 645	MHz	40	62	—	dB
	674... 735	MHz	30	56	—	dB
	735... 777	MHz	20	28	—	dB
	851... 884	MHz	20	28	—	dB
	884... 945	MHz	30	56	—	dB
	974... 1065	MHz	40	54	—	dB
	1065... 1564.5	MHz	20	42	—	dB
	1564.5... 1594.5	MHz	30	43	—	dB
	2326.5... 2371.5	MHz	36	41	—	dB
Group delay ripple		$\Delta\tau_{var}$	—	27	75 ³⁾	ns
	794... 825	MHz	—	27	75 ³⁾	ns

¹⁾ 2.8 dB at 25±2°C.

²⁾ 1.4 dB at 25±2°C.

³⁾ 50 ns at 25±2°C.

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7 Characteristics Filter2

Temperature range for specification	$T_{SPEC} = -30\text{ °C} \dots +85\text{ °C}$
Filter2 input terminating impedance	$Z_{Filter2\ IN} = 50\ \Omega$
Filter2 output terminating impedance	$Z_{Filter2\ OUT} = 50\ \Omega$

Characteristics Filter2			min. for T_{SPEC}	typ. @+25 °C	max. for T_{SPEC}	
Center frequency		f_C	—	769	—	MHz
Maximum insertion attenuation		α_{max}	—	1.7	2.6 ¹⁾	dB
	762... 776	MHz	—	1.7	2.6 ¹⁾	dB
Amplitude ripple (p-p)		$\Delta\alpha$	—	0.4	1.0	dB
	762... 776	MHz	—	0.4	1.0	dB
Maximum VSWR		$VSWR_{max}$	—	1.5	1.7	
@ Filter2 input port	762... 776	MHz	—	1.5	1.7	
@ Filter2 output port	762... 776	MHz	—	1.5	1.7	
Minimum attenuation		α_{min}				
	10... 431	MHz	57	60	—	dB
	431... 604	MHz	50	60	—	dB
	604... 690	MHz	30	58	—	dB
	690... 733	MHz	20	52	—	dB
	733... 752	MHz	9	22	—	dB
	804... 847	MHz	25	36	—	dB
	847... 892.7	MHz	30	52	—	dB
	892.7... 910.7	MHz	50	56	—	dB
	910.7... 995.3	MHz	47	54	—	dB
	995.3... 1121	MHz	42	52	—	dB
	1524... 1554	MHz	30	42	—	dB
	2286... 2331	MHz	30	39	—	dB
Group delay ripple		$\Delta\tau_{var}$	—	22	50	ns
	762... 776	MHz	—	22	50	ns

¹⁾ 2.4 dB at 25±2°C.

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8 Maximum ratings

Operable temperature	$T_{OP} = -40\text{ °C} \dots +125\text{ °C}$	
Storage temperature	$T_{STG}^{1)} = -40\text{ °C} \dots +125\text{ °C}$	
DC voltage	$ V_{DC} = 5.0\text{ V (max.)}$	
ESD voltage	$V_{ESD}^{2)} = 100\text{ V (max.)}$	Machine model.
Input power	P_{IN}	
@ Filter1 input port: 794 ... 825 MHz	15 dBm (max.)	Source and load impedance 50 Ω
@ Filter2 input port: 762 ... 776 MHz	15 dBm (max.)	Source and load impedance 50 Ω

¹⁾ Not valid for packaging material. Storage temperature for packaging material is -25 °C to $+40\text{ °C}$.

²⁾ According to JESD22-A115B (MM – Machine Model), 10 negative & 10 positive pulses.

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9 Transmission coefficient Filter1

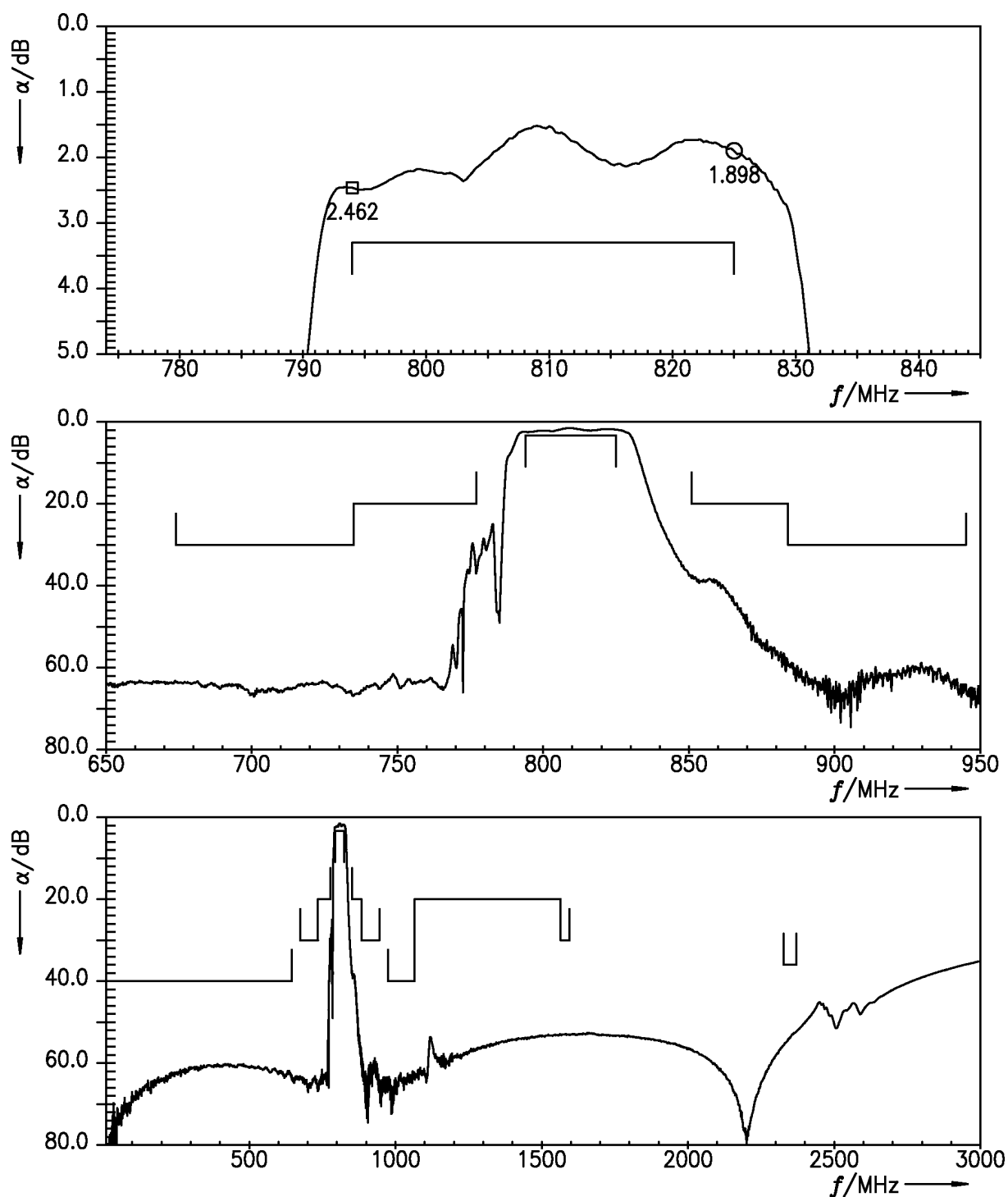


Figure 4: Attenuation Filter1.

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10 Reflection coefficients Filter1

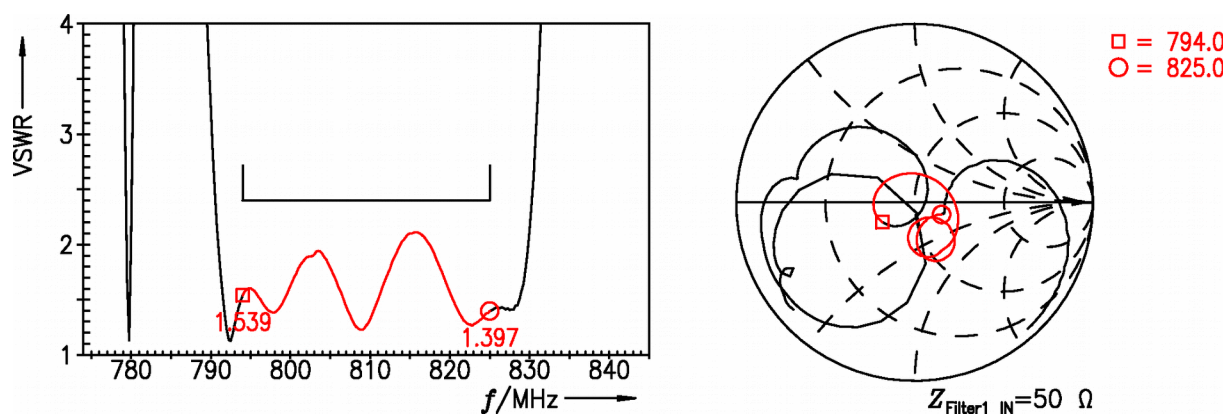


Figure 5: Reflection coefficient Filter1 at IN port.

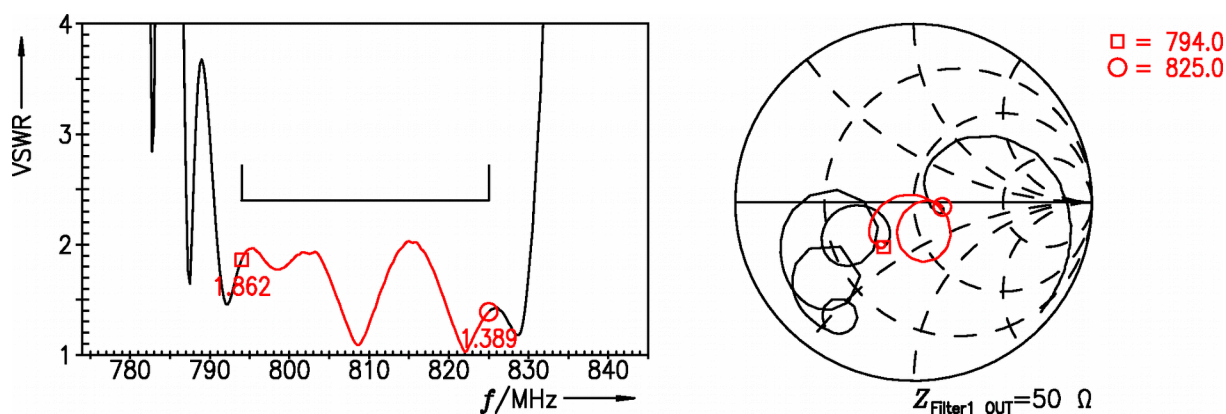


Figure 6: Reflection coefficient Filter1 at OUT port.

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11 Transmission coefficient Filter2

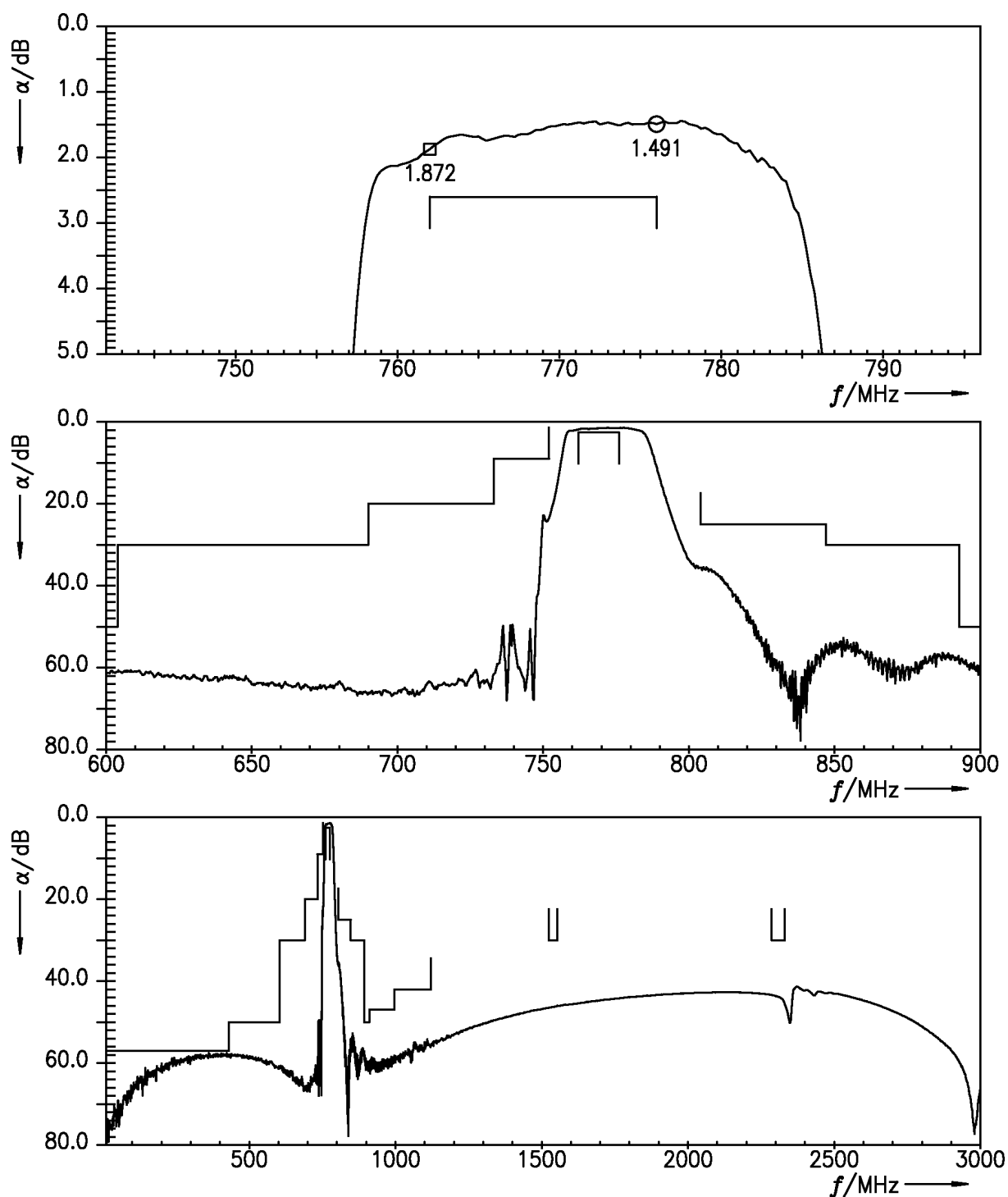


Figure 7: Attenuation Filter2.

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12 Reflection coefficients Filter2

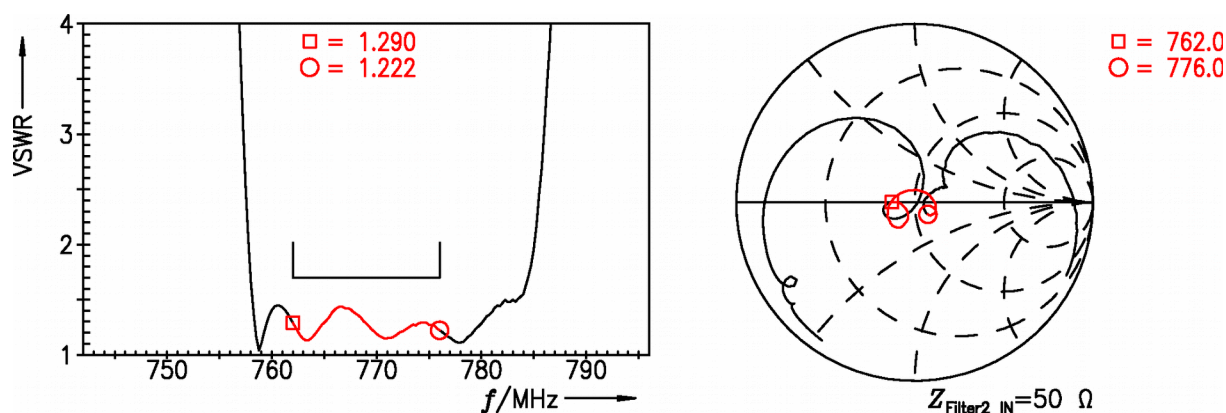


Figure 8: Reflection coefficient Filter2 at IN port.

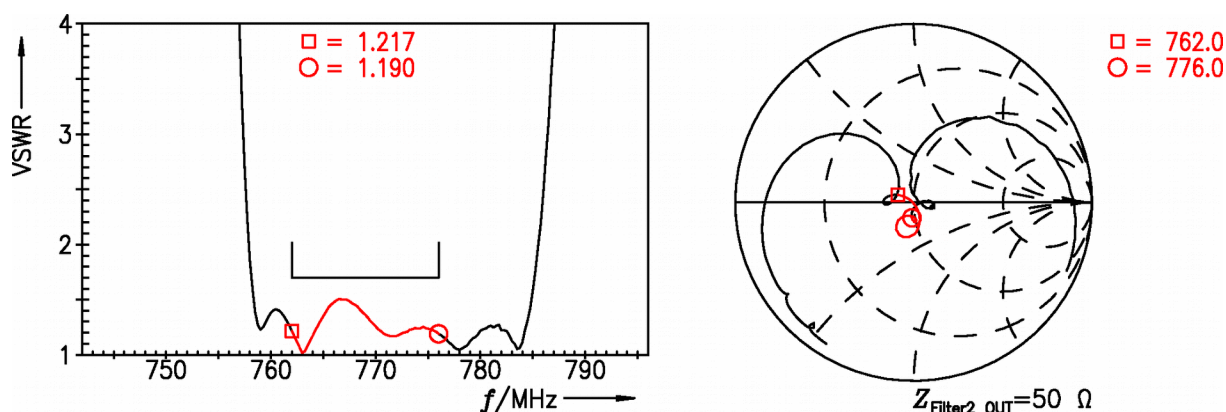


Figure 9: Reflection coefficient Filter2 at OUT port.

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13 Packing material

13.1 Tape

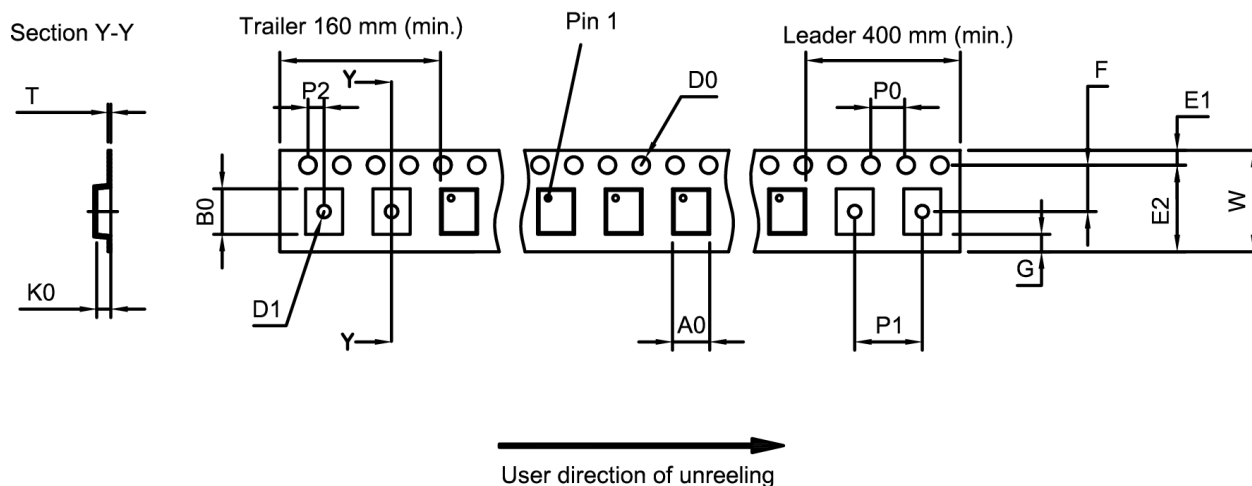


Figure 10: Drawing of tape (first-angle projection) with tape dimensions according to Table 1.

A ₀	2.85±0.1 mm
B ₀	3.3±0.1 mm
D ₀	1.5+0.1/-0 mm
D ₁	1.5 mm (min.)
E ₁	1.75±0.1 mm

E ₂	10.25 mm (min.)
F	5.5±0.05 mm
G	0.75 mm (min.)
K ₀	1.3±0.1 mm
P ₀	4.0±0.1 mm

P ₁	4.0±0.1 mm
P ₂	2.0±0.1 mm
T	0.3±0.05 mm
W	12.0+0.3/-0.1 mm

Table 1: Tape dimensions.

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13.2 Reel with diameter of 180 mm

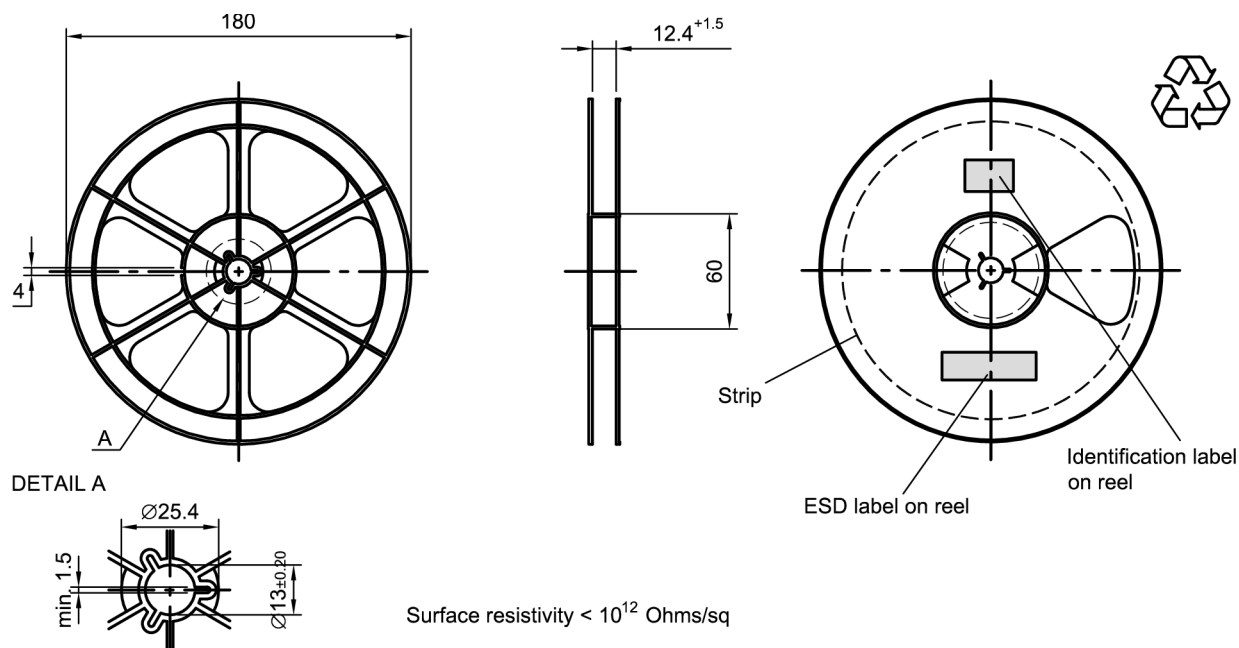


Figure 11: Drawing of reel (first-angle projection) with diameter of 180 mm.

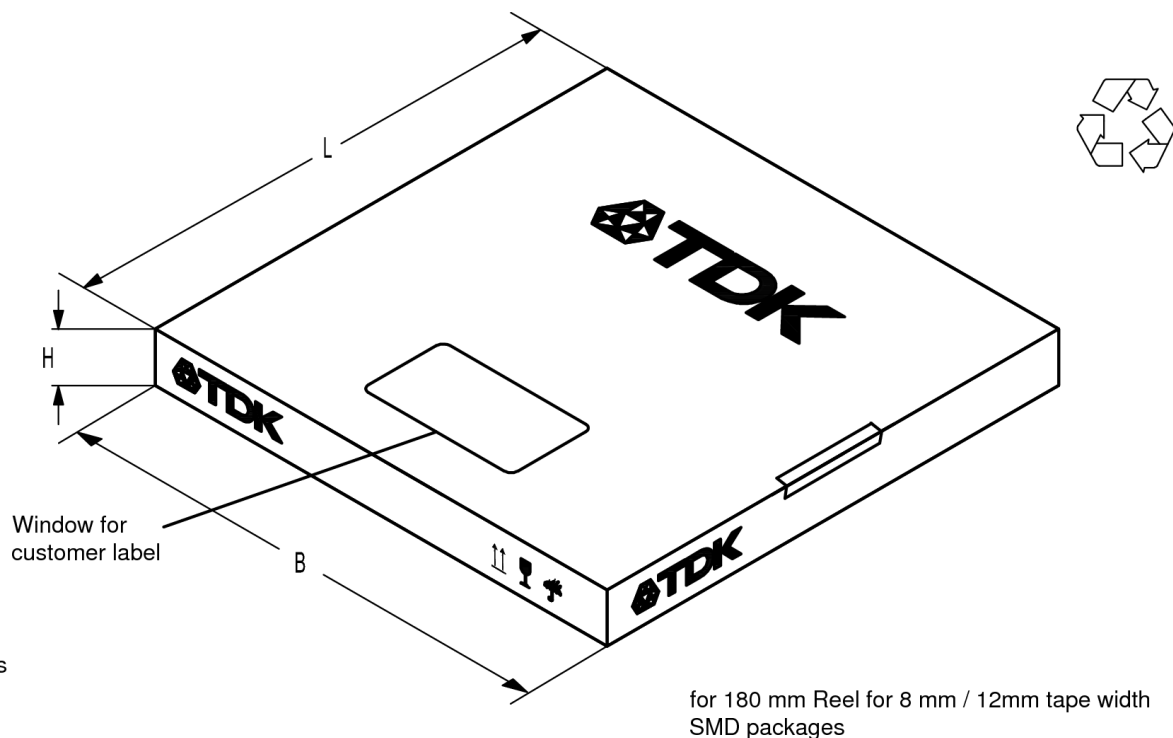


Figure 12: Drawing of folding box for reel with diameter of 180 mm.

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13.3 Reel with diameter of 330 mm

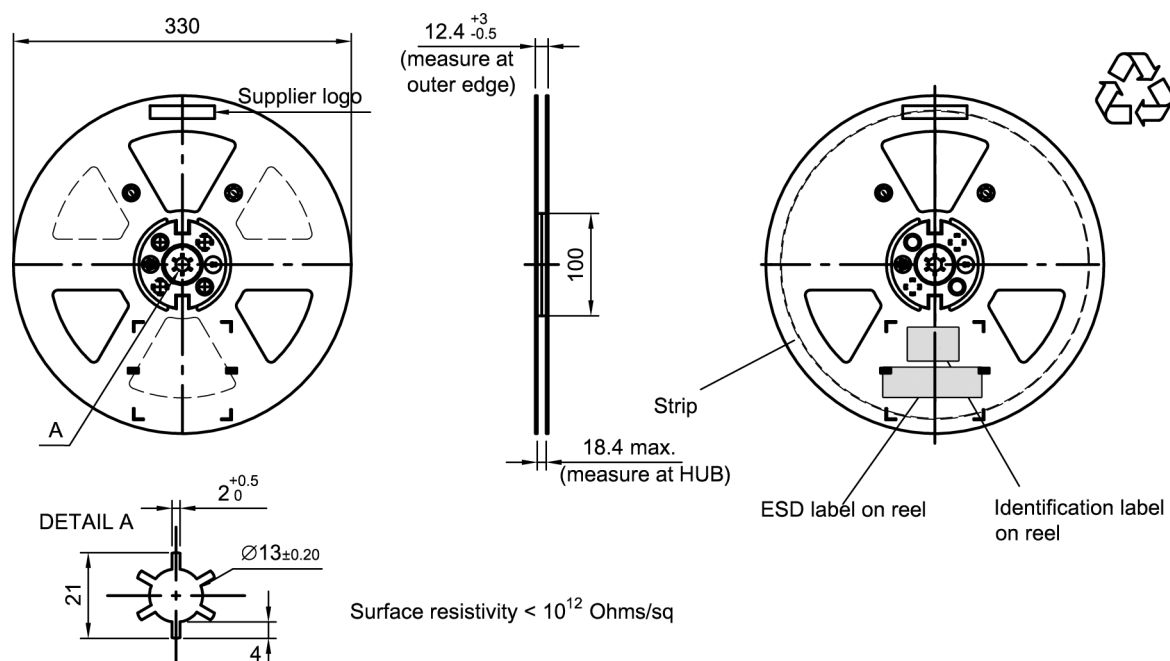
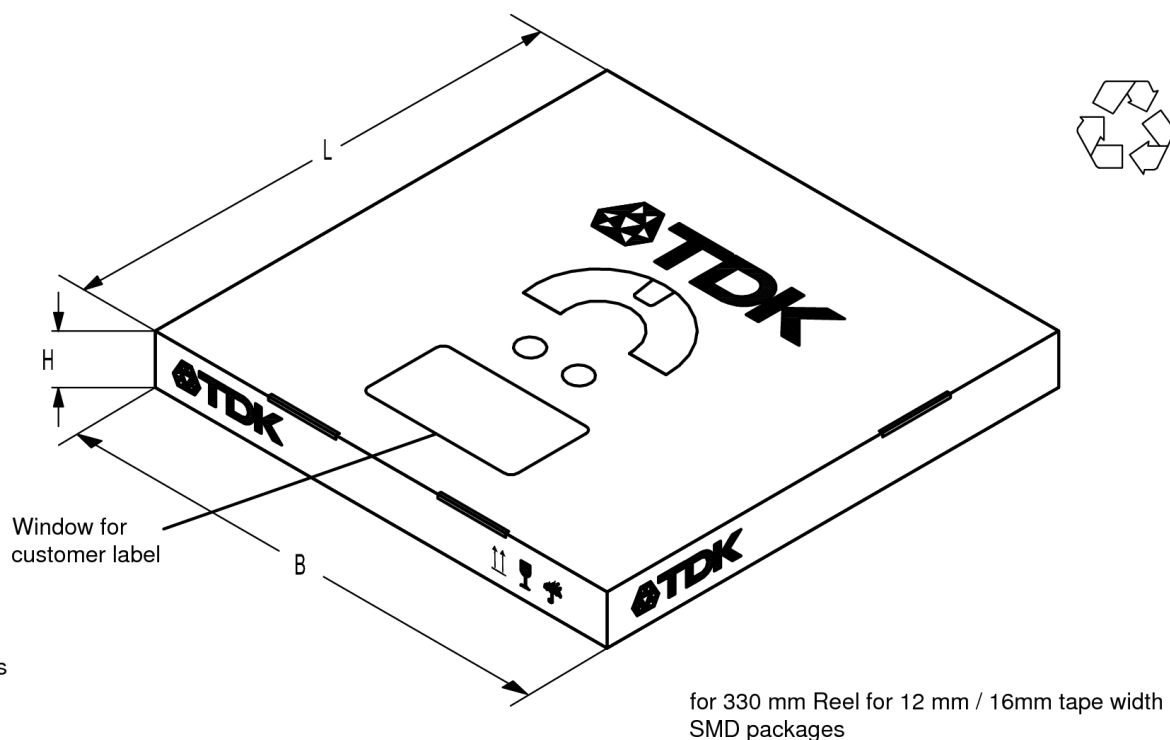


Figure 13: Drawing of reel (first-angle projection) with diameter of 330 mm.



Dimensions

L = 340
B = 340
H = 25

Figure 14: Drawing of folding box for reel with diameter of 330 mm.

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14 Marking

Products are marked with device designation, lot number, as well as production location and date code.

- Device designation: The 4-character device designation of the ordering code is used for the marking.

Example for 4-character device designation: B3xxxxB1234xxxx

- Lot number: The last 5 digits of the lot number are used for the marking.

Example: 12345

- Production location and date code: The production location is Wuxi (encoded in the first character 'C'). The production date code is encoded in the last three characters according to Table 2.

1 st digit (day)						2 nd digit (year)				3 rd digit (month)			
Day	Code	Day	Code	Day	Code	Year	Code	Year	Code	Month	Code	Month	Code
1	1	11	A	21	M	2010	A	2022	P	Jan	1	Jul	7
2	2	12	B	22	N	2011	B	2023	R	Feb	2	Aug	8
3	3	13	C	23	P	2012	C	2024	S	Mar	3	Sep	9
4	4	14	D	24	R	2013	D	2025	T	Apr	4	Oct	0
5	5	15	E	25	S	2014	E	2026	U	May	5	Nov	N
6	6	16	F	26	T	2015	F	2027	V	Jun	6	Dec	D
7	7	17	H	27	U	2016	H	2028	W				
8	8	18	J	28	V	2017	J	2029	X				
9	9	19	K	29	W	2018	K	2030	Z				
10	0	20	L	30	X	2019	L	2031	A				
				31	Z	2020	M	2032	B				
						2021	N	and so on					

Table 2: Production date code.

Example of how to decode production location and date code:

Code: **CTF6**

Location: C → Wuxi

Day: T → 26th

Year: F → 2015

Month: 6 → June

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15 Soldering profile

The recommended soldering process is in accordance with IEC 60068-2-58 – 3rd edit and IPC/JEDEC J-STD-020B.

ramp rate	≤ 3 K/s
preheat	125 °C to 220 °C, 150 s to 210 s, 0.4 K/s to 1.0 K/s
$T > 220\text{ °C}$	30 s to 70 s
$T > 230\text{ °C}$	min. 10 s
$T > 245\text{ °C}$	max. 20 s
$T \geq 255\text{ °C}$	–
peak temperature T_{peak}	250 °C +0/-5 °C
wetting temperature T_{min}	230 °C +5/-0 °C for 10 s ± 1 s
cooling rate	≤ 3 K/s
soldering temperature T	measured at solder pads

Table 3: Characteristics of recommended soldering profile for lead-free solder (Sn95.5Ag3.8Cu0.7).

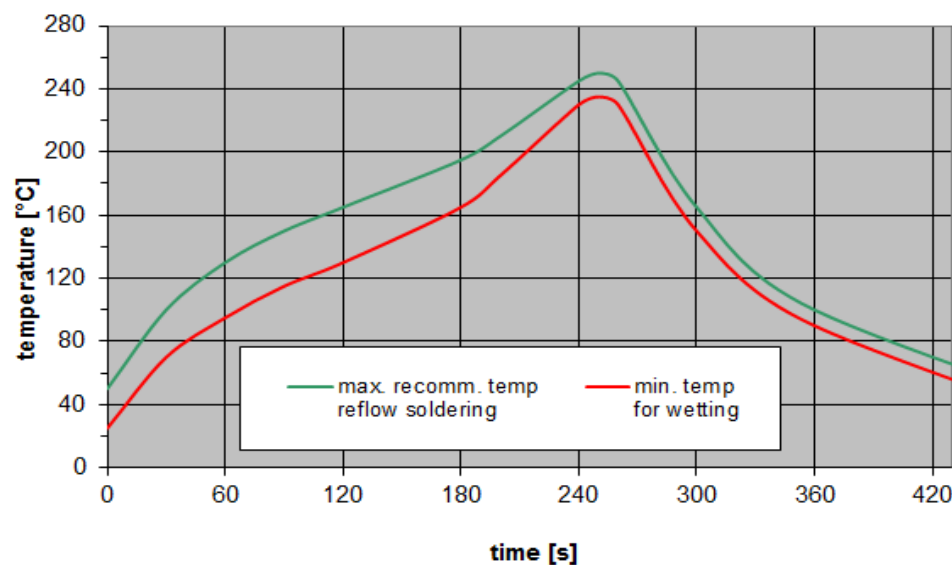


Figure 15: Recommended reflow profile for convection and infrared soldering – lead-free solder.

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16 Annotations

16.1 Matching coils

See TDK inductor pdf-catalog <http://www.tdk.co.jp/tefe02/coil.htm#aname1> and Data Library for circuit simulation <http://www.tdk.co.jp/etvcl/index.htm>.

16.2 RoHS compatibility

ROHS-compatible means that products are compatible with the requirements according to Art. 4 (substance restrictions) of Directive 2011/65/EU of the European Parliament and of the Council of June 8th, 2011, on the restriction of the use of certain hazardous substances in electrical and electronic equipment ("Directive") with due regard to the application of exemptions as per Annex III of the Directive in certain cases.

16.3 Scattering parameters (S-parameters)

The pin/port assignment is available in the headers of the S-parameter files. Please contact your local EPCOS sales office.

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17 Cautions and warnings

17.1 Display of ordering codes for EPCOS products

The ordering code for one and the same product can be represented differently in data sheets, data books, other publications and the website of EPCOS, or in order-related documents such as shipping notes, order confirmations and product labels. The varying representations of the ordering codes are due to different processes employed and do not affect the specifications of the respective products. Detailed information can be found on the Internet under www.epcos.com/orderingcodes.

17.2 Material information

Due to technical requirements components may contain dangerous substances. For information on the type in question please also contact one of our sales offices.

For information on recycling of tapes and reels please contact one of our sales offices.

17.3 Moldability

Before using in overmolding environment, please contact your local EPCOS sales office.

17.4 Package information

Landing area

The printed circuit board (PCB) land pattern (landing area) shown is based on EPCOS internal development and empirical data and illustrated for example purposes, only. As customers' SMD assembly processes may have a plenty of variants and influence factors which are not under control or knowledge of EPCOS, additional careful process development on customer side is necessary and strongly recommended in order to achieve best soldering results tailored to the particular customer needs.

Dimensions

Unless otherwise specified all dimensions are understood using unit millimeter (mm).

Projection method

Unless otherwise specified first-angle projection is applied.

Important notes

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3. **The warnings, cautions and product-specific notes must be observed.**
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