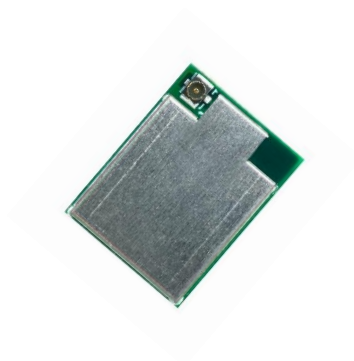




PIXI-9377

Qualcomm QCA9377

802.11ac/b/g/n 1T1R + Bluetooth 5

M.2 LGA Type 1216 Module

REV. A1 – VER. 1.03

October 22, 2019

REVISION HISTORY

Revision	Date	Originator	Notes
1.00	April 5, 2018	TechNexion	Initial Public release
1.01	January 6, 2019	TechNexion	Add 1.8V mode details
1.02	March 29, 2019	TechNexion	Add Bluetooth 5
1.03	October 22, 2019	TechNexion	PCIe signal clarification

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1. Introduction

1.1 General Introduction

PIXI-9377 is a high performance 2.4 + 5GHz WLAN and Bluetooth combo module based on latest-generation silicon (Qualcomm Atheros QCA9377). With an industrial temperature rating, broad country certifications, and the availability in different package styles, the PIXI-9377 provides significant flexibility to meet various end user application needs.

The on-module chip antenna package style for the PIXI-9377 eliminates complexity for design integration, simplifies manufacturing assembly with larger pin outs, and features an advanced chip antenna that offers greater resistance to de-tuning than typical trace or chip antennas.

The module includes the MAC, Baseband and Radio to support WLAN applications and an independent, high-speed UART is provided for the Bluetooth host interface. In addition, the latest Linux and Android drivers are supported directly by TechNexion and Qualcomm Atheros.

- Need to get to market quickly?
- Not an expert in 802.11 wireless connectivity?
- Need a custom antenna?
- Would you like a custom design?
- Not quite sure what you need?
- Do you need help with your host board?

TechNexion has a Design Services Team standby that is happy to develop custom hardware or software or assist with integrating the design.

Contact your TechNexion Sales Contact Window or email us at sales@technexion.com.

1.2 Product Key Features

General	Specification
Chipset	Qualcomm QCA-9377
Formfactor	M.2 LGA Type 1216
Antenna	IPEX MHF-4 Connector
Operation Temperature	-40 to +85 Degrees Celsius
Operation Humidity	10 – 90 % Non-Condensing

Wi-Fi	Specification	
Chipset	Qualcomm QCA-9377	
Host Interface	PIXI-9377-S	SDIO
	PXI-9377-P	PCIE
Standards	802.11a/b/g/n/ac	
Radio Stream	1T1R	
Frequency Range	2.4Ghz	2412 – 2484Mhz
	5Ghz	4900 – 5925Mhz
Frequency Step	2.4GHz 802.11b/g/n	5Mhz
	5Ghz 802.11a/n/ac	20Mhz
	5Ghz 802.11 n/ac	40Mhz
	5Ghz 802.11ac	80Mhz
Link Data Rate	802.11b	1,2, 5.5, 11 Mbps
	802.11a/g	6,9,12,18,244,36,48,54 Mbps
	802.11n	MCS 0,1,2,3,4,5,6,7
	802.11ac	MCS 0,1,2,3,4,5,6,7,8,9
Modulation Type	802.11b	DSSS (DBPSK, DQPSK, CCK)
	802.11a/g/n	OFDM (BPSK, QPSK, 16QAM, 64QAM)
	802.11ac	OFDM (BPSK, QPSK, 16QAM, 256QAM)
Encryption	RC4	128 bits
	AES	128 bits

Bluetooth	Specification	
Chipset	Qualcomm QCA-9377	
Host Interface	PIXI-9377-S	UART
	PXI-9377-P	USB
Standards	Bluetooth 5 (backwards compatible with 4.2, 4.1, 4.0 LE, 3.0+HS, 2.1+EDR)	
Frequency Range	2402 – 2480Mhz	
Channels	0 – 79	
Link Data Rate	1 Mbps, 2Mbps and Upto 3Mbps EDR	
Modulation Type	FHSS, GFSK, DPSK, DQPSK	

1.3 Product Variants

The PIXI-9377 Base Module is a System in Package (SIP) module, which can be assembled into an OEM end product and is available in the following configurations.

	Partnumber	Description
	PIXI-9377-S	PIXI QCA9377 M.2 SIP Module Wi-Fi : SDIO BT : UART + PCM
	PIXI-9377-P	PIXI QCA9377 M.2 SIP Module Wi-Fi : PCIe BT : USB

1.4 Add-On Card Variants

TechNexion offers a range of “add-on cards” for customers that want to add the PIXI-9377 in their OEM project but don’t have the freedom or technical design or manufacturing knowledge to work with SIP packages

	Partnumber	Description
	CLIX-9377	CLIX QCA9377 Module Wi-Fi : SDIO BT : UART + PCM
	STIX-M2-9377-AE	M.2 Key AE QCA9377 Module Wi-Fi : PCIe BT : USB
	STIX-M2-9377-B	M.2 Key B QCA9377 Module Wi-Fi : PCIe BT : USB
	STIX-MPCIE-9377	Mini-PCle QCA9377 Module Wi-Fi : PCIe BT : USB

1.5 Block Diagrams

Figure 1 – PIXI-9377 Block Diagram

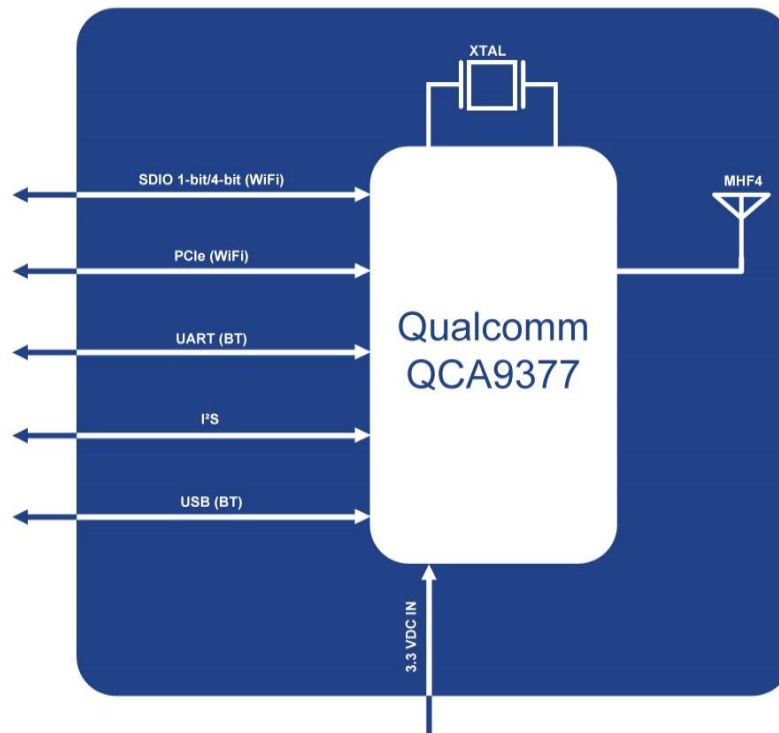


Figure 2 – CLIX-9377 Block Diagram

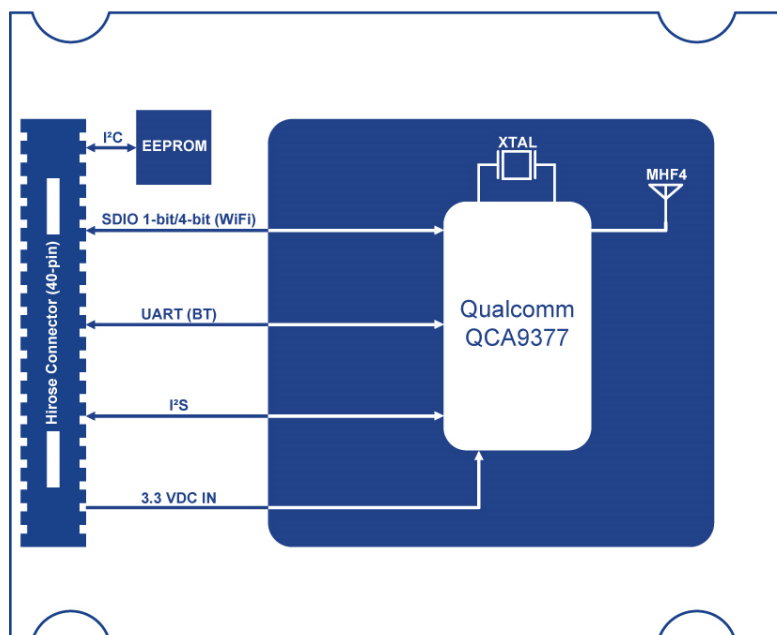


Figure 3 – STIX-M2-9377AE and STIX-M2-9377B Block Diagram

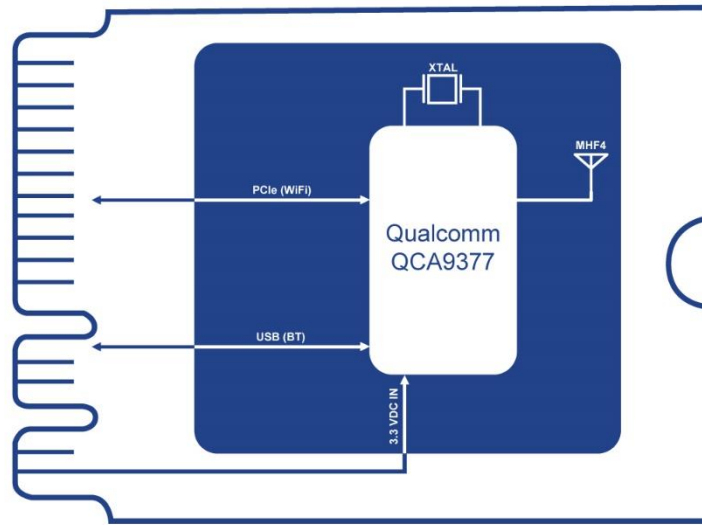
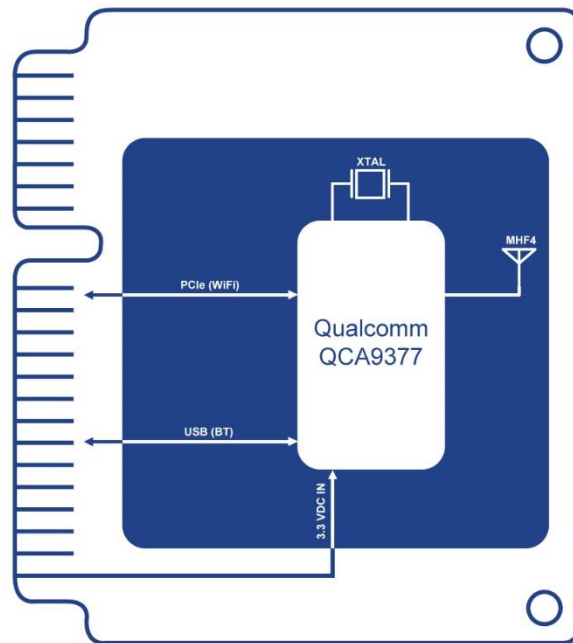
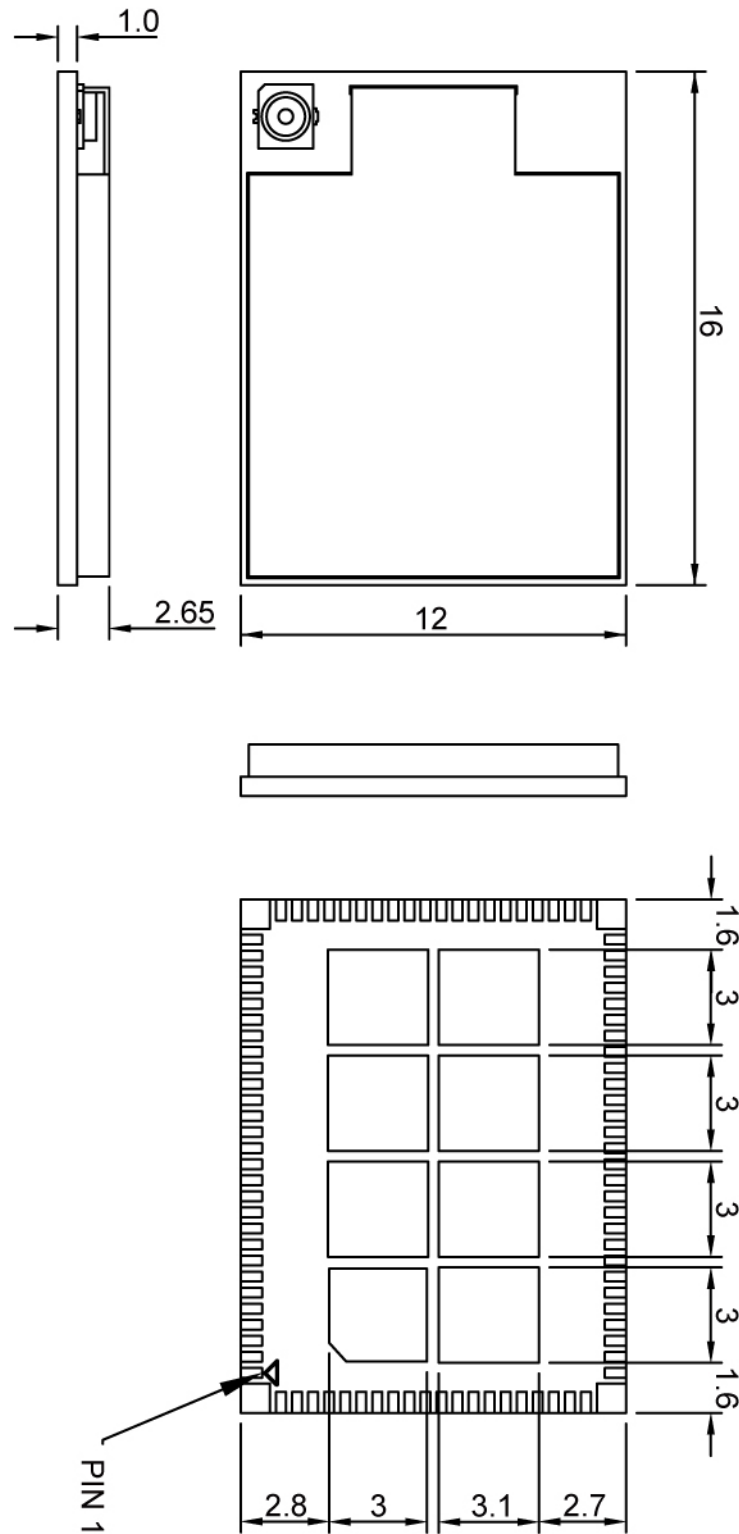


Figure 4 – STIX-MPCIE-9377 Block Diagram



1.6 Dimensional Drawing PIXI-9377

Figure 5 – PIXI-9377 Dimensional Drawing



2. PIXI-9377 Signal Pinout

Pin	Name	Type	Description
1	NC		Not Connected
2	NC		Not Connected
3	NC		Not Connected
4	VDD_3P3	P	3.3V
5	VDD_3P3	P	3.3V
6	GND	P	Ground
7	NC		Not Connected
8	NC		Not Connected
9	NC		Not Connected
10	NC		Not Connected
11	NC		Not Connected
12	NC		Not Connected
13	NC		Not Connected
14	NC		Not Connected
15	NC		Not Connected
16	NC		Not Connected
17	GND	P	Ground
18	NC		Not Connected
19	NC		Not Connected
20	GND	P	Ground
21	VENDOR ID		TechNexion Vendor Manufacturing Pin
22	VENDOR ID		TechNexion Vendor Manufacturing Pin
23	GND	P	Ground
24	VENDOR ID		TechNexion Vendor Manufacturing Pin
25	RSVD		Reserved
26	GND	P	Ground
27	NC		Not Connected
28	WL_REG_ON	I	WLAN Enable. (Active High)
29	Pewake#	I	PCI Express Wake Up Signal
30	Clxreq	I	PCI Express Clock Request Signal
31	PCIE_RST	I	PCI Express Reset Signal
32	GND	P	Ground
33	PCIE_RCLK-	I	PCI Express Clock differential pair signal
34	PCIE_RCLK+	I	
35	GND	P	Ground
36	PCIE_TX-	O	PCI Express Transmit differential pair signal
37	PCIE_TX+	O	
38	GND	P	Ground
39	PCIE_RX-	I	PCI Express Receive differential pair signal
40	PCIE_RX+	I	
41	GND	P	Ground
42	NC		Not Connected
43	NC		Not Connected
44	NC		Not Connected
45	NC		Not Connected
46	SDIO_WAKE_n	O	SDIO Interrupt Signal
47	SDIO_DATA3	I/O	SDIO Data bit 3
48	SDIO_DATA2	I/O	SDIO Data bit 2
49	SDIO_DATA1	I/O	SDIO Data bit 1
50	SDIO_DATA0	I/O	SDIO Data bit 0
51	SDIO_CMD	I	SDIO CMD Line Signal
52	SDIO_CLK	OD	SDIO Clock Signal
53	BT_WAKE_n	I	Bluetooth Host Wake Up Signal (Active High)

Pin	Name	Type	Description
54	UART_CTS	I	UART Clear to Send Signal
55	UART_TX	O	UART Transmit Signal
56	UART_RX	I	UART Receive Signal
57	UART_RTS	O	UART Ready to Send Signal
58	PCM_SYNC	I/O	Bluetooth PCM SYNC Signal
59	PCM_IN	OD	Bluetooth PCM IN Signal
60	PCM_OUT	O	Bluetooth PCM OUT Signal
61	PCM_CLK	I	Bluetooth PCM Clock Signal
62	GND	P	Ground
63	BT_REG_ON	I	Bluetooth enable. Active High
64	NC		Not Connected
65	WL_LED_n	O	Wi-Fi LED Signal
66	NC		Not Connected
67	NC		Not Connected
68	GND	P	Ground
69	USB_DN	I/O	Universal Serial Bus differential pair signal
70	USB_DP	I/O	
71	GND	P	Ground
72	VDD_SDIO	P	SDIO Voltage (1.8V or 3.3V)
73	VDD_IO	P	I/O Voltage (1.8V or 3.3V)
74	GND	P	Ground
75	GND	P	Ground
76	GND	P	Ground
77	GND	P	Ground
78	GND	P	Ground
79	NC		Not Connected
80	GND	P	Ground
81	GND	P	Ground
82	GND	P	Ground
83	GND	P	Ground
84	NC		Not Connected
85	GND	P	Ground
86	GND	P	Ground
87	GND	P	Ground
88	GND	P	Ground
89	NC		Not Connected
90	GND	P	Ground
91	GND	P	Ground
92	GND	P	Ground
93	GND	P	Ground
94	NC		Not Connected
95	GND	P	Ground
96	GND	P	Ground
G1	GND	P	Ground
G2	GND	P	Ground
G3	GND	P	Ground
G4	GND	P	Ground
G5	GND	P	Ground
G6	GND	P	Ground
G7	GND	P	Ground
G8	GND	P	Ground
G9	GND	P	Ground
G10	GND	P	Ground
G11	GND	P	Ground
G12	GND	P	Ground

3. RF Characteristics

Table 1 - RX Sensitivity 2.4 Ghz (TA = 25°C VCC = 3.3V)

Parameter	Test condition	Min.	Typ	Max	Unit
802.11b / 20MHz	11Mbps		-88		dBm
802.11g / 20MHz	54Mbps		-72		dBm
802.11n (MCS7_HT20)	65Mbps		-65		dBm
802.11n (MCS7_HT40)	135Mbps		-65		dBm

Table 2 - RX Sensitivity 5 Ghz (TA = 25°C VCC = 3.3V)

Parameter	Test condition	Min.	Typ	Max	Unit
802.11a / 20MHz	54Mbps		-70		dBm
802.11n (MCS7_HT20)	65Mbps		-65		dBm
802.11n (MCS7_HT40)	135Mbps		-65		dBm
802.11ac (MCS9_VHT40)	180Mbps		-65		dBm
802.11ac (MCS9_VHT80)	390Mbps		-51		dBm

Table 3 - TX Output Power 2.4 GHz (TA = 25°C VCC = 3.3V)

Parameter	Test condition	Min.	Typ	Max	Unit
802.11b / 20MHz	11Mbps		19		dBm
802.11g / 20MHz	54Mbps		17		dBm
802.11n (MCS7_HT20)	65Mbps		16		dBm
802.11n (MCS7_HT40)	135Mbps		16		dBm

Table 4 - TX Output Power 5 Ghz (TA = 25°C VCC = 3.3V)

Parameter	Test condition	Min.	Typ	Max	Unit
802.11a / 20MHz	54Mbps		12		dBm
802.11n (MCS7_HT20)	65Mbps		11		dBm
802.11n (MCS7_HT40)	135Mbps		11		dBm
802.11ac (MCS9_VHT40)	180Mbps		9		dBm
802.11ac (MCS9_VHT80)	390Mbps		9		dBm

Table 5 - RX Sensitivity Bluetooth (TA = 25°C VCC = 3.3V)

Parameter	Test condition	Min.	Typ	Max	Unit
Bluetooth Class 1 Device			-70		dBm

Table 6 - TX Output Power Bluetooth (TA = 25°C VCC = 3.3V)

Parameter	Test condition	Min.	Typ	Max	Unit
Bluetooth Class 1 Device			10		dBm

4. Power

4.1. WLAN Power Consumption

Table 7 - RX Sensitivity 2.4 Ghz (TA = 25°C VCC = 3.3V)

Parameter	Test condition	Min.	Typ	Max	Unit
802.11b / 20MHz	11Mbps		62		mA
802.11g / 20MHz	54Mbps		70		mA
802.11n (MCS7_HT20)	65Mbps		67		mA
802.11n (MCS7_HT40)	135Mbps		72		mA

Table 8 - RX Sensitivity 5 Ghz (TA = 25°C VCC = 3.3V)

Parameter	Test condition	Min.	Typ	Max	Unit
802.11a / 20MHz	54Mbps		96		mA
802.11n (MCS7_HT20)	65Mbps		104		mA
802.11n (MCS7_HT40)	135Mbps		102		mA
802.11ac (MCS9_VHT40)	180Mbps		116		mA
802.11ac (MCS9_VHT80)	390Mbps		135		mA

Table 9 - TX Output Power 2.4 GHz (TA = 25°C VCC = 3.3V)

Parameter	Test condition	Min.	Typ	Max	Unit
802.11b / 20MHz	11Mbps		366		mA
802.11g / 20MHz	54Mbps		345		mA
802.11n (MCS7_HT20)	65Mbps		352		mA
802.11n (MCS7_HT40)	135Mbps		334		mA

Table 10 - TX Output Power 5 Ghz (TA = 25°C VCC = 3.3V)

Parameter	Test condition	Min.	Typ	Max	Unit
802.11a / 20MHz	54Mbps		510		mA
802.11n (MCS7_HT20)	65Mbps		445		mA
802.11n (MCS7_HT40)	135Mbps		485		mA
802.11ac (MCS9_VHT40)	180Mbps		485		mA
802.11ac (MCS9_VHT80)	390Mbps		430		mA

4.2. Bluetooth Power Consumption

Table 11 - RX Sensitivity 2.4 Ghz (TA = 25°C VCC = 3.3V)

Parameter	Min.	Typ	Max	Unit
Continuous RX burst		22		mA
Continuous TX Class 2 (+4 dBm)		39		mA
Continuous TX Class 2 (+12.5dBm)		66		mA
1.28 sec page scan (non-interlaced)		0.35		mA
1.28 sec LE ADV		0.21		mA
1.28 sec Sniff as master		0.19		mA
1.28 sec Sniff as slave		0.26		mA

5. Electric Specifications

The PIXI-9377-P and PIXI-9377-S can operate at 3.3V operation.

3.3V Operation Mode (PIXI-9377-P and PIXI-9377-S)

Parameter	Min.	Typ	Max	Unit
VDD_3P3	3.135	3.3	3.465	V
VDD_IO	3.135	3.3	3.465	V
VDD_SDIO	3.135	3.3	3.465	V

The PIXI-9377-S can also operate at 1.8V operation as follow.

1.8V Operation Mode (PIXI-9377-S only)

Parameter	Min.	Typ	Max	Unit
VDD_3P3	3.135	3.3	3.465	V
VDD_IO	1.71	1.8	1.89	V
VDD_SDIO	1.71	1.8	1.89	V

5.1 Power domain associated pins

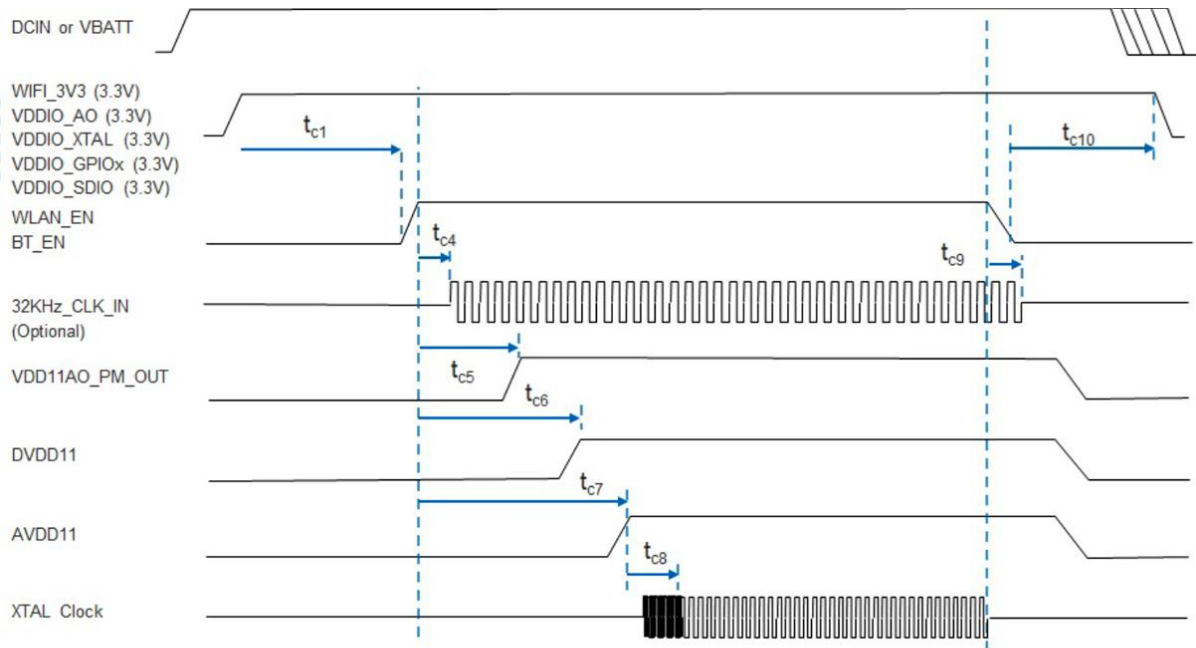
Power Domain	Pins Connected
VDD_3P3	General Power Domain for operation
VDD_IO	28, 53~61, 63, 65
VDD_SDIO	46~52

6. Power Sequence Specifications

6.1 All power rails supplied with 3.3 V

All power pins are connected to 3.3 V only include VDDIO_AO, VDDIO_XTAL, VDDIO_GPIOx.

Note: All host signals are either GND or floating before WLAN_EN/BT_EN = "high", and after WLAN_EN/BT_EN = Low.



Symbol	Parameter	Min	Max	Unit
ta1	90% of 3.3 V to 0.7 V of both WLAN_EN and BT_EN	0		μs
ta4	WLAN_EN valid to LF_CLK_IN input	0		μs
ta5	WLAN_EN valid to VDD11AO_PM_OUT established		50	μs
ta6	WLAN_EN to DVDD11		3.5	μs
ta7	WLAN_EN to AVDD11		4	μs
ta8	AVDD11 to XTAL clock stable	1		μs
ta9	WLAN_EN de-assert ("low") to LF_CLK_IN de-assert (tristate or low)	0		μs
ta10	Both WLAN_EN = low and BT_EN = low to 90% of 3.3 V	10		μs

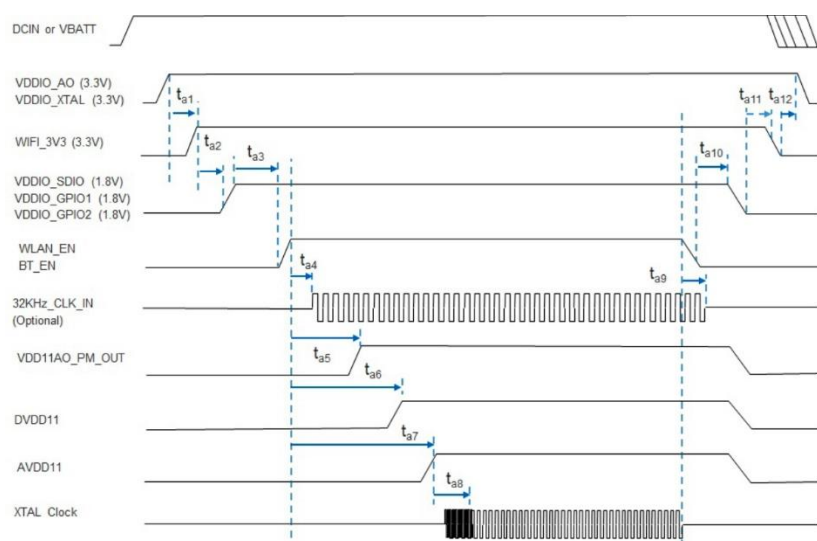
6.2 I/O Power Rails Supplied with 1.8V

6.2.1 With Removable Power Source.

If the battery source can be removed from the end user device (battery removed, AC/DC plugged in), this is the recommended power sequence. It will avoid violating the power off sequence by allowing the 3.3 V rail to shut down after the 1.8 V rail.

Notes:

1. VDDIO_GPIO voltage should match VIO voltage from the host. In some applications, VDDIO_GPIO may connected to 3.3 V upon Host VIO voltage.
2. In this case, both WLAN_EN and BT_EN on the QCA9377 are at 3.3 V due to using the VDD_AO power rail. If the host VIO voltage is 1.8 V, it must have level shifters to interface with host.
3. All host interface signals must stay floating or low before valid power on sequence (WLAN_EN and BT-EN goes high).



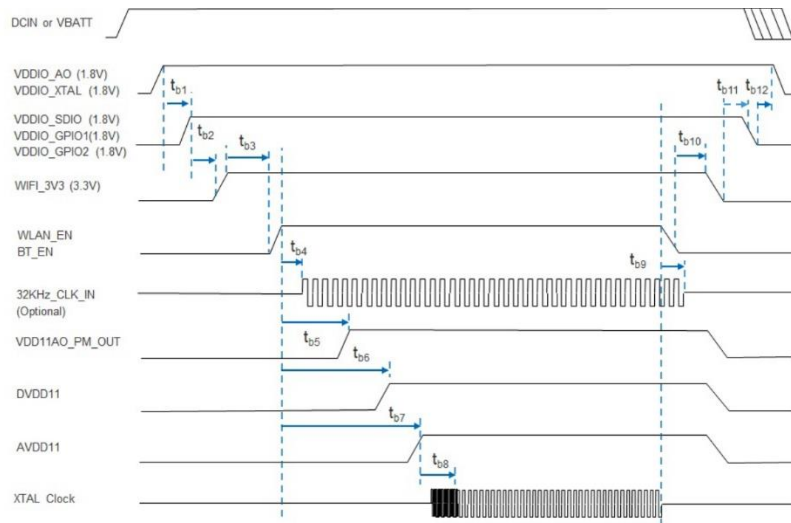
Symbol	Parameter	Min	Max	Unit
ta1	No requirement if VDDIO_AO connected to 3.3 V	0		μs
ta2	90% of 3.3 V to 10% of 1.8 V	0	0	μs
ta3	90% of VDDIO_GPIO to 0.7 V of both WLAN_EN and BT_EN	10		μs
ta4	WLAN_EN valid to LF_CLK_IN input	0		μs
ta5	WLAN_EN valid to VDD11AO_PM_OUT established		50	μs
ta6	WLAN_EN to DVDD11		3.5	μs
ta7	WLAN_EN to AVDD11		4	μs
ta8	AVDD11 to XTAL clock stable	1		μs
ta9	WLAN_EN de-assert ("low") to LF_CLK_IN de-assert (tristate or low)	0		μs
ta10	Both WLAN_EN = low and BT_EN = low to 90% of 1.8 V	10		μs
ta11	3.3 V always higher than 1.8 V during operation, with power off by removing battery or unplugging AC/DC	0		μs
ta12	VDDIO_AO and VDDIO_XTAL should be connected to 3.3 V power rail	0		μs

6.2.2. Without Removable Power Source

If the battery source cannot be removed from the end user device, this is the recommended power sequence for this application. This sequence allows the software to control the power on/off sequence.

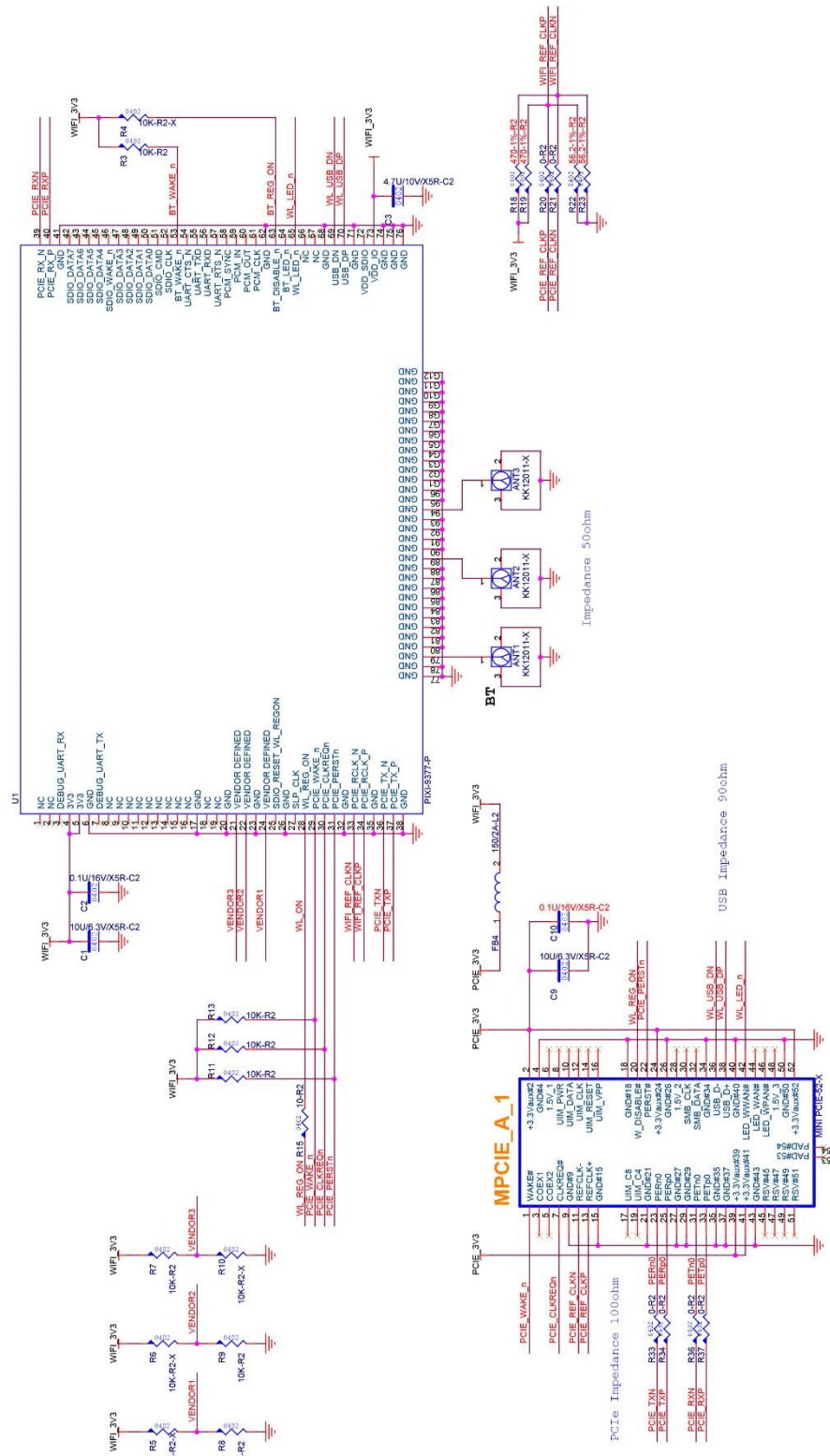
Notes:

1. VDDIO_GPIO voltage should match VIO voltage from the host. In some applications, VDDIO_GPIO may connected to 3.3 V upon host VIO voltage.
2. Both WLAN_EN and BT_EN of QCA9377 are 1.8 V. If host VIO voltage is 1.8 V, it does not need level shifter to interface with host.
3. All host interface signals must stay floating or low before WLAN_EN/BT_EN = "high", and after WLAN_EN/BT_EN = Low.



Symbol	Parameter	Min	Max	Unit
ta1	No requirement if VDDIO_AO connected to 1.8 V	0		μs
ta2	90% of 1.8 V to 10% of 3.3 V	0		μs
ta3	90% of 3.3 V to 0.7 V of both WLAN_EN and BT_EN	10		μs
ta4	WLAN_EN valid to LF_CLK_IN input	0		μs
ta5	WLAN_EN valid to VDD11AO_PM_OUT established		50	μs
ta6	WLAN_EN to DVDD11		3.5	μs
ta7	WLAN_EN to AVDD11		4	μs
ta8	AVDD11 to XTAL clock stable	1		μs
ta9	WLAN_EN de-assert ("low") to LF_CLK_IN de-assert (tristate or low)	0		μs
ta10	Both WLAN_EN = low and BT_EN = low to 90% of 3.3 V	10		μs
ta11	10% of 3.3 V to 90% of 1.8 V	0		μs
ta12	VDDIO_AO and VDDIO_XTAL should be connected to 1.8 V power rail	0		μs

7.2 PIXI-9377-P (PCIe)



7.3 Signal Connection Checklist while integrating PIXI-9377

While integrating the PIXI-9377 in an OEM design is straightforward. The following common design mistakes should be checked against.

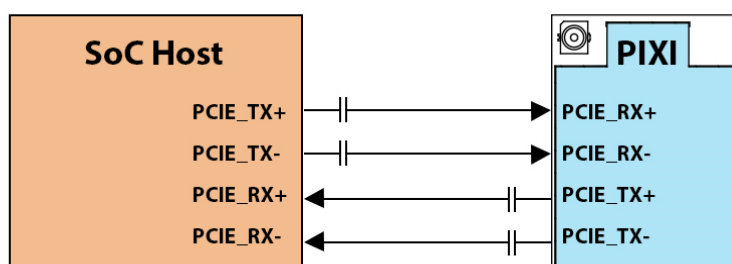
7.3.1 Module Integration Considerations - Circuit Implementation

It is recommended that all connection PCB (printed circuit board) traces to the power supply and digital control terminal be as short as possible. Though not necessarily required in all cases, it is a best practice to provide an optional shunt capacitor placement at the module pin on all active and routed power supply and digital control lines. Further, a series damping resistor placement should be incorporated between the module pin/shunt capacitor node and the source/sink of the digital control signals. This provides for effective bypassing and decoupling of digital lines from the radio module, in the event that the application circuit has longer power supply and digital routing.

7.3.2 PCIe Decoupling Capacitors on PIXI-9377-P Designs

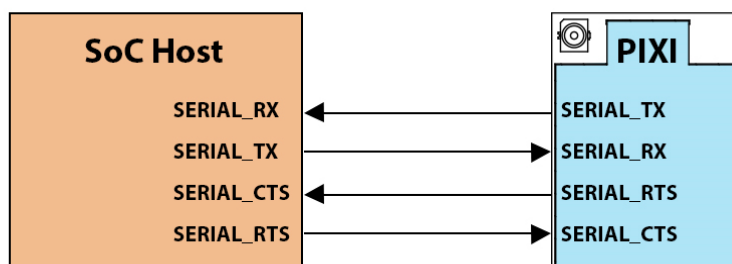
For PIXI-9377-P the Wi-Fi is connected over PCI Express. Make sure that the SOC TX signals connect to the PIXI-9377 RX signals and vice-versa.

It is strongly recommended to add decoupling capacitors on the PCI Express lanes TX side with a value of 100nF.



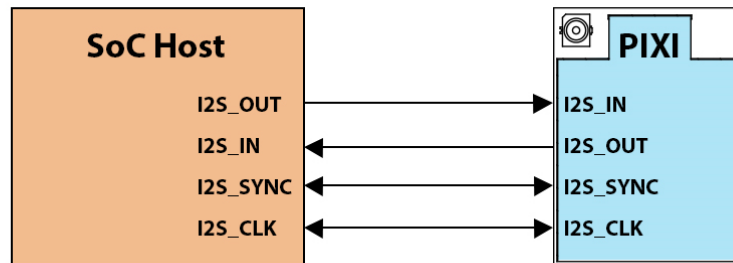
7.3.3 Bluetooth UART Signal Swapping on PIXI-9377-S Designs.

When using the PIXI-9277-S. The Bluetooth data signals are connected over the UART. Makes sure that the SOC TX signals connect to the PIXI-9377 RX signals and vice-versa.



7.3.4 Bluetooth PCM Audio Signal Swapping on PIXI-9377-S Designs.

When using the PIXI-9277-S. The Bluetooth audio signals are connected over the PCM. Makes sure that the SOC IN signals connect to the PIXI-9377 OUT signal and vice-versa.



8. EMC Compliance certification

The PIXI-9377 module has been tested and approved as a Modular Radio in accordance with the appropriate FCC, IC, ETSI, RED, TELEC and RCM standards. The supporting test data modular test report can be found on our corporate homepage download section at www.technexion.com.

Since this module and its associated set of approved antennas have been certified as a Modular Radio, this allows the end user to integrate this module into an end-product without the requirement of recertifying the radio module. The module-integrator is responsible for the unintentional conducted and radiated emissions and must verify that the integrated product is compliant with the rules associated with unintentional radiators. The module integrator is also required to maintain an engineering record of the verification testing and declare on the product through proper labeling and marking that the device is compliant with these particular rules.

The installed module's FCC ID and IC numbers need to be clearly marked on the product with the following verbiage "Contains FCC ID: 2AKZA-QCA9377" and "Contains IC: 22364-QCA9377".

8.1 FCC Testing Requirements for End-Product

Once the module is integrated and the end-product is realized, the end-product must be tested and follow the verification process for Unintentional Conducted and Radiated Emissions in accordance to the FCC and IC guidelines. The module needs to be powered and placed in the receive mode for this test. The receiver must be tuned to its lowest frequency channel, mid-frequency channel, and highest frequency channel. The supporting test data does not need to be submitted to the FCC or IC. The implementation of the module in a specific end-product should also be reviewed to ensure compliance with the FCC and IC requirements for SAR and MPE if applicable.

8.2 FCC Wireless certification: New Filing vs. Permissive Change

Products are continually under revision due to obsolete or unavailable parts, cost cutting, updates for a product release, and so on. The dilemma for most companies is determining the process and requirements for altered products. This section outlines the options available and highlights a few examples for guidance.

Here we focus on changes for unlicensed transmitters that require an FCC Certification or Equipment Authorization. Products require “Document of Compliance” testing if the change could adversely affect the radiating characteristics of the equipment. This procedure is outlined in Title 47 Part 2.1073.

To allow products to be modified without requiring a new filing, the FCC has defined three Permissive Change options listed in Title 47 Part 2.1043.

Class I Permissive Change

Class I Permissive Change includes modifications which do not degrade the characteristics reported by the manufacturer and accepted by the Commission when certification is granted. No filing with the Commission is required for a Class I Permissive Change. [FCC source: 2009 Title 47 CFR 2.1043 ((b)(1))]

Class II Permissive Change

Class II Permissive Change includes modifications which degrade the performance characteristics as reported to the Commission at initial certification. Such degraded performance must still meet the minimum requirements of the applicable rules. When a Class II permissive change is made, the grantee must supply the Commission with complete information and results of tests of characteristics affected by such change. Modified equipment cannot be marketed under the existing grant of certification prior to acknowledgment by the Commission that the change is acceptable. [FCC source: 2009 Title 47 CFR 2.1043 ((b)(2))]

Class III Permissive Change

Class III Permissive Change includes software modifications of a software-defined radio transmitter that change the frequency range, modulation type or maximum output power (either radiated or conducted) outside the parameters previously approved, or that change the circumstances under which the transmitter operates in accordance with Commission rules.

When a Class III Permissive Change is made, the grantee must supply the Commission with a description of the changes and test results showing that the equipment complies with applicable rules with the new software loaded, including compliance with applicable RF exposure requirements. The modified software must not be loaded into the equipment, and the equipment must not be marketed with modified software under the existing grant of certification, prior to acknowledgment by the Commission that the change is acceptable.

Class III changes are permitted only for equipment on which no Class II changes have been made from the originally approved device. [FCC source: 2009 Title 47 CFR 2.1043 ((b)(3))]

Following are examples of three common changes to certified radios.

Example 1: Change of Antenna for a Part 15 Subpart C Unlicensed Radio

Both products and modules are tested and certified with specific antennas. In many cases the end user deviates from the specified antenna due to cost, style or availability. To maintain a Class I Permissive Change, the new antenna must be an equivalent antenna, defined as one of the same type (e.g., yagi or dipole) and must be of equal or less gain than an antenna previously authorized under the same FCC ID, and must have similar in-band and out-of-band characteristics (consult

specification sheet for cutoff frequencies).[FCC source: 178919 D01 Permissive Change Policy v04r04]

Therefore, if the antenna is of a different type or higher gain, the product or radio module requires a Class II Permissive Change, and all FCC provisions of Title 47 CFR 15.203 for antenna requirements must be met.

Example 2: Electrical Hardware Changes

The main deciding factor for hardware changes is whether the device is “electrically equivalent.” If the device is electrically equivalent, a Class I or Class II Permissive Change is acceptable, depending on the test result evaluation. Again, it is the manufacturer’s responsibility to ensure that the devices are electrically equivalent and to perform analysis that the device’s performance has not degraded. Some form of testing is generally required to support the claim that the device’s performance has not degraded.

Changes in frequency or output power, or removal or addition of components related to the RF section of the device automatically require a new filing and new FCC ID number.

Example 3: Enclosure Change

Obviously, enclosure changes do not apply to modular radios, but for other products, slight changes are allowed to the enclosure under a permissive change. However, major reorientation of the device inside the enclosure is not acceptable. For example, if the device was originally tested and certified lying flat and the new enclosure has the device mounted vertically, this would require retesting and a new FCC filing.

Summary

Changes to a modular radio or product will result in either a Permissive Change or a new FCC filing and ID number. The degree of change is the determining factor for both the process and the amount of supporting data required to illustrate compliance. TechNexion takes pride in helping customers evaluate and develop the most beneficial test plan to properly address these changes.

9 Antenna Integration Guidelines

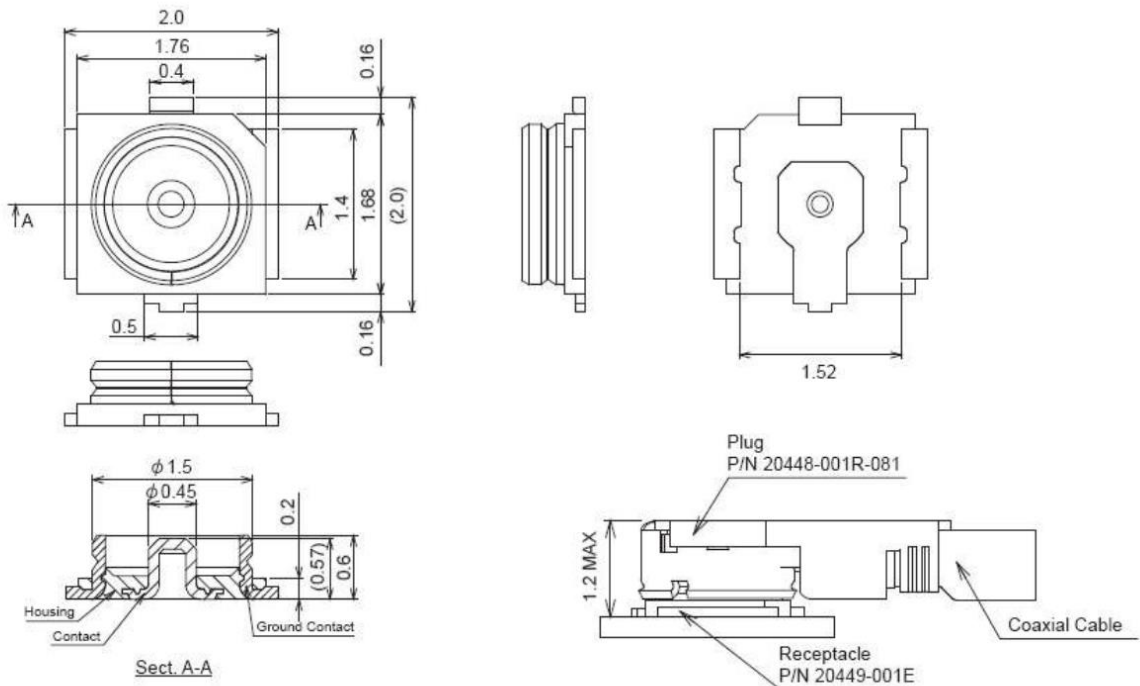
The antenna should be placed such that it is minimally disturbed by the product's packaging material. The incorporation of the largest practical free-space clearance around the antenna is important for maximizing overall performance. Further, the antenna must be placed such that at least a 20 cm separation distance is maintained from the antenna to all other radio transmitters.

In addition to the recommendations given for the antenna systems and the module placement onto a product PCB, it is recommended that all wiring and interconnect systems within the product not be routed anywhere close to the PIXI-9377 module and its associated circuitry on the PCB, doing so could change the emission characteristics of the module.

9.1 Antenna Connector Specifications

The PIXI-9377 module has an on-board IPEX MHF4 antenna connector following the specifications below.

When choosing a mating coax patch cable please refer to the following dimensional criteria.



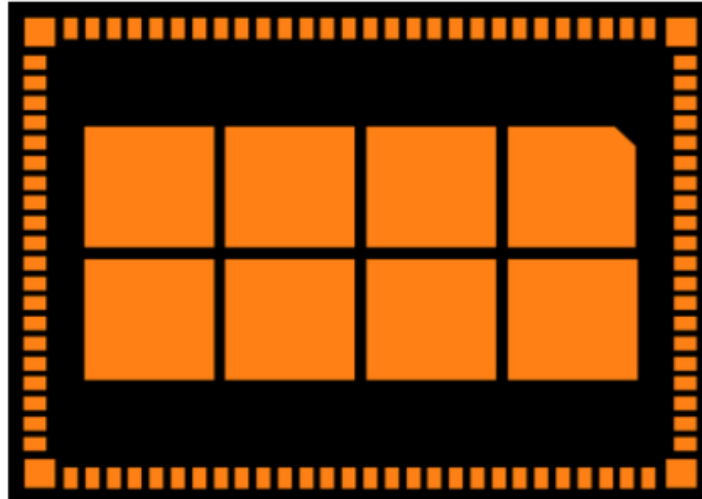
9.2 Certified Antenna Options

The PIXI-9377 has been FCC / IC / TELEC certified when used with one of the approved antennas.

Table 12 – Antenna Options

	Partnumber	Description
	ANTP150P232525D2450MHF4	Internal patch antenna (1.5dB)
	ANTP180A138045D2450MHF4	External Dipole antenna (2dB)
	ANTP180A207070D2450MHF4	External Dipole antenna (7dB)

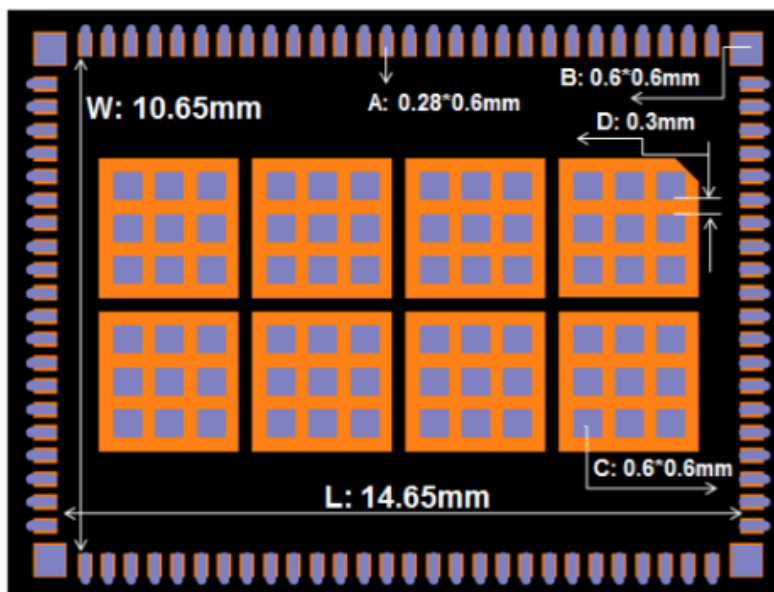
BTC (BOTTOM TERMINAL CHIP) PAD DESIGN



Recommendation:

- Mainboard PAD VS Module PAD = 1 : 1

BTC STENCIL APERTURE (RECOMMENDATION)

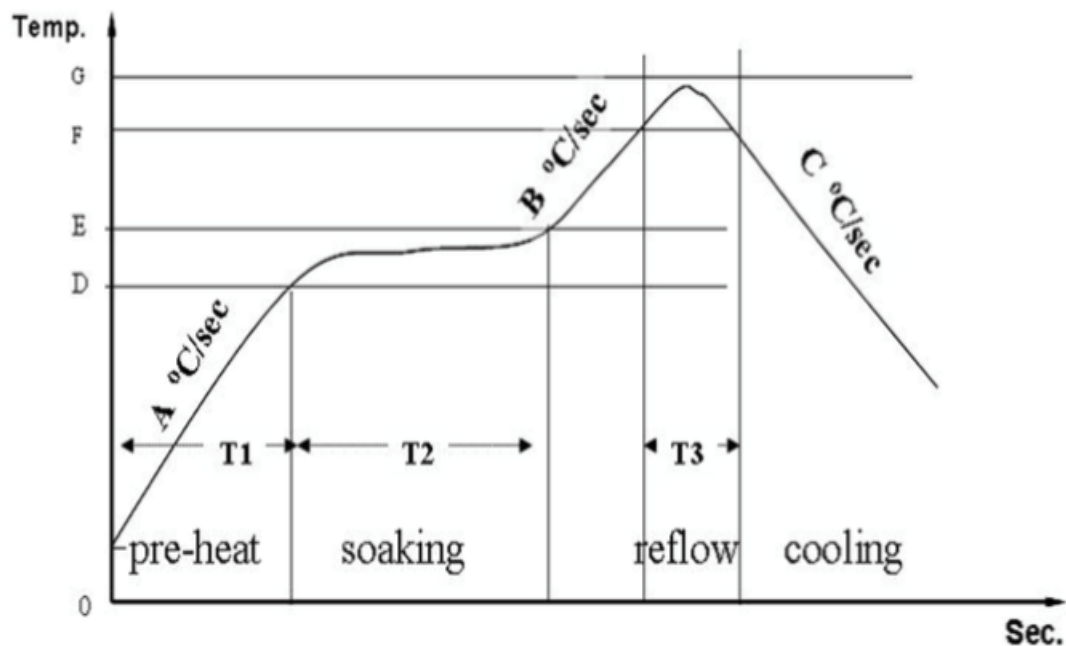


Remark:

- Orange area: PCB PAD
- Light-blue area: Stencil aperture
- Stencil thickness: 0.1~0.12mm

Recommendation:

- 0.5mm pitch functional pin stencil aperture size (A): 0.28*0.6mm, the pin hole internal spur: Width (W) =10.65mm, Length (L) =14.65mm.
- Four corners' stencil aperture size (B): 0.6*0.6mm, they should be center located of PAD.
- 9 square apertures on each GND PAD, each size (C): 0.6* 0.6mm, interval(D): 0.3mm

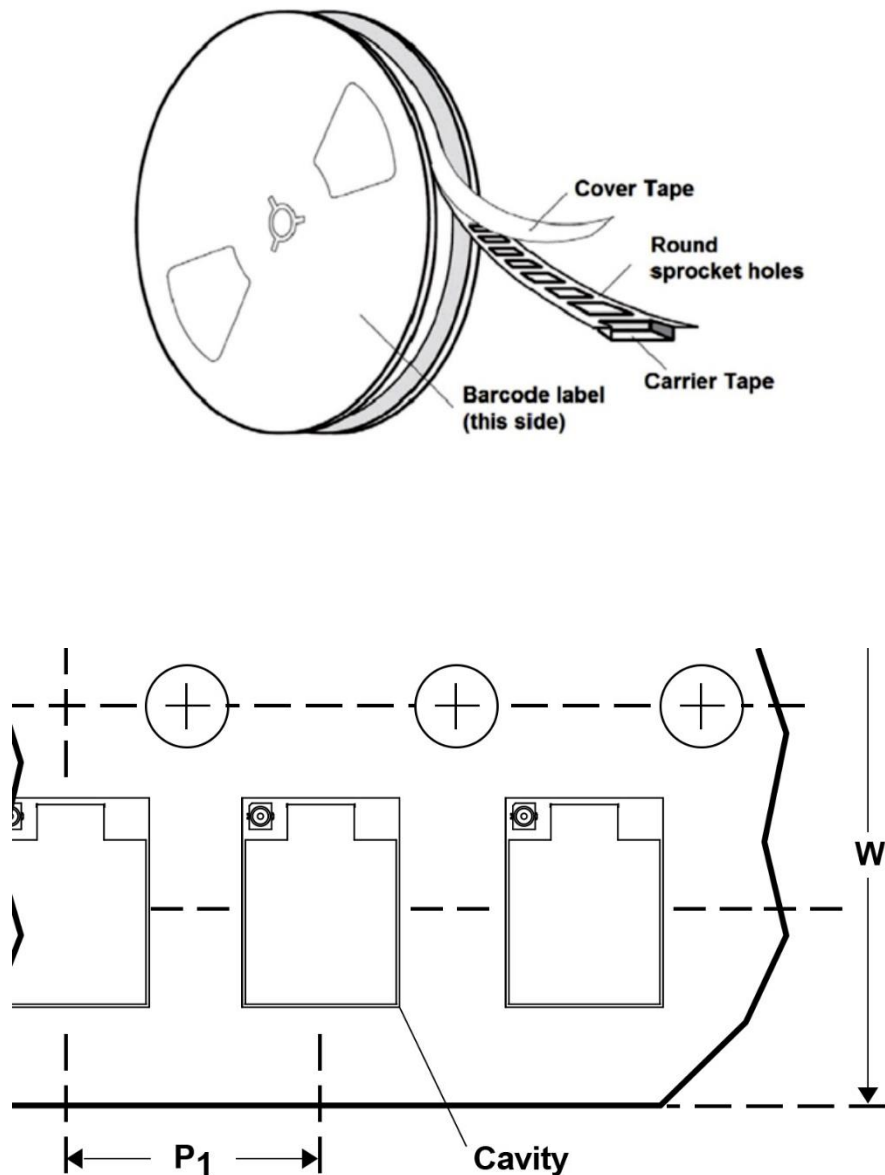
BTC REFLOW SOLDERING PROFILE (RECOMMENDATION)**Standard conditions for reflow soldering:**

- a. Pre-heating Ramp (A) (Initial temperature: 150°C): 1~2.5°C/sec;
- b. Soaking Time (T2) (150°C~180°C): 60sec~100sec;
- c. Peak Temperature (G): 230~250°C;
- d. Reflow Time (T3) (>220°C): 30~60 sec;
- e. Ramp-up Rate (B): 0~2.5°C/ sec;
- f. Ramp-down Rate (C): 1~3°C/ sec.

11. Packaging Information

TechNexion PIXI-9377 Modules are packaged either trays or reels.

11.1. Reel Packaging Information

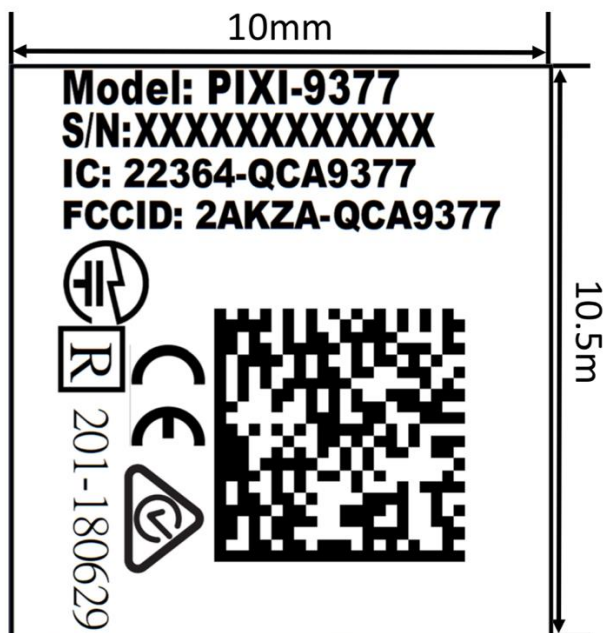


NOTE:

1. Dimensions in mm.
2. 10 Sprocket Hole Pitch Cumulative Tolerance $\pm 0.1\text{mm}$.
3. Pocket Position Relative to Sprocket Hole Measured as True Position of Pocket, not Pocket Hole
4. A full reel contains 1000 modules.

12. Labelling Information

The PIXI-9377 contains an unique serial label as follow:



13. Ordering Information

The PIXI-9377 can be ordered in the following configurations.

Partnumber	Description Min	Wi-Fi	Bluetooth
PIXI-9377-S	QCA9377 SDIO 802.11ac + UART BT 5 MODULE	SDIO	UART
PIXI-9377-P	QCA9377 PCIE 802.11ac + USB BT 5 MODULE	PCIE	USB

14. Important Notice

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TechNexion Ltd.
16F-5, No. 736, Zhongzheng Road,
ZhongHe District, 23511, New Taipei City, Taiwan
Phone : +886-2-82273585
Fax : +886-2-82273590
E-mail : sales@technexion.com
Web : <http://www.technexion.com/>