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Application Processor

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S3C44B0



• Derivative Market

General Description

SAMSUNG's S3C44B0X 16/32bit RISC microprocessor is designed to provide a cost-effective and high performance micro-controller solution for hand-held devices and general applications. To reduce total system cost, S3C44B0X also provides the following: 8KB cache, optional internal SRAM, LCD controller, 2-channel UART with handshake, 4-channel DMA, System manager (chip select logic, FP/ EDO/SDRAM controller); 5 channel timers with PWM, I/O ports, RTC, 8-channel 10-bit ADC, IIGBUS interface, IISBUS interface, Sync. SIO interface and PLL for clock.

The S3C44B0X was developed using a ARM7TDMI core, 0.25 um CMOS standard cells, and a memory compiler. Its low-power, simple, elegant and fully static design is particularly suitable for cost-sensitive and power sensitive applications. Also S3C44B0X adopts a new bus architecture, SAMBA II (SAMSUNG ARM CPU embedded Microcontroller Bus Architecture).

An outstanding feature of the S3C44B0X is its CPU core, a 16/32bit ARM7TDMI RISC processor (66MHz) designed by Advanced RISC Machines, Ltd. The architectural enhancements of ARM7TDMI include the Thumb decompressor, an on-chip ICE breaker debug support, and a 32bit hardware multiplier. By providing a complete set of common system peripherals, the S3C44B0X minimizes overall system costs and eliminates the need to configure additional components.

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RoHS information

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material declaration sheet does not contain hazardous materials defined in China RoHS

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S3C44B0X01-EDR0 is Lead-free and RoHS 5 of 6. 

S3C44B0X01-YD80 is Lead-free and RoHS-compliant. 

S3C44B0X01-YDR0 is Lead-free and RoHS 5 of 6. 

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