

LV44J007-156-25M LVDS Glock Oscillator







LV44J 3.2 x 2.5 x 0.9 mm LCC Ceramic Package

Features

- Quartz crystal controlled Precision Square Wave Oscillator
- LVDS Output
- Enable/Disable Function on pad 1
- Low Jitter
- 2.5V nominal Supply Voltage
- 156.25 MHz Nominal Frequency

Electrical Characteristics					
Parameter	Min	Тур	Max	Unit	Condition
Frequency Fo	-	156.25	-	MHz	
Frequency Stability	-	-	±50	ppm	Includes supply voltage change, load change, aging for 1 year at 25°C \pm 2°C, shock, vibration and temperatures
Operating Temperature Range	-40	-	+85	°C	
Supply Voltage ¹ V _{CC}	2.25	2.5	2.75	V	
Supply Current I _{CC}	-	-	25	mA	
Output Waveform		LV	'DS		
Output High Level V _{OH}	-	1.43	1.6	V	
Output Low Level V _{OL}	0.9	1.1	-	V	
Differential Output Voltage V _{OD}	247	330	454	mV	
Output Offset Voltage Vos	1.125	1.25	1.375	V	See Load Circuit R = 50 ohms
Differential Output Error ΔV _{OD}	-	-	50	mV	
Output Offset Voltage ΔV _{os}	-	-	50	mV	
Output Voltage Amplitude Vo p-p	494	-	-	mV	
Output T _R and T _F	-	-	0.4	ns	Vth is 20% and 80% of waveform
Startup Time	-	-	10	ms	Time for output to reach specified frequency
Duty Cycle	45	-	55	%	Referenced to 50% of Vo p-p; (Tw/Tper)*100, see waveform diagram
V _{DISABLE}	-	-	30	%Vcc	Referenced to Ground
V _{ENABLE}	70	-	-	70 VCC	Referenced to Ground
Enable Time	-	-	10	ms	Time for output to reach a logic state
Disable Time	-	-	200	ns	Time for output to reach a high Z state
Enable/Disable Internal Pull-up	30	70	150	ΚΩ	To V_{CC} , measured with pad 1 = 0.0 volts
Standby Current	-	-	30	μA	Pad 1 low, device disabled
Jitter	-	36	90	fs	12 kHz to 20MHz from Fo
Phase Noise 100 Hz 1 kHz 10 kHz 100 kHz 1 MHz 20 MHz	-	-67 -105 -153 -160 -164 -165 -165	-		25°C at Fo
Storage Temperature Range	-55	-	+125	°C	

May 25, 2023 Rev. A Product information is current as of publication date. The product conforms to specifications per the terms of the Pletronics standard warranty. Production processing does not necessarily include testing of all parameters.

Notes: Specifications with Pad 1 E/D open circuit

1 Place an appropriate power supply bypass capacitor next to device for correct operation



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Device Marking

P*FF.FF* L
• YMDxxx

P = Pletronics

FFF.FF L = Frequency in MHz, L for LVDS

YMD = Date Code, All other marking is internal code

Note: Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD (Year Month Day)

Code	2	3		4	5	6	Code		4	В	С	D	Е		F	G	Н	J	K	L	М
Year	2022	202	23	2024	2025	2026	Montl	ı JA	٨N	FEB	MAR	APR	MA'	Y JL	JN	JUL	AUG	SEP	OCT	NOV	DEC
								•					•	•				•		•	
Code	1	2	3	4	5	6	7	8	9	Α	В	С	D	Е	F	G	i				
Day	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	5 16	6				
Code	Н	J	K	L	М	N	Р	R	Т	U	٧	w	Χ	Υ	Z						
Day	17	18	19	9 20	21	22	23	24	25	26	27	28	29	30	31	1					

Package Labeling

P/N Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII

RoHs Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

RoHS Compliant

2nd LvL Interconnect

Category=e4

Max Safe Temp=260C for 10s 2X Max

Pletronics Inc. certifies this device is in accordance with the RoHS and REACH directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's

Weight of the Device: 0.028 grams

Moisture Sensitivity Level: 1 As defined in J-STD-020D

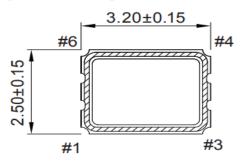
Second Level Interconnect code: e4



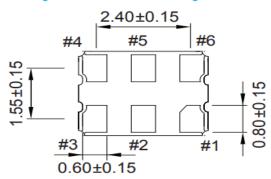
LVDS Glock Oscillator

Mechanical Dimensions

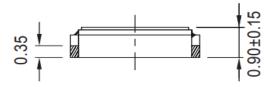
[TOP VIEW]



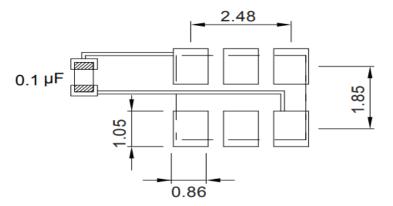
[BOTTOM VIEW]



[SIDE VIEW]



Pin# **Function** Tri-State NC 2 **GND** 3 Output 4 5 Comp.Output 6 **V**DD



Pad Layout

Disclaimer: Recommended layout shown. Adjust layout as needed for individual process requirements.

To ensure optimal oscillator performance, place a by-pass capacitor of 0.1µF as close to the part as possible between Vdd and GND pads.

Contacts (pads): Gold (0.3 to 1.0 µm) over Nickel (1.27 to 8.89 µm)

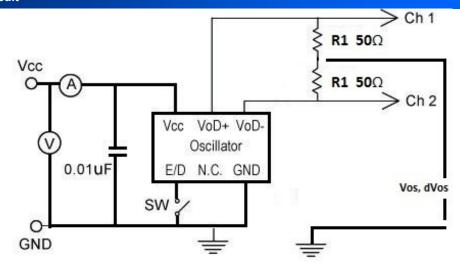
For Optimum Jitter Performance, Pletronics recommends:

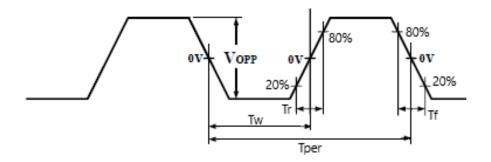
- A ground plane under the device
- Do not route large transient signals (both current and voltage) under the device
- Do not place near a large magnetic field such as a high frequency switching power supply
- Do not place near piezoelectric buzzers or mechanical fans



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Electrical Test /Load Circuit





Environmental / ESD Ratings

Reliability: Environmental

Parameter	Condition
Mechanical Shock	MIL-STD-883, Method 2002, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A
Solderability	IPC J-STD-002
Thermal Cycle	MIL-STD-883 Method 1010, Condition B

Thermal Characteristics:

The maximum die or junction temperature is 150°C

ESD Rating

Model	Min. Voltage	Condition		
Human Body Model	2000V	JESD22-A114		
Machine Model	200V	JESD22-A115		

Absolute Maximum Ratings

Parameter	Unit
V _{CC} Supply Voltage	-0.3V to +4.5V
Vi Input Voltage	-0.3V to V _{CC} + 0.3V
Vo Output Voltage	-0.3V to V _{CC} + 0.3V

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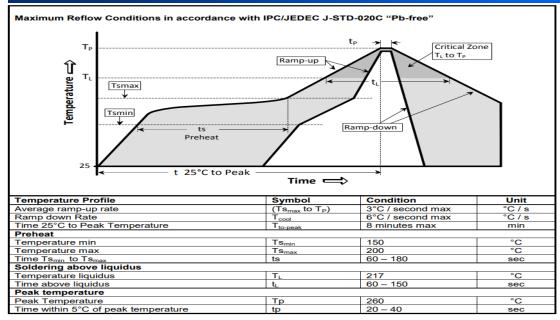
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LV08 Glock Oscillator

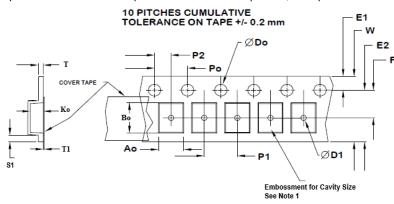
Reflow Cycle



The part may be reflowed 2 times without degradation (typical for lead free processing).

Tape and Reel

Tape and Reel available for quantities of 250 to 3000 per reel, cut tape for < 250. 8mm tape, 4mm pitch.

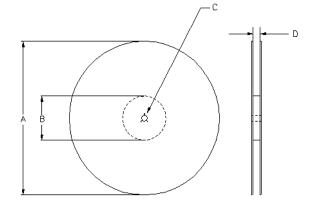


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Tape Variable Dimensions Table 2										
Tape Size	E2 typ	F	P1	W max	Ao	Во	Ko			
8mm	6.25	3.5 ±0.05	4.0 ±0.1	8.2	2.7±0.1	3.4±0.1	1.4±0.1			

Dimensions in mm Drawing Not to scale Note 1: Embossed cavity to conform to EIA- 481-B

Tape Constant Dimensions Table 1										
Tape Size	Do	D1 typ	E1	Po	P2	S1 min	T max	T1 max		
8mm	1.5	1.0	1.75	4.0	2.0	0.6	0.3	0.1		
OIIIIII	+0.1 -0.0	1.0	±0.1	±0.1	±0.05	0.0	0.3	0.1		



Reel Dimensions (may vary) Table 3										
		A	В		С	D				
Reel Size	Inch- es	mm	Inches	mm	mm	mm				
_					13.0	Tape size +0.4				
7	7.0	177.8	2.50	63.5	+0.5 -0.2	+2.0 -0.0				



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