

# BC856BS

# 65 V, 100 mA PNP/PNP general-purpose transistor

r 2025 Product data sheet

# 1. General description

PNP/PNP general-purpose transistor pair in a very small Surface-Mounted Device (SMD) plastic package.

NPN/NPN complement: BC846BS NPN/PNP complement: BC846BPN

## 2. Features and benefits

- Low collector capacitance
- Low collector-emitter saturation voltage
- · Closely matched current gain
- · Reduces number of components and board space
- · No mutual interference between the transistors

# 3. Applications

· General-purpose switching and amplification

## 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transistor						
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	-65	V
I <sub>C</sub>	collector current		-	-	-100	mA
h <sub>FE</sub>	DC current gain	$V_{CE} = -5 \text{ V; } I_{C} = -2 \text{ mA}$	200	290	450	

# 5. Pinning information

**Table 2. Pinning information** 

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E1	emitter TR1		C1 B2 E2
2	B1	base TR1	6 75 74	
3	C2	collector TR2		( TR1 )
4	E2	emitter TR2		
5	B2	base TR2	☐1 ☐2 ☐3 TOOODS (COTSOS)	E1 B1 C2
6	C1	collector TR1	TSSOP6 (SOT363)	sym018



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# 6. Ordering information

#### **Table 3. Ordering information**

- table of or table grant and table grant						
Type number	Package	ge				
	Name	Description	Version			
BC856BS	TSSOP6	plastic, surface-mounted package; 6 leads; 0.65 mm pitch; 2.1 mm x 1.25 mm x 0.95 mm body	<u>SOT363</u>			

# 7. Marking

#### Table 4. Marking codes

Type number	Marking code[1]
BC856BS	%E6

<sup>[1] % =</sup> placeholder for manufacturing site code

# 8. Limiting values

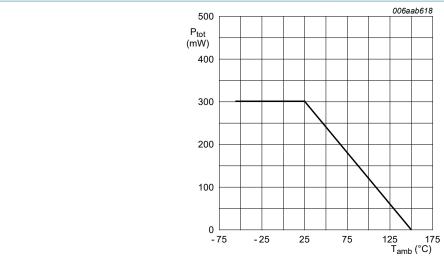
#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transisto	or		'	'		
$V_{CBO}$	collector-base voltage	open emitter		-	-80	V
$V_{CEO}$	collector-emitter voltage	open base		-	-65	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	-6	V
Ic	collector current			-	-100	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-200	mA
I <sub>BM</sub>	peak base current			-	-200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	200	mW
Per device	'		'	"		,
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	300	mW
T <sub>j</sub>	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

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FR4 PCB, single-sided, 35  $\mu m$  copper, tin-plated and standard footprint

Fig. 1. Per device: Power derating curve

### 9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions			Min	Тур	Max	Unit
Per transist	tor							
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1	1]	-	-	625	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point				-	-	230	K/W
Per device			•					
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1	1]	-	-	416	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

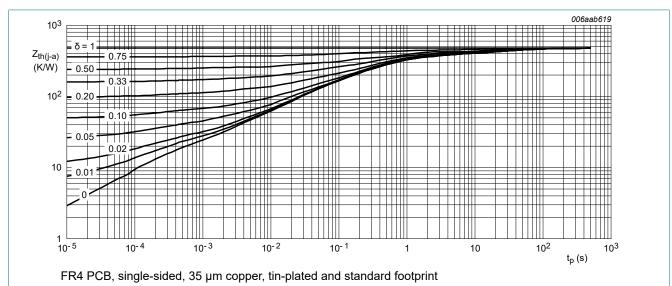


Fig. 2. Per transistor: Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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## 10. Characteristics

**Table 7. Characteristics** 

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transist	tor					
I <sub>CBO</sub>	collector-base cut-off	V <sub>CB</sub> = -50 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	-15	nA
	current	V <sub>CB</sub> = -30 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C	-	-	-5	μA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = -6 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	-100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = -5 V; I <sub>C</sub> = -10 μA; T <sub>amb</sub> = 25 °C	-	270	-	
		V <sub>CE</sub> = -5 V; I <sub>C</sub> = -2 mA	200	290	450	
V <sub>CEsat</sub>	collector-emitter	$I_C$ = -10 mA; $I_B$ = -0.5 mA; $T_{amb}$ = 25 °C	-	-55	-100	mV
	saturation voltage	$I_C$ = -100 mA; $I_B$ = -5 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	-	-200	-300	mV
V <sub>BEsat</sub>	base-emitter saturation	$I_C$ = -10 mA; $I_B$ = -0.5 mA; $T_{amb}$ = 25 °C	-	-755	-850	mV
voltage	voltage	$I_C$ = -100 mA; $I_B$ = -5 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	-	-900	-	mV
V <sub>BE</sub>	base-emitter voltage	$V_{CE}$ = -5 V; $I_{C}$ = -2 mA; $T_{amb}$ = 25 °C	-600	-650	-750	mV
		$V_{CE}$ = -5 V; $I_{C}$ = -10 mA; $T_{amb}$ = 25 °C	-	-	-820	mV
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = -10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	2.3	-	pF
C <sub>e</sub>	emitter capacitance	$V_{EB} = -0.5 \text{ V}; I_{C} = 0 \text{ A}; i_{c} = 0 \text{ A};$ $f = 1 \text{ MHz}; T_{amb} = 25 ^{\circ}\text{C}$	-	10	-	pF
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = -5 V; I <sub>C</sub> = -10 mA; f = 100 MHz; T <sub>amb</sub> = 25 °C	100	-	-	MHz
NF	noise figure	$V_{CE}$ = -5 V; $I_{C}$ = -0.2 mA; $R_{S}$ = 2 k $\Omega$ ; f = 10 Hz to 15.7 kHz; $T_{amb}$ = 25 °C	-	1.6	-	dB
	-	$V_{CE}$ = -5 V; $I_{C}$ = -0.2 mA; $R_{S}$ = 2 k $\Omega$ ; $f$ = 1 kHz; $B$ = 200 Hz; $T_{amb}$ = 25 °C	-	2.9	-	dB

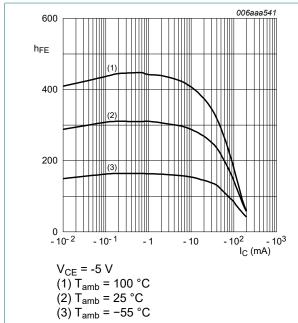


Fig. 3. Per transistor: DC current gain as a function of collector current; typical values

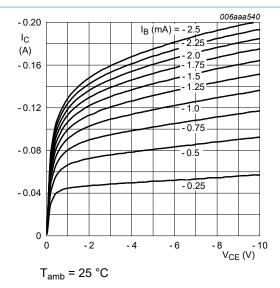


Fig. 4. Per transistor: Collector current as a function of collector-emitter voltage; typical values

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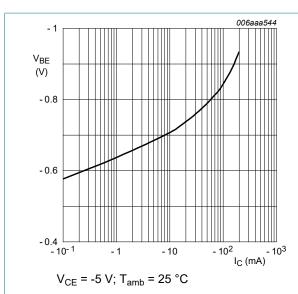
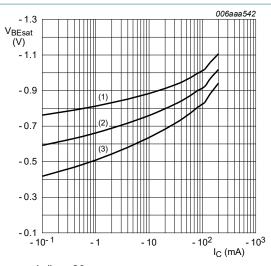
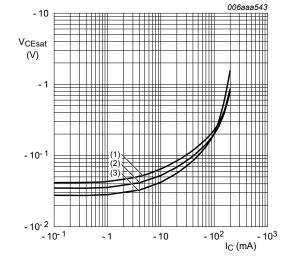


Fig. 5. Per transistor: Base-emitter voltage as a function of collector current; typical values



 $I_C/I_B = 20$ (1)  $T_{amb} = -55 \,^{\circ}C$ (2)  $T_{amb} = 25 \,^{\circ}C$ (3)  $T_{amb} = 100 \,^{\circ}C$ 

Fig. 6. Per transistor: Base-emitter saturation voltage as a function of collector current; typical values



 $I_C/I_B = 20$ (1)  $T_{amb} = 100 \,^{\circ}C$ (2)  $T_{amb} = 25 \,^{\circ}C$ (3)  $T_{amb} = -55 \,^{\circ}C$ 

Fig. 7. Per transistor: Collector-emitter saturation voltage as a function of collector current; typical values

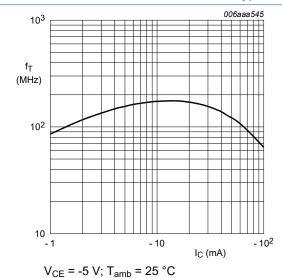


Fig. 8. Per transistor: Transition frequency as a function of collector current; typical values

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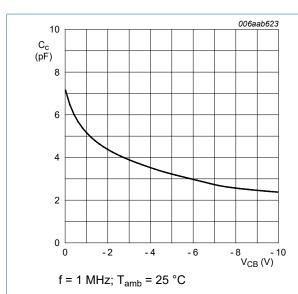


Fig. 9. Per transistor: Collector capacitance as a function of collector-base voltage; typical values

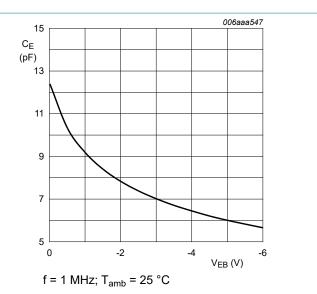
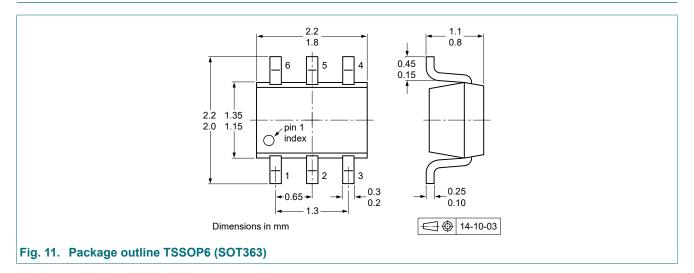


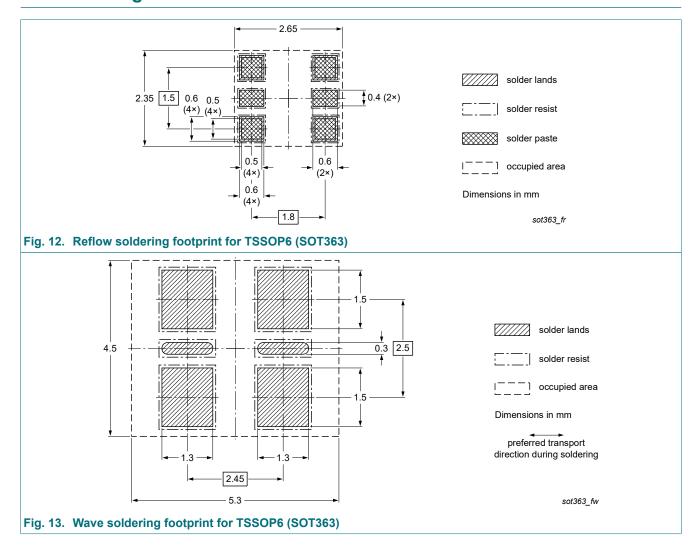
Fig. 10. Per transistor: Emitter capacitance as a function of emitter-base voltage; typical values

# 11. Package outline



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# 12. Soldering



## 65 V, 100 mA PNP/PNP general-purpose transistor

# 13. Revision history

#### **Table 8. Revision history**

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
BC856BS v.2	20251002	Product data sheet	-	BC856BS_1			
Modifications:	<ul> <li>Section "Packing information" removed.</li> <li>Product(s) changed to non-automotive qualification. Please refer to nexperia.com for automotive (-Q) product alternative(s).</li> </ul>						
BC856BS_1	20090811	Product data sheet	-	-			

2 October 2025

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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