

## 1A, 100V Schottky Barrier Surface Mount Rectifier

### FEATURES

- AEC-Q101 qualified
- Low power loss, high efficiency
- Ideal for automated placement
- High surge current capability
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free

### APPLICATIONS

- Low voltage, high frequency
- DC/DC converter
- Freewheeling diodes
- Reverse battery protection
- Car lighting

### MECHANICAL DATA

- Case: DO-214AC (SMA)
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.06g (approximately)

KEY PARAMETERS		
PARAMETER	VALUE	UNIT
$I_F$	1	A
$V_{RRM}$	100	V
$I_{FSM}$	70	A
$T_J \text{ MAX}$	175	°C
Package	DO-214AC (SMA)	
Configuration	Single die	



DO-214AC (SMA)



ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)			
PARAMETER	SYMBOL	VALUE	UNIT
Repetitive peak reverse voltage	$V_{RRM}$	100	V
Reverse voltage, total rms value	$V_{R(RMS)}$	70	V
Forward current	$I_F$	1	A
Peak forward surge current, 8.3ms single half sine-wave superimposed on rated load	$I_{FSM}$	70	A
Junction temperature	$T_J$	- 55 to +175	°C
Storage temperature	$T_{STG}$	- 55 to +175	°C

**THERMAL PERFORMANCE**

PARAMETER	SYMBOL	TYP	UNIT
Junction-to-lead thermal resistance	$R_{\theta JL}$	18	°C/W
Junction-to-ambient thermal resistance	$R_{\theta JA}$	66	°C/W
Junction-to-case thermal resistance	$R_{\theta JC}$	24	°C/W

**Thermal Performance Note:** Units mounted on PCB (5mm x 5mm Cu pad test board)

**ELECTRICAL SPECIFICATIONS** ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

PARAMETER	CONDITIONS	SYMBOL	TYP	MAX	UNIT
Forward voltage <sup>(1)</sup>	$I_F = 0.5\text{A}, T_J = 25^\circ\text{C}$	$V_F$	0.61	-	V
	$I_F = 1.0\text{A}, T_J = 25^\circ\text{C}$		0.68	0.73	V
	$I_F = 0.5\text{A}, T_J = 125^\circ\text{C}$		0.49	-	V
	$I_F = 1.0\text{A}, T_J = 125^\circ\text{C}$		0.55	0.59	V
Reverse current @ rated $V_R$ <sup>(2)</sup>	$T_J = 25^\circ\text{C}$	$I_R$	-	10	$\mu\text{A}$
	$T_J = 125^\circ\text{C}$		-	1	mA
Junction capacitance	1MHz, $V_R = 4.0\text{V}$	$C_J$	66	-	pF

**Notes:**

1. Pulse test with  $PW = 0.3\text{ms}$
2. Pulse test with  $PW = 30\text{ms}$

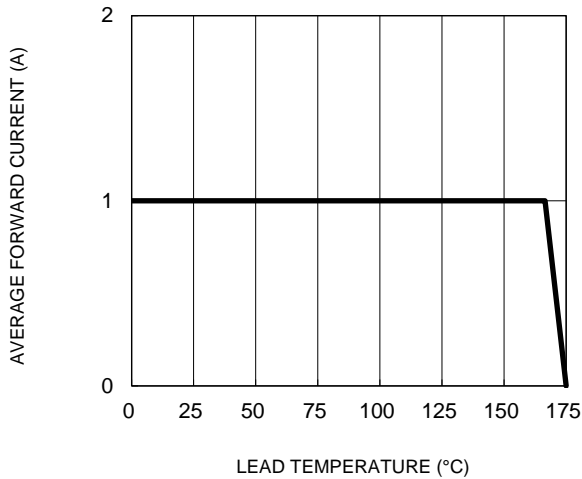
**ORDERING INFORMATION**

ORDERING CODE	PACKAGE	PACKING
SSA1H100H	DO-214AC (SMA)	7,500 / Tape & Reel

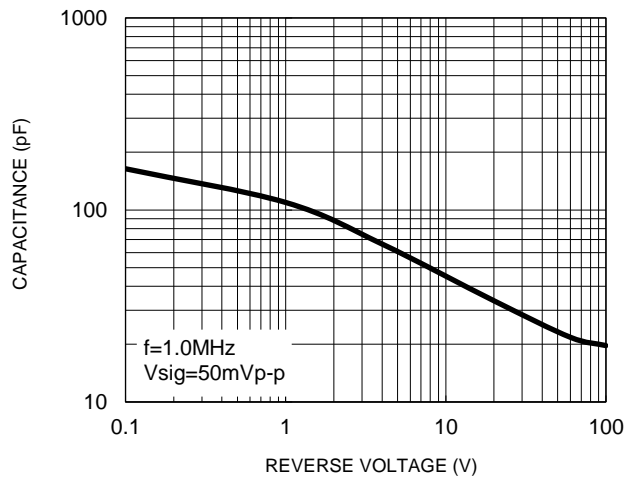
## CHARACTERISTICS CURVES

( $T_A = 25^\circ\text{C}$  unless otherwise noted)

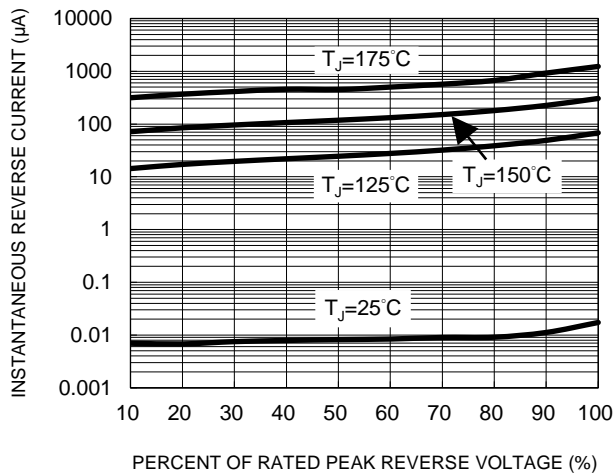
**Fig.1 Forward Current Derating Curve**



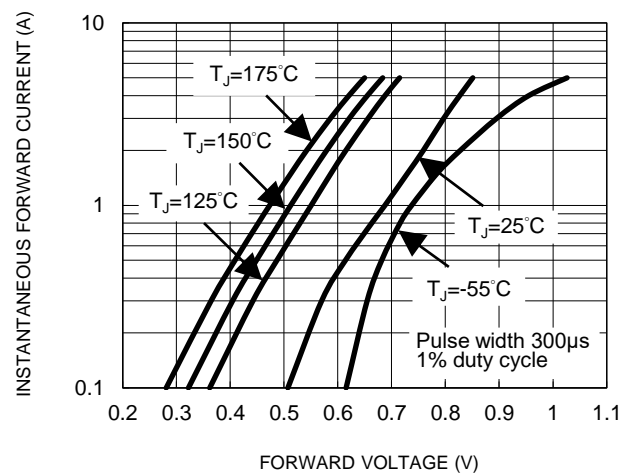
**Fig.2 Typical Junction Capacitance**



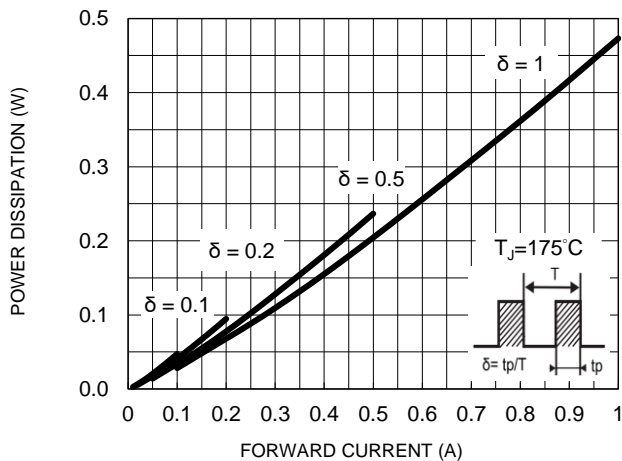
**Fig.3 Typical Reverse Characteristics**



**Fig.4 Typical Forward Characteristics**



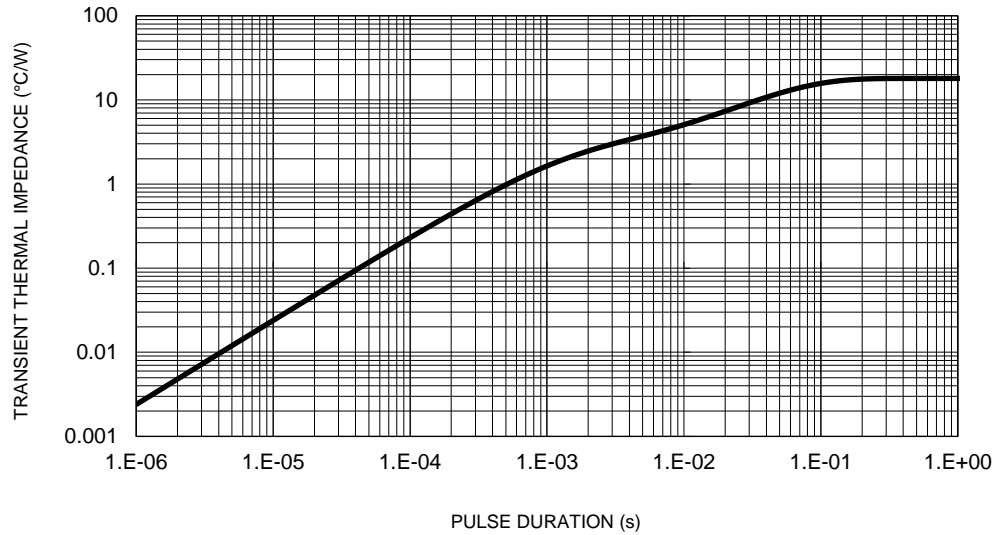
**Fig.5 Typical Forward Power Dissipation vs. Forward Current**



## CHARACTERISTICS CURVES

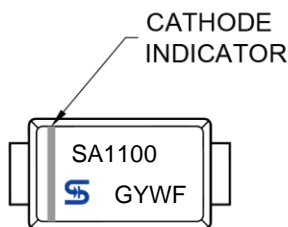
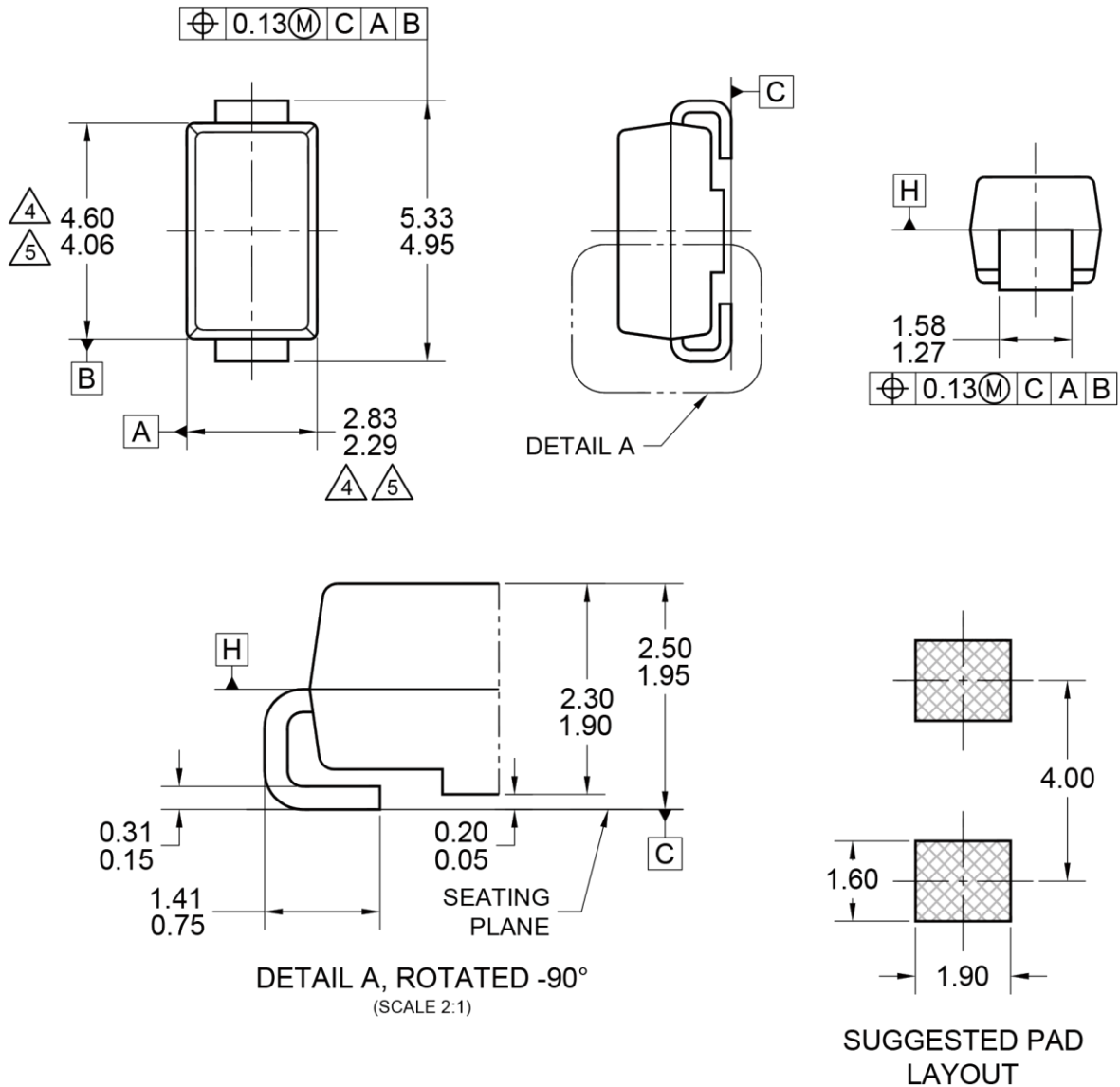
( $T_A = 25^\circ\text{C}$  unless otherwise noted)

**Fig.6 Typical Transient Thermal Characteristics**



## PACKAGE OUTLINE DIMENSIONS

### DO-214AC (SMA)



### MARKING DIAGRAM

G = Green compound  
YW = Date code  
F = Factory code

### NOTES: UNLESS OTHERWISE SPECIFIED

- ALL DIMENSIONS ARE IN MILLIMETERS.
- DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.
- PACKAGE OUTLINE REFERENCE: JEDEC DO-214, VARIATION AC, ISSUE D.
- MOLDED PLASTIC BODY DIMENSIONS DO NOT INCLUDE MOLD FLASH.
- MOLDED PLASTIC BODY LATERAL DIMENSIONS TO BE DETERMINED AT DATUM PLANE H.
- DWG NO. REF: HQ2SD07-DO214SMA-034 REV B.

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