

# 650 V, 8 A Silicon Carbide Schottky Diode

### **Features**

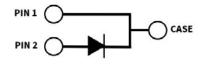
- 650-Volt Schottky rectifier
- Zero reverse recovery current
- Zero forward recovery voltage
- High-frequency operation
- Temperature-independent switching behavior
- Extremely fast switching
- Positive temperature coefficient on V<sub>E</sub>







TO-252-2



Package Types: TO-252-2

PN: C3D08065

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### **Applications**

- Switch mode power supplies (SMPS)
- Power factor correction
- Motor drives

### **Benefits**

- Replace bipolar with unipolar rectifiers
- Essentially no switching losses
- Higher efficiency
- Reduction of heat sink requirements
- Parallel devices without thermal runaway

# **Maximum Ratings** (T<sub>c</sub> = 25 °C Unless Otherwise Specified)

Parameter	Symbol	Value	Unit	Test Conditions	Note	
Repetitive Peak Reverse Voltage	V <sub>RRM</sub>	650				
Surge Peak Reverse Voltage	V <sub>RSM</sub>	650	V			
DC Blocking Voltage	V <sub>DC</sub>	650				
		25.5		T <sub>c</sub> = 25 °C		
Continuous Forward Current	l <sub>F</sub>	12	A	T <sub>c</sub> = 135 °C	Fig. 3	
		8		T <sub>c</sub> = 155 °C		
Repetitive Peak Forward Surge Current		34		T <sub>c</sub> = 25 °C, t <sub>p</sub> = 10 ms, Half Sine Wave		
	FRM	25		T <sub>c</sub> = 110 °C, t <sub>p</sub> = 10 ms, Half Sine Wave		
Non-Repetitive Peak Forward Surge Current	I <sub>FSM</sub>	71		T <sub>c</sub> = 25 °C, t <sub>p</sub> = 10 ms, Half Sine Wave	T:= 0	
		60		T <sub>c</sub> = 110 °C, t <sub>p</sub> = 10 ms, Half Sine Wave	Fig. 8	
Non-Repetitive Peak Forward Surge Current	I <sub>F, Max</sub>	650		T <sub>C</sub> = 25 °C, t <sub>P</sub> = 10 μs, Pulse	F:~ 0	
		530		T <sub>C</sub> = 110 °C, t <sub>P</sub> = 10 μs, Pulse	Fig. 8	
Power Dissipation	P <sub>tot</sub>	120	120 52	T <sub>c</sub> = 25 °C	F:- 4	
		52		T <sub>c</sub> =110 °C	Fig. 4	
Operating Junction and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C			

### **Electrical Characteristics**

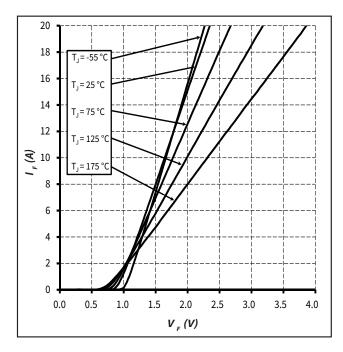
Parameter	Symbol	Тур.	Max.	Unit	Test Conditions	Note
Forward Voltage	V <sub>F</sub>	1.5	1.8	V	I <sub>F</sub> = 8 A, T <sub>J</sub> = 25 °C	Fig. 1
		2.1	2.4		I <sub>F</sub> = 8 A, T <sub>J</sub> = 175 °C	
	10	50		V <sub>R</sub> = 650 V, T <sub>J</sub> = 25 °C	Fig. 2	
Reverse Current	R	20	200 μΑ	V <sub>R</sub> = 650 V, T <sub>J</sub> = 175 °C		
Total Capacitive Charge	Q <sub>c</sub>	20		nC	$V_R = 650 \text{ V, I}_F = 8 \text{ A}$ $di/dt = 500 \text{ A}/\mu\text{S}$ $T_J = 25 \text{ °C}$	Fig. 5
Total Capacitance		395			V <sub>R</sub> = 0 V, T <sub>J</sub> = 25 °C, f = 1 MHz	Fig. 6
	С	37		pF	$V_R = 200 \text{ V}, T_J = 25 \text{ °C}, f = 1 \text{ MHz}$	
		32			V <sub>R</sub> = 400 V, T <sub>J</sub> = 25 °C, f = 1 MHz	
Capacitance Stored Energy	E <sub>c</sub>	3.0		μJ	V <sub>R</sub> = 400 V	Fig. 7

Note: This is a majority carrier diode, so there is no reverse recovery charge.

# **Thermal Characteristics**

Parameter	Symbol	Тур.	Unit	Note
Thermal Resistance from Junction to Case	R <sub>eJC</sub>	1.25	°C/W	Fig. 9

# **Typical Performance**





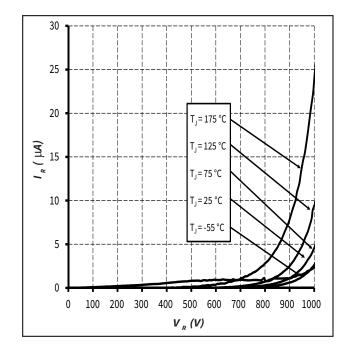
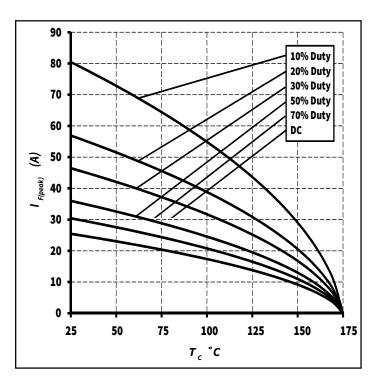


Figure 2. Reverse Characteristics

# **Typical Performance**



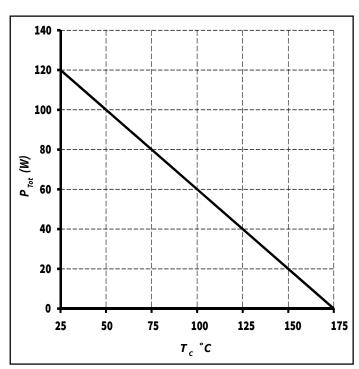
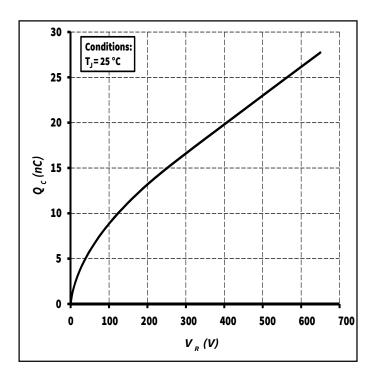


Figure 3. Current Derating

Figure 4. Power Derating





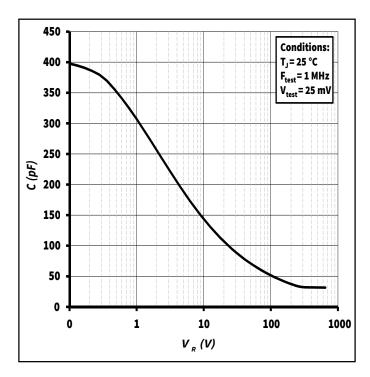
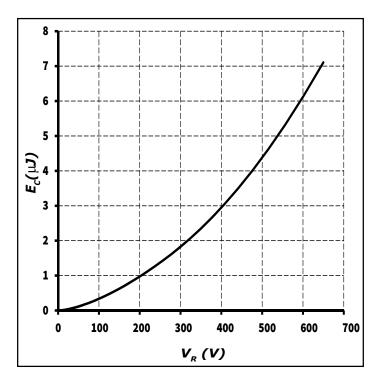


Figure 6. Capacitance vs. Reverse Voltage

# 4

# **Typical Performance**



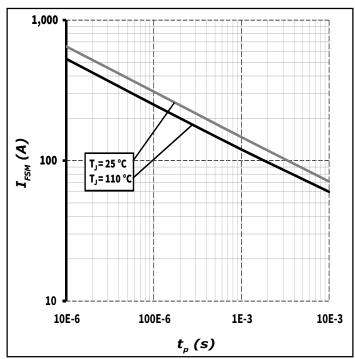


Figure 7. Capacitance Stored Energy

Figure 8. Non-Repetitive Peak Forward Surge Current Versus Pulse Duration (Sinusoidal Waveform)

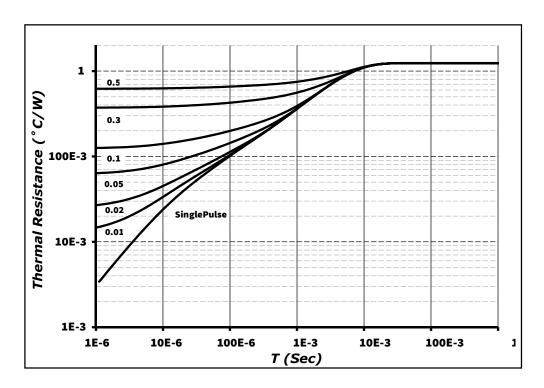
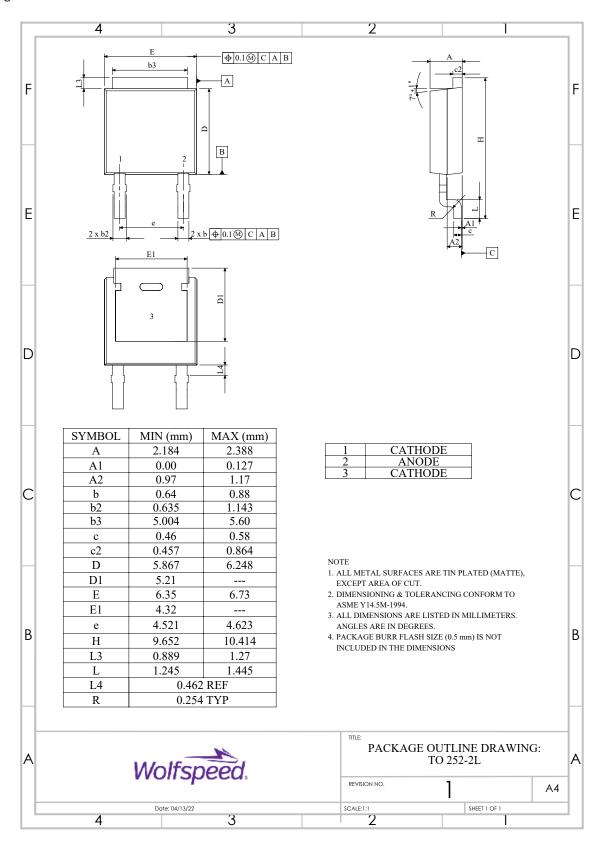


Figure 9. Transient Thermal Impedance

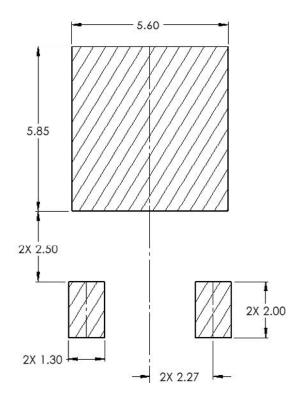
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# **Package Dimensions**

Package: TO-252-2



### **Recommended Solder Pad Layout**



Part Number	Package	Marking
C3D08065E	TO-252-2	C3D08065

### **Diode Model**

$$\begin{array}{c|c} & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & \\ & & & \\ & &$$

$$Vf_T = V_T + If * R_T$$

$$V_T = 0.95 + (T_J * -1.2*10^{-3})$$
  
 $R_T = 0.054 + (T_J * 5.5*10^{-4})$ 

Note: T<sub>j</sub> = Diode Junction Temperature In Degrees Celsius, valid from 25°C to 175°C

# **Revision History**

Current Revision	Date of Release	Description of Changes
5	September-2023	Updated Wolfspeed branding, package drawing, and solder pad lay- out, Removed AEC-Q101 banner
6	October-2023	Corrected solder pad layout and diode model

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#### **Contact info:**

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