

N-Channel Power MOSFET

FEATURES

- Latest super-junction technology
- Low gate charge capacitance
- High gate noise immunity
- RoHS compliant
- Halogen-free

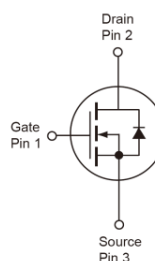
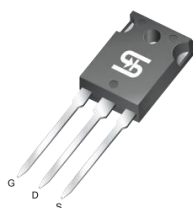
APPLICATIONS

- Switching applications
- HV motor driver
- Industrial

KEY PERFORMANCE PARAMETERS		
PARAMETER	VALUE	UNIT
$V_{DS} @ T_{j,max}$	650	V
$R_{DS(on)} (max)$	69	mΩ
$Q_{g,typ}$	86	nC



TO-247



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise noted)			
PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V_{DS}	600	V
Gate-Source Voltage	V_{GS}	± 30	V
Continuous Drain Current	I_D	51	A
Pulsed Drain Current (Note 1)	I_{DM}	204	A
Total Power Dissipation @ $T_C = 25^\circ\text{C}$	P_D	417	W
Single Pulse Avalanche Energy (Note 2)	E_{AS}	780	mJ
Single Pulse Avalanche Current (Note 2)	I_{AS}	5.6	A
Operating Junction and Storage Temperature Range	T_J, T_{STG}	- 55 to +150	$^\circ\text{C}$

THERMAL PERFORMANCE			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction to Case Thermal Resistance	$R_{\theta JC}$	0.3	$^\circ\text{C/W}$
Junction to Ambient Thermal Resistance (Note 3)	$R_{\theta JA}$	50	$^\circ\text{C/W}$

Notes:

1. Pulse Width $\leq 100\mu\text{s}$.
2. $L = 50\text{mH}$, $R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$.
3. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistances. $R_{\theta JA}$ is guaranteed by design while $R_{\theta JA}$ is determined by the user's board design.

ELECTRICAL SPECIFICATIONS (T _A = 25°C unless otherwise noted)						
PARAMETER	CONDITIONS	SYMBOL	MIN	TYP	MAX	UNIT
Static (Note 4)						
Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 1mA	BV _{DSS}	600	--	--	V
Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 3.5mA	V _{GS(TH)}	4	4.8	6	V
Gate Body Leakage	V _{GS} = ±30V, V _{DS} = 0V	I _{GSS}	--	--	±100	nA
Zero Gate Voltage Drain Current	V _{DS} = 600V, V _{GS} = 0V	I _{DSS}	--	--	100	μA
Drain-Source On-State Resistance	V _{GS} = 10V, I _D = 17A	R _{DS(on)}	--	55	69	mΩ
	V _{GS} = 12V, I _D = 17A		--	52	60	
Dynamic (Note 5)						
Total Gate Charge	V _{DS} = 480V, I _D = 51A, V _{GS} = 10V	Q _g	--	86	--	nC
Gate-Source Charge		Q _{gs}	--	27	--	
Gate-Drain Charge		Q _{gd}	--	50	--	
Input Capacitance	V _{DS} = 300V, V _{GS} = 0V, f = 100kHz	C _{iSS}	--	3566	--	pF
Output Capacitance		C _{oSS}	--	99	--	
Reverse Transfer Capacitance		C _{rSS}	--	6	--	
Gate Resistance	f = 1.0Hz	R _g	--	0.9	--	Ω
Switching (Note 6)						
Turn-On Delay Time	V _{DD} = 300V, R _G = 3.3Ω, I _D = 51A, V _{GS} = 10V	t _{d(on)}	--	49	--	ns
Turn-On Rise Time		t _r	--	66	--	
Turn-Off Delay Time		t _{d(off)}	--	80	--	
Turn-Off Fall Time		t _f	--	37	--	
Source-Drain Diode						
Forward Voltage (Note 4)	I _S = 17A, V _{GS} = 0V	V _{SD}	--	0.9	1.5	V
Reverse Recovery Time	I _S = 25.5A	t _{rr}	--	465	--	ns
Reverse Recovery Charge	dI _F /dt = 100A/μs	Q _{rr}	--	10	--	μC

Notes:

- Pulse test: Pulse Width $\leq 300\mu s$, duty cycle $\leq 2\%$.
- Defined by design. Not subject to production test.
- Switching time is essentially independent of operating temperature.

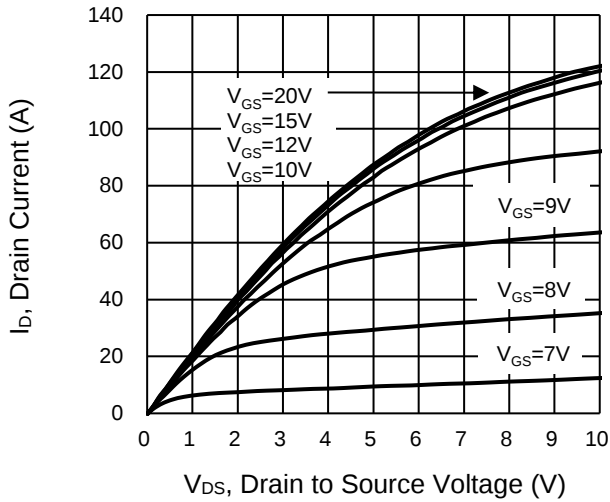
ORDERING INFORMATION

ORDERING CODE	PACKAGE	PACKING
TSM60NE069PW C0G	TO-247	30pcs / Tube

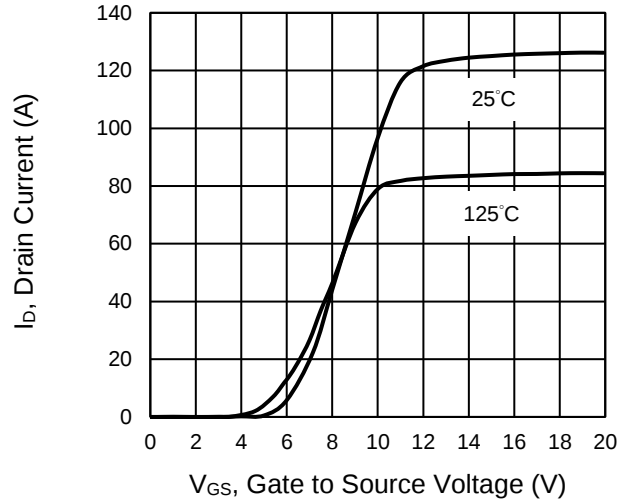
CHARACTERISTICS CURVES

($T_c = 25^\circ\text{C}$ unless otherwise noted)

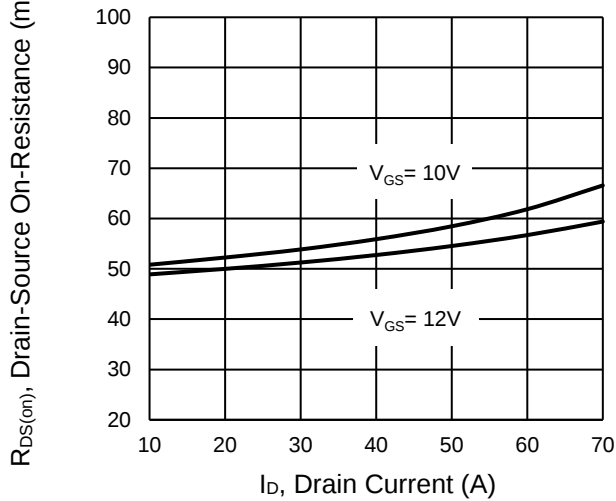
Output Characteristics



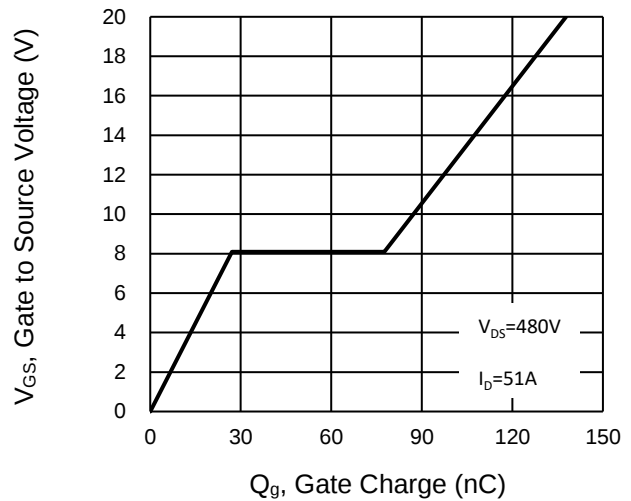
Transfer Characteristics



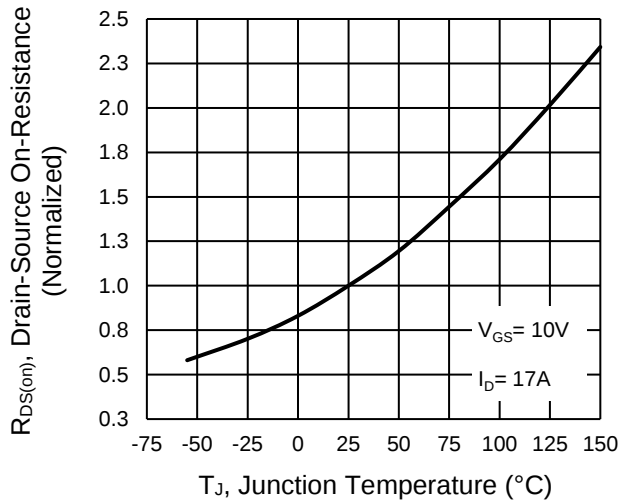
On-Resistance vs. Drain Current



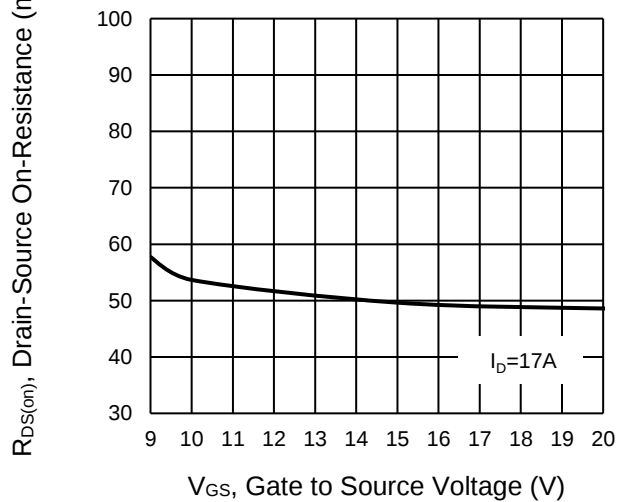
Gate-Source Voltage vs. Gate Charge



On-Resistance vs. Junction Temperature

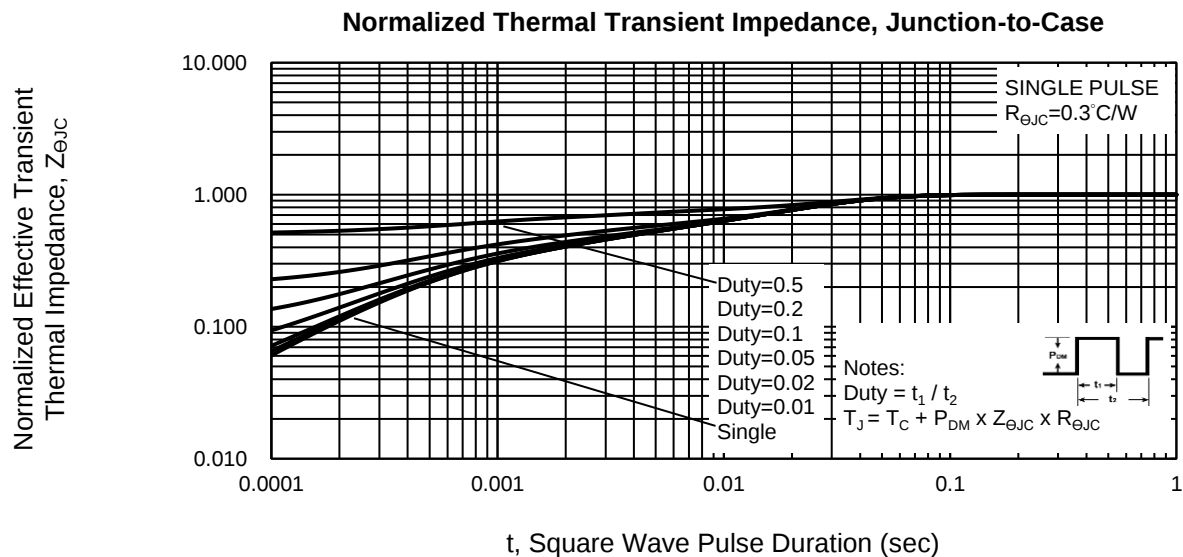
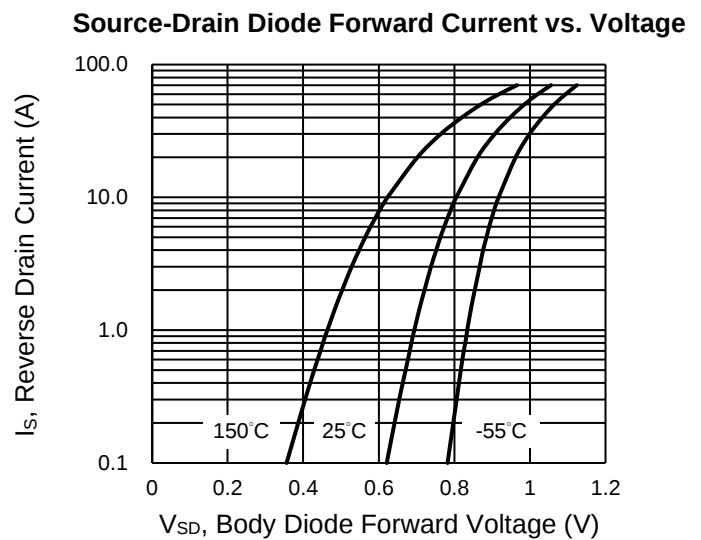
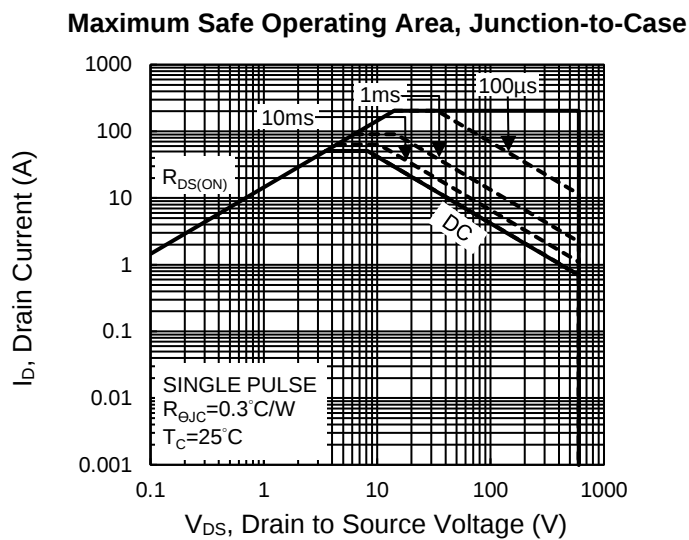
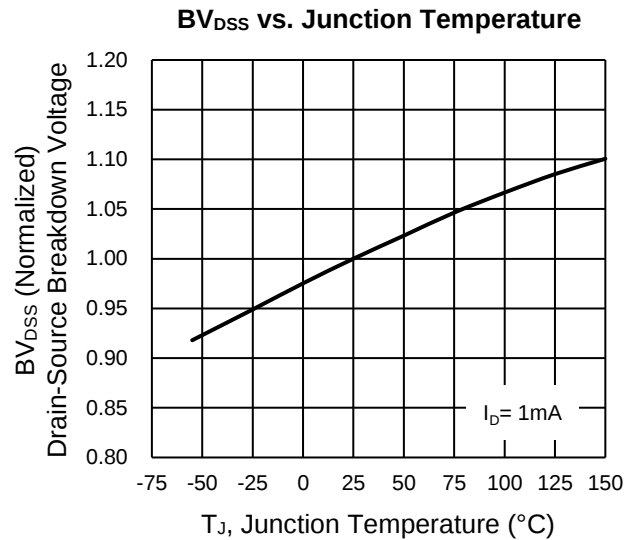
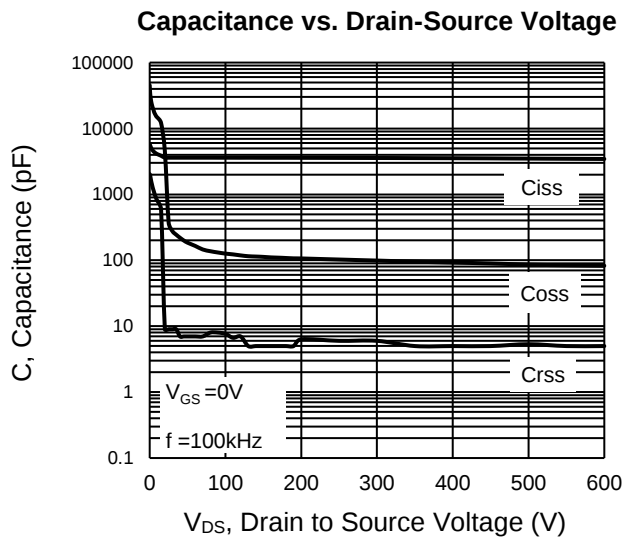


On-Resistance vs. Gate-Source Voltage



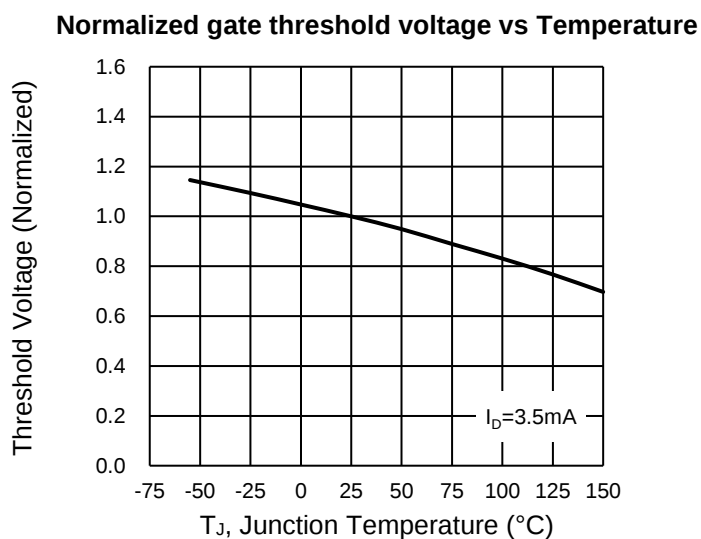
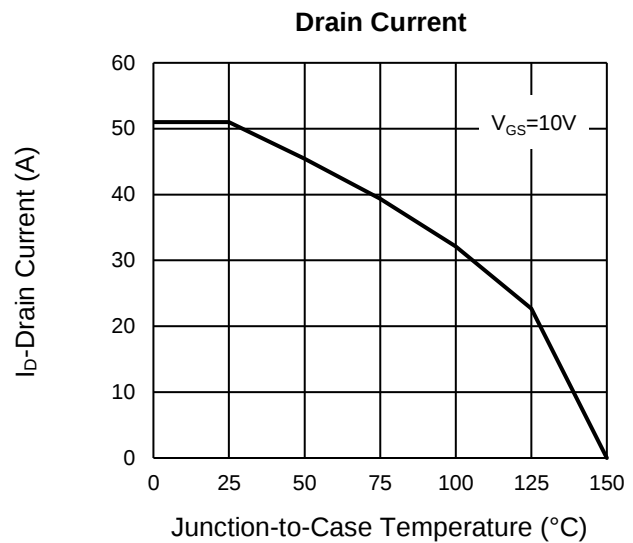
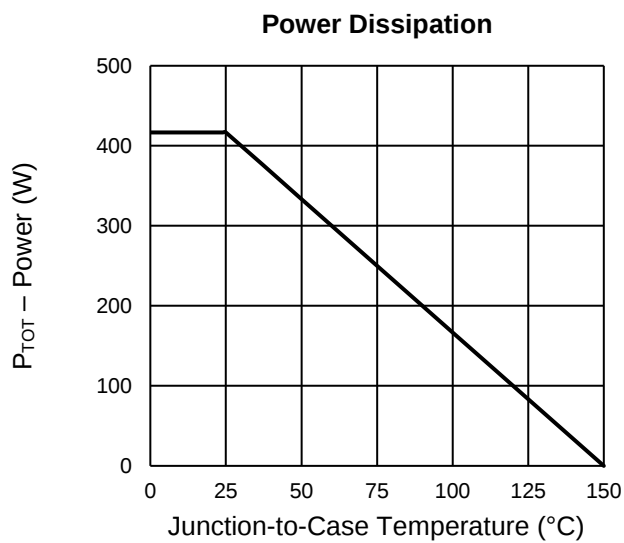
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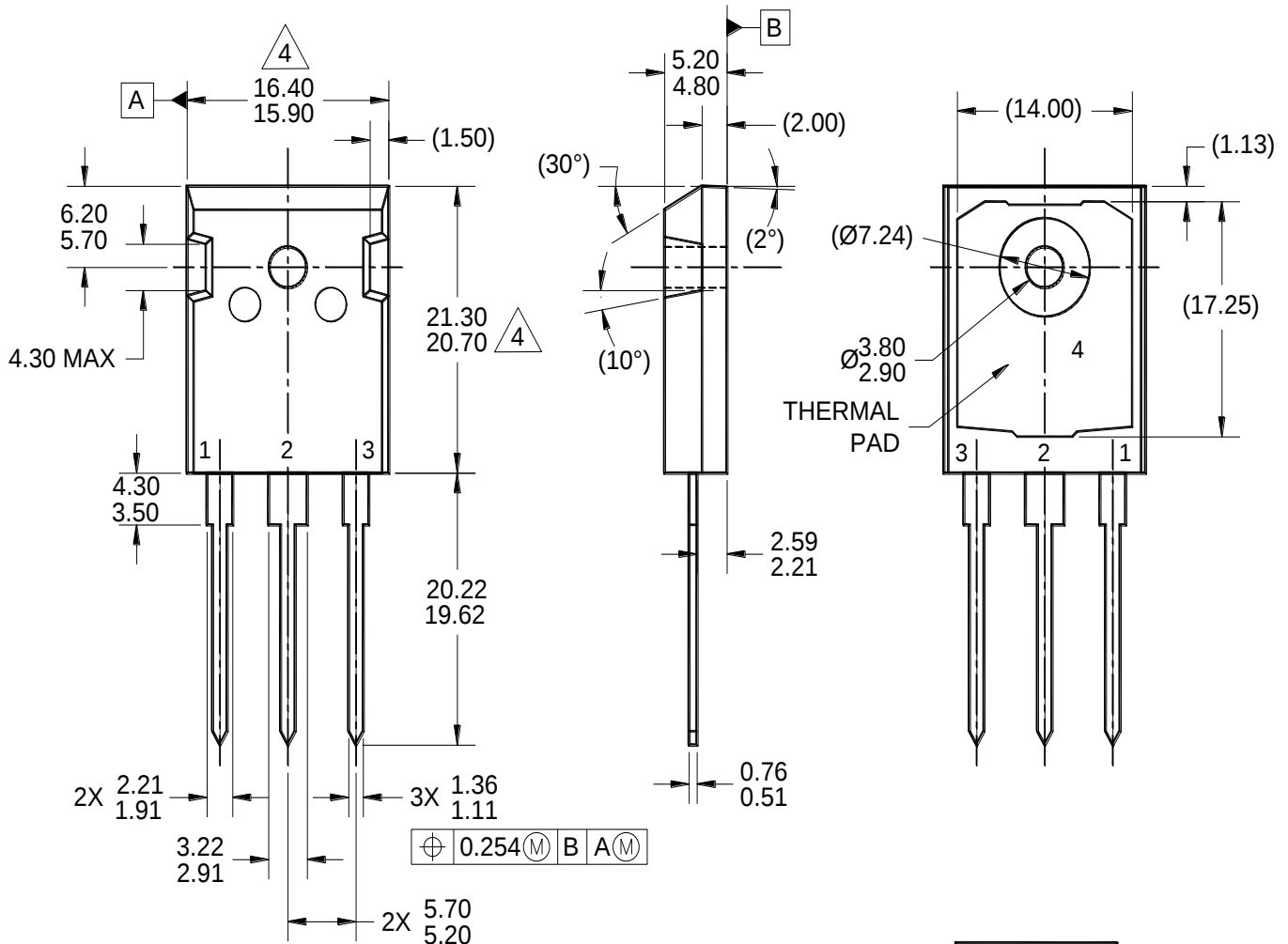
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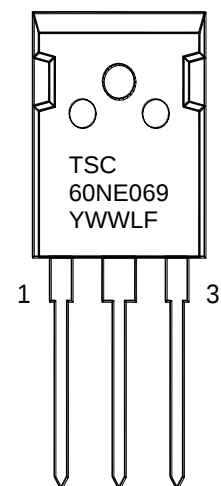
PACKAGE OUTLINE DIMENSIONS (Unit: Millimeters)

TO-247



NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL DIMENSIONS ARE IN MILLIMETERS.
2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.
3. PACKAGE OUTLINE REFERENCE: JEDEC TO-247, VARIATION AD, ISSUE E.
4. MOLDED PLASTIC BODY DIMENSIONS DO NOT INCLUDE MOLD FLASH. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREME OF THE PLASTIC BODY.
5. DWG NO. REF: HQ2SD07-TO247AD-071 REV C.



MARKING DIAGRAM

- Y = YEAR CODE
WW = WEEK CODE (01~52)
L = LOT CODE (1~9, A~Z)
F = FACTORY CODE

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