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SSD1963

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Advance Information

1215KB Embedded Display SRAM LCD Display Controller

SSD1963 is a display controller of 1215K byte frame buffer to support up to 864 x 480 x 24bit graphics content. It also equips parallel MCU interfaces in different bus width to receive graphics data and command from MCU. Its display interface supports common RAM-less LCD driver of color depth up to 24 bit-per-pixel.

[Features](#)

- Display feature
 - Built-in 1215K bytes frame buffer. Support up to 864 x 480 at 24bpp display
 - Support TFT 18/24-bit generic RGB interface panel
 - Support 8-bit serial RGB interface
 - Hardware rotation of 0, 90, 180, 270 degree
 - Hardware display mirroring
 - Hardware windowing
 - Programmable brightness, contrast and saturation control
 - Dynamic Backlight Control (DBC) via PWM signal
- MCU connectivity
 - 8/9/16/18/24-bit MCU interface
 - Tearing effect signal
- I/O Connectivity
 - 4 GPIO pins
- Built-in clock generator
- Deep sleep mode for power saving
- Core supply power (VDDPLL and VDDD): 1.2V±0.1V
- I/O supply power (VDDIO): 1.65V to 3.6V
- LCD interface supply power (VDDLCD): 1.65V to 3.6V

[Ordering Information](#)

Ordering Part No.	Package
SSD1963G41	TFBGA-80
SSD1963QL9	LQFP-128
SSD1963QL9R	LQFP-128

Contact our sales representative for more information:

Tel: (852) 2207 1111

sales@solomon-systech.com

or

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