AUTOMOTIVE

RoHS

COMPLIANT HALOGEN

FREE

GREEN (5-2008)



Vishay Semiconductors

Silicon NPN Phototransistor



VEMT2023SLX01 is a silicon NPN epitaxial planar phototransistor in a miniature side looking, surface mount

package (SMD) with dome lens and daylight blocking filter.

Filter bandwidth is matched with 830 nm to 950 nm IR

FEATURES

- Package type: surface mount
- · Package form: side view
- Dimensions (L x W x H in mm): 2.3 x 2.55 x 2.3
- AEC-Q101 qualified
- · High radiant sensitivity
- Daylight blocking filter matched with 830 nm to 950 nm IR emitters
- Fast response times
- Angle of half sensitivity: $\varphi = \pm 35^{\circ}$
- Package matched with IR emitter series VSMB2943SLX01
- Floor life: 4 weeks, MSL 2a, acc. J-STD-020
- Lead (Pb)-free reflow soldering
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912



- · Detector in automotive applications
- · Photo interrupters
- · Miniature switches
- Counters
- Encoders
- · Position sensors

PRODUCT SUMMARY				
COMPONENT	I _{ca} (mA)	φ (deg)	λ _{0.5} (nm)	
VEMT2023SLX01	2.7	± 35	790 to 970	

Note

DESCRIPTION

emitters.

• Test condition see table "Basic Characteristics"

ORDERING INFORMATION				
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM	
VEMT2023SLX01	Tape and reel	MOQ: 3000 pcs, 3000 pcs/reel	Side view	

Note

MOQ: minimum order quantity

ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000



ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Collector emitter voltage		V _{CEO}	20	V
Emitter collector voltage		V _{ECO}	7	V
Collector current		I _C	50	mA
Power power dissipation	T _{amb} ≤ 75 °C	Pv	100	mW
Junction temperature		Tj	100	°C
Operating temperature range		T _{amb}	- 40 to + 100	°C
Storage temperature range		T _{stg}	- 40 to + 100	°C
Soldering temperature	Acc. reflow profile fig. 8	T _{sd}	260	°C
Thermal resistance junction/ambient	Acc. J-STD-051	R _{thJA}	250	K/W

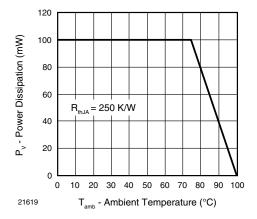


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

BASIC CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Collector emitter breakdown voltage	I _C = 0.1 mA	V _{CEO}	20			V
Collector dark current	$V_{CE} = 5 \text{ V}, E = 0$	I _{CEO}		1	100	nA
Collector emitter capacitance	$V_{CE} = 0 \text{ V, } f = 1 \text{ MHz, } E = 0$	C _{CEO}		25		pF
Collector light current	E_e = 1 mW/cm ² , λ = 950 nm, V_{CE} = 5 V	I _{ca}	1.3	2.7	4.1	mA
Angle of half sensitivity		φ		± 35		deg
Wavelength of peak sensitivity		λ_{p}		860		nm
Range of spectral bandwidth		λ _{0.5}		790 to 970		nm
Collector emitter saturation voltage	$I_{\rm C} = 0.05 \; {\rm mA}$	V _{CEsat}			0.4	V
Temperature coefficient of Ica	E_e = 1 mW/cm ² , λ = 950 nm, V_{CE} = 5 V	Tk _{lca}		1.1		%/K

BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

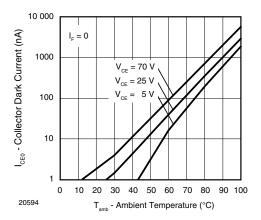


Fig. 2 - Collector Dark Current vs. Ambient Temperature

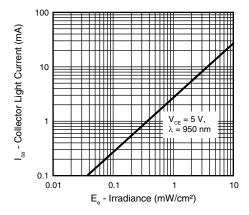


Fig. 3 - Collector Light Current vs. Irradiance

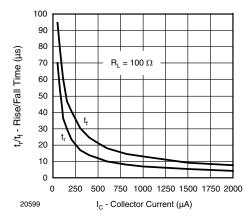


Fig. 4 - Rise/Fall Time vs. Collector Current

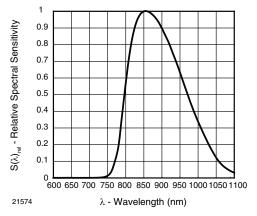


Fig. 5 - Relative Spectral Sensitivity vs. Wavelength

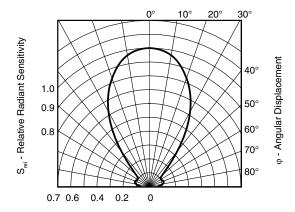


Fig. 6 - Relative Radiant Sensitivity vs. Angular Displacement

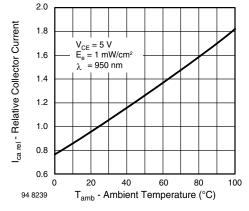


Fig. 7 - Relative Collector Current vs. Ambient Temperature



REFLOW SOLDER PROFILE

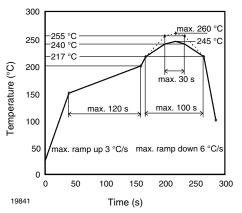


Fig. 8 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020

DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 4 weeks

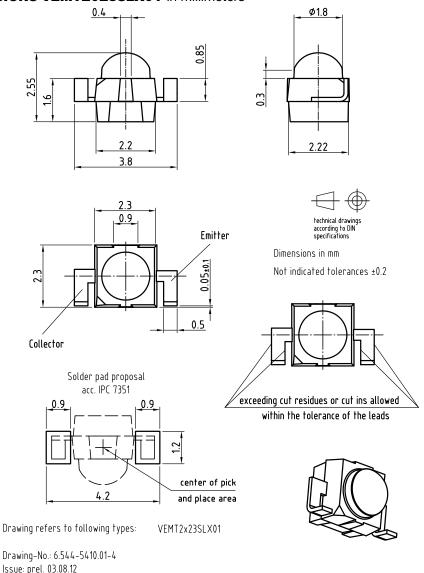
Conditions: T_{amb} < 30 °C, RH < 60 %

Moisture sensitivity level 2a, acc. to J-STD-020.

DRYING

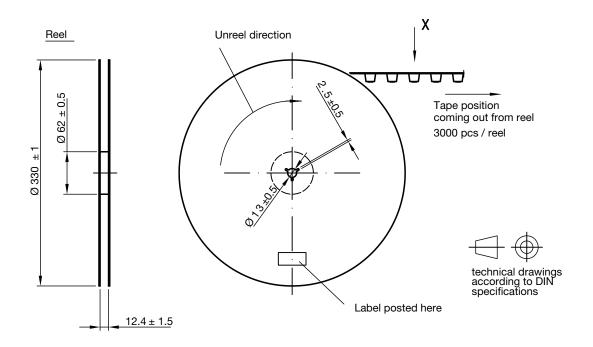
In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 $^{\circ}$ C (+ 5 $^{\circ}$ C), RH < 5 $^{\circ}$ M.

PACKAGE DIMENSIONS VEMT2023SLX01 in millimeters

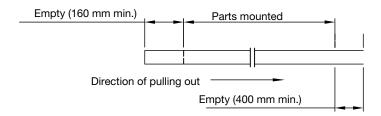


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TAPE AND REEL DIMENSIONS VEMT2023SLX01 in millimeters

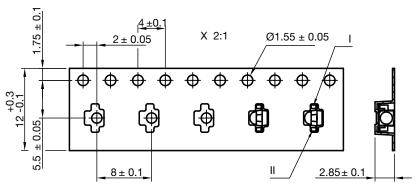


Leader and tailer tape:



Terminal position in tape

Device	Lead I	Lead II	
VSMB2943SLX01			
VSMF2893SLX01	0-464-	A	
VSMB2948SL	Cathode	Anode	
VEMD2023SLX01			
VEMD2523SLX01			
VEMT2023SLX01	Collector	Emitter	
VEMT2523SLX01	Collector	Emiller	
VSMY2853SL	Anode	Cathode	



Drawing refers to following types: see table

Reel dimensions and tape

Drawing-No.: 9.800-5123.01-4

Issue: 2; 19.02.13

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