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Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (_), the underscore (_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.onsemi.com. Please email any questions regarding the system integration to Fairchild_questions@onsemi.com.

FAIRCHILD SEMICONDUCTOR®

February 2007

FDMS3672
N-Channel UltraFET Trench MOSFET
100V, 22A, 23mΩ

Features

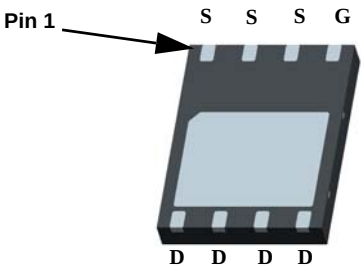
- Max $r_{DS(on)}$ = 23mΩ at V_{GS} = 10V, I_D = 7.4A
- Max $r_{DS(on)}$ = 29mΩ at V_{GS} = 6V, I_D = 6.6A
- Typ Qg = 31nC at V_{GS} = 10V
- Low Miller Charge
- Optimized efficiency at high frequencies
- RoHS Compliant

General Description

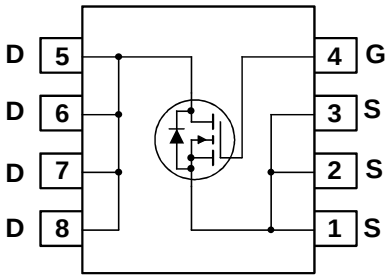
UltraFET devices combine characteristics that enable benchmark efficiency in power conversion applications. Optimized for $r_{DS(on)}$, low ESR, low total and Miller gate charge, these devices are ideal for high frequency DC to DC converters.

Application

- DC - DC Conversion



Power56 (Bottom view)



MOSFET Maximum Ratings T_A = 25°C unless otherwise noted

Symbol	Parameter			Ratings	Units
V_{DS}	Drain to Source Voltage			100	V
V_{GS}	Gate to Source Voltage			±20	V
I_D	Drain Current -Continuous (Package limited)	T_C = 25°C		22	A
	-Continuous (Silicon limited)	T_C = 25°C		41	
	-Continuous	T_A = 25°C	(Note 1a)	7.4	
	-Pulsed			30	
P_D	Power Dissipation	T_C = 25°C		78	W
	Power Dissipation	T_A = 25°C	(Note 1a)	2.5	
T_J, T_{STG}	Operating and Storage Junction Temperature Range			-55 to +150	°C

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance, Junction to Case	1.6	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Note 1a)	50	

Package Marking and Ordering Information

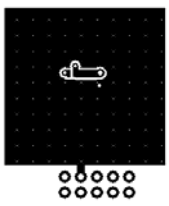
Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDMS3672	FDMS3672	Power 56	13”	12mm	3000 units

Electrical Characteristics T_J = 25°C unless otherwise noted

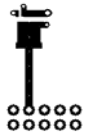
Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain to Source Breakdown Voltage	I_D = 250μA, V_{GS} = 0V	100			V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	I_D = 250μA, referenced to 25°C		104		mV/°C
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 80V, V_{GS} = 0V			1	μA
		V_{DS} = 80V, V_{GS} = 0V, T_J = 55°C			10	μA
I_{GSS}	Gate to Source Leakage Current	V_{GS} = ±20V, V_{DS} = 0V			±100	nA
On Characteristics						
$V_{GS(th)}$	Gate to Source Threshold Voltage	V_{GS} = V_{DS}, I_D = 250μA	2	3.1	4	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate to Source Threshold Voltage Temperature Coefficient	I_D = 250μA, referenced to 25°C		-11		mV/°C
$r_{DS(on)}$	Drain to Source On Resistance	V_{GS} = 10V, I_D = 7.4A		19	23	mΩ
		V_{GS} = 6V, I_D = 6.6A		24	29	mΩ
		V_{GS} = 10V, I_D = 7.4A, T_J = 125°C		33	40	mΩ
g_{FS}	Forward Transconductance	V_{DS} = 10V, I_D = 7.4A		20		S
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS} = 50V, V_{GS} = 0V, f = 1MHz		2015	2680	pF
C_{oss}	Output Capacitance			210	280	pF
C_{rss}	Reverse Transfer Capacitance			90	135	pF
R_g	Gate Resistance	f = 1MHz		1.3		Ω
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	V_{DD} = 50V, I_D = 7.4A, V_{GS} = 10V, R_{GEN} = 6Ω		23	37	ns
t_r	Rise Time			11	20	ns
$t_{d(off)}$	Turn-Off Delay Time			36	58	ns
t_f	Fall Time			8	16	ns
Q_g	Total Gate Charge at 10V	V_{GS} = 0V to 10V, V_{DD} = 50V, I_D = 7.4A		31	44	nC
Q_g	Total Gate Charge at 4.5V	V_{GS} = 0V to 4.5V, V_{DD} = 50V, I_D = 7.4A				nC
Q_{gs}	Gate to Source Gate Charge	V_{DD} = 50V, I_D = 7.4A		9.5		nC
Q_{gd}	Gate to Drain “Miller” Charge	V_{DD} = 50V, I_D = 7.4A		8		nC
Drain-Source Diode Characteristics						
V_{SD}	Source to Drain Diode Forward Voltage	V_{GS} = 0V, I_S = 7.4A (Note 2)		0.8	1.2	V
t_{rr}	Reverse Recovery Time	I_F = 7.4A, di/dt = 100A/μs		52	78	ns
Q_{rr}	Reverse Recovery Charge	I_F = 7.4A, di/dt = 100A/μs		101	152	nC

Notes:

1: $R_{\theta JA}$ is determined with the device mounted on a 1in^2 pad 2 oz copper pad on a 1.5 x 1.5 in. board of FR-4 material. $R_{\theta JC}$ is guaranteed by design while $R_{\theta CA}$ is determined by the user's board design.



a. 50°C/W when mounted on a 1 in^2 pad of 2 oz copper



b. 125°C/W when mounted on a minimum pad of 2 oz copper

2: Pulse Test: Pulse Width < 300μs, Duty cycle < 2.0%.

Typical Characteristics T_J = 25°C unless otherwise noted

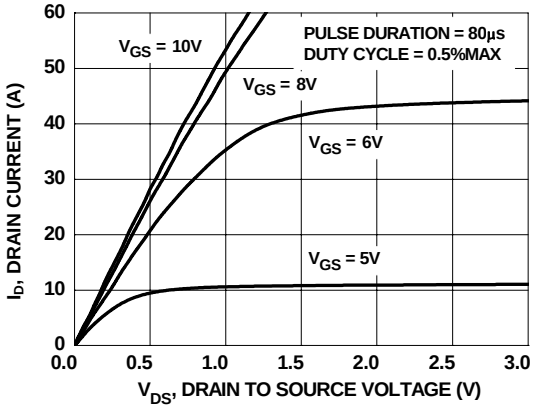


Figure 1. On-Region Characteristics

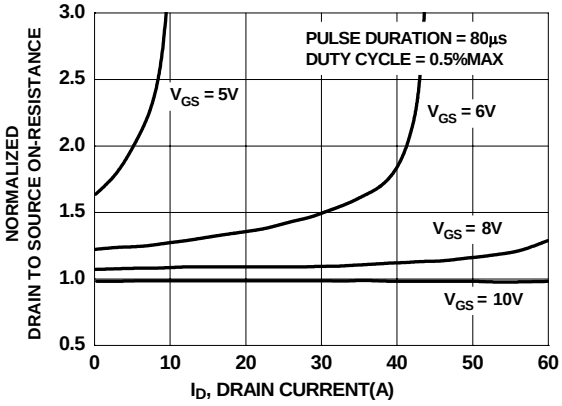


Figure 2. Normalized On-Resistance vs Drain Current and Gate Voltage

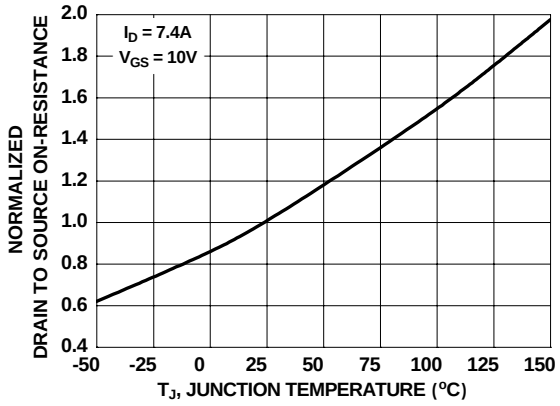


Figure 3. Normalized On-Resistance vs Junction Temperature

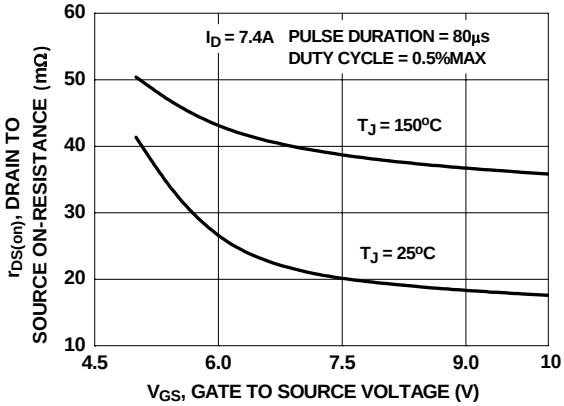


Figure 4. On-Resistance vs Gate to Source Voltage

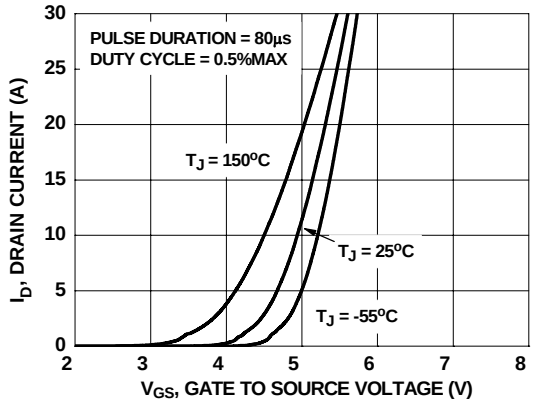


Figure 5. Transfer Characteristics

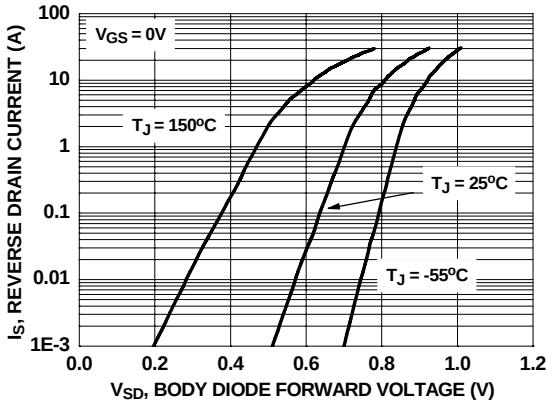


Figure 6. Source to Drain Diode Forward Voltage vs Source Current

Typical Characteristics T_J = 25°C unless otherwise noted

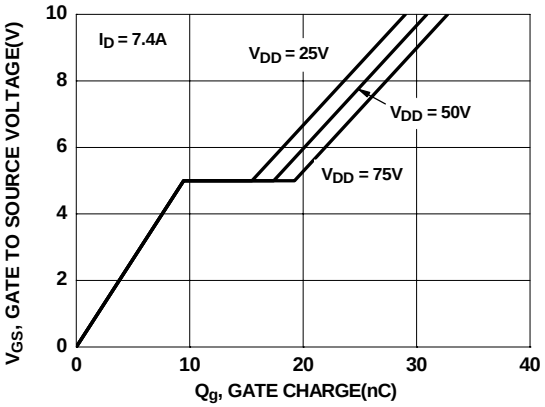


Figure 7. Gate Charge Characteristics

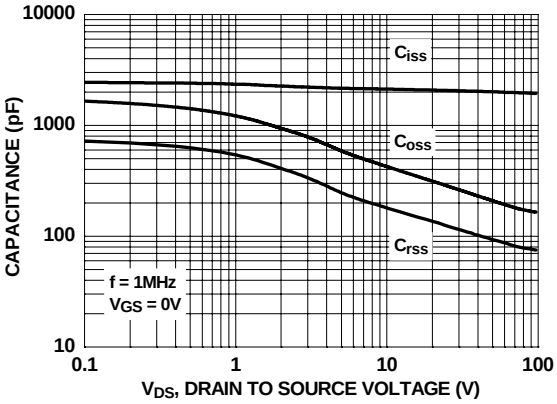


Figure 8. Capacitance vs Drain to Source Voltage

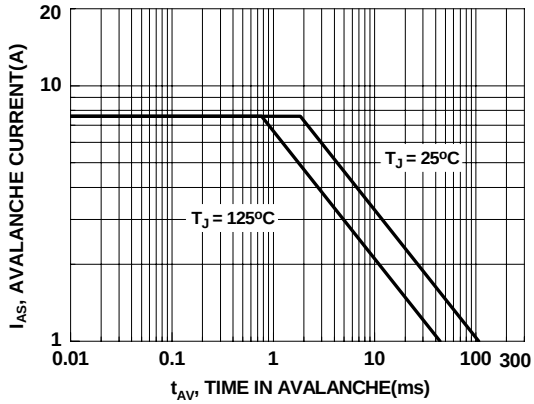


Figure 9. Unclamped Inductive Switching Capability

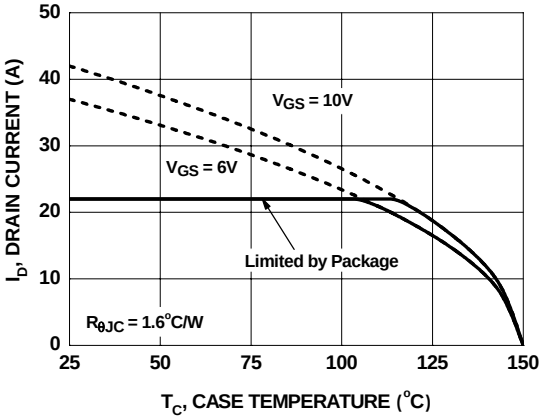


Figure 10. Maximum Continuous Drain Current vs Case Temperature

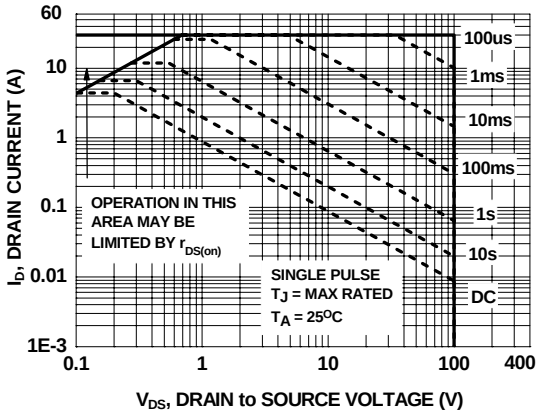


Figure 11. Forward Bias Safe Operating Area

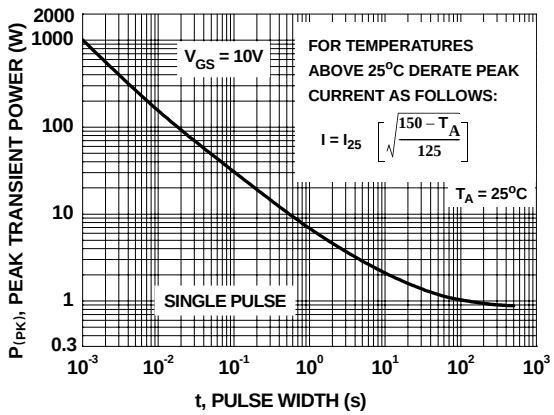


Figure 12. Single Pulse Maximum Power Dissipation

Typical Characteristics T_J = 25°C unless otherwise noted

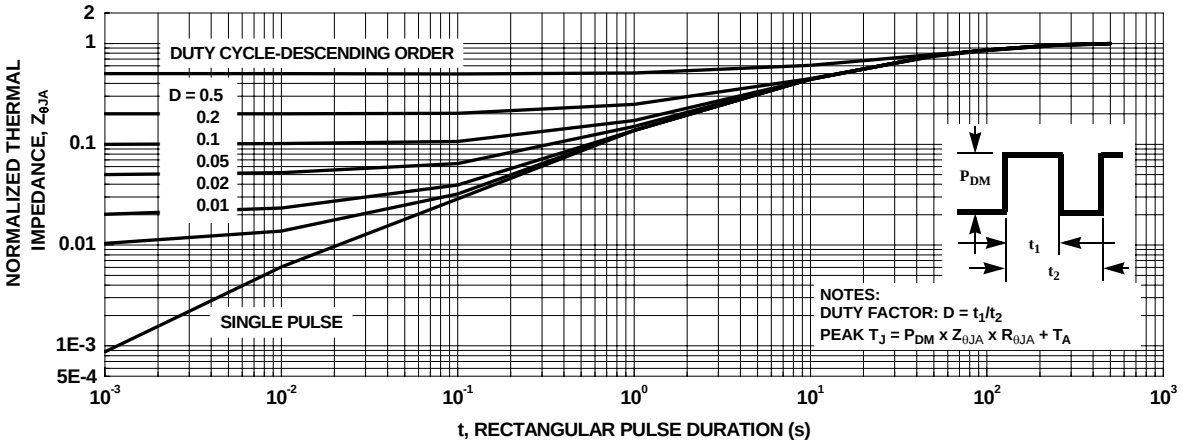
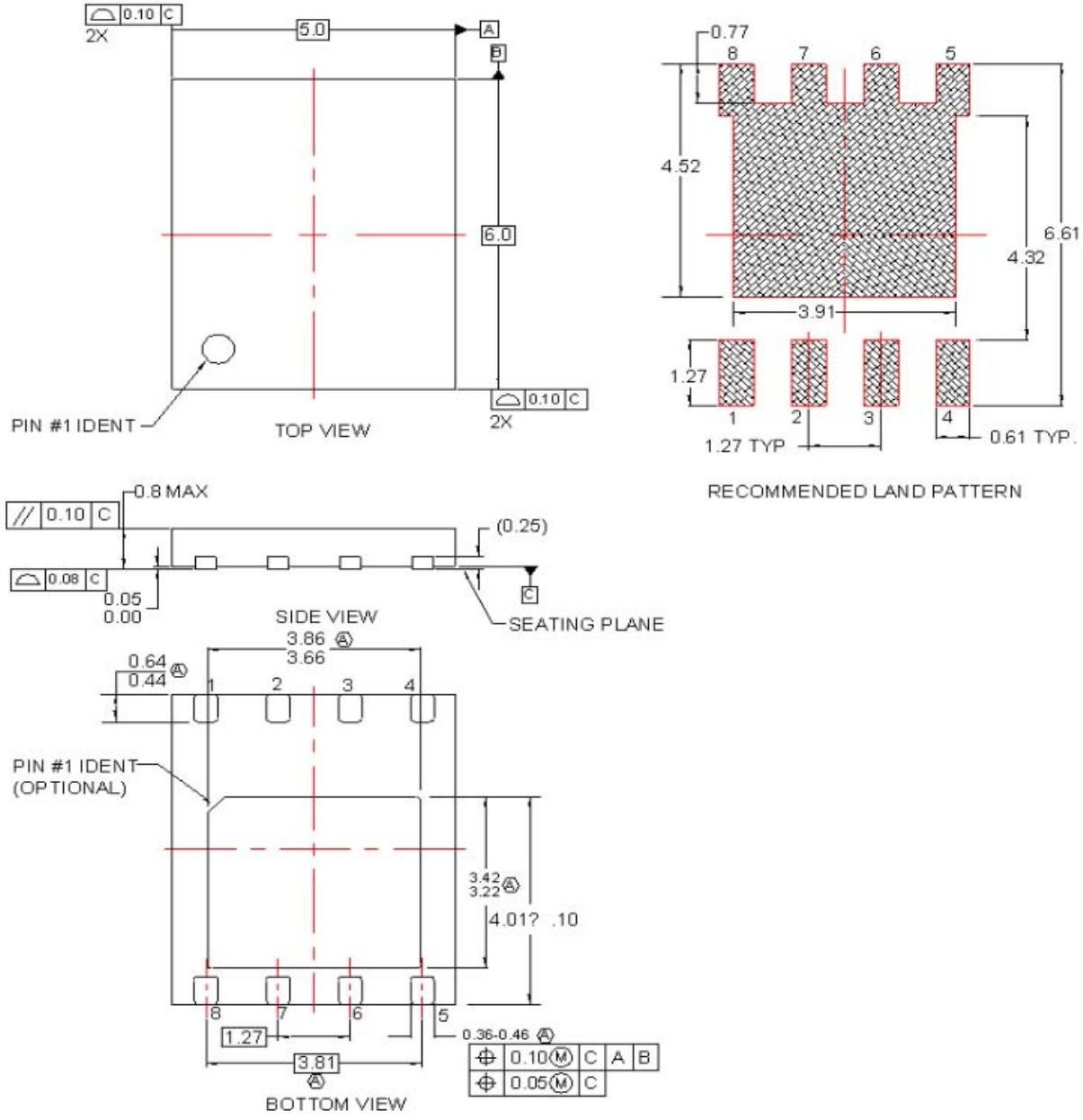


Figure 13. Transient Thermal Response Curve



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C. DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 1994

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