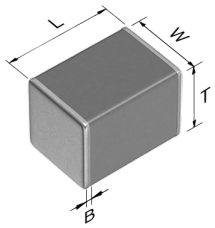
# Multilayer Ceramic Chip Capacitors

## C4532C0G2A104J320KA

| | |
|---|---|
| **Product Status** | Production |
| **TDK Item Description** | C4532C0G2A104JT**** |
| **Applications** | Commercial Grade<br>Please refer to Part No. CGA8R2C0G2A104J320KA for Automotive use. |
| **Feature** | MidMid Voltage (100 to 630V) |
| **Series** | C4532 [EIA 1812] |
| **Status** | Production |
| **Brand** | TDK |
| **Environmental Compliance** | RoHS, REACH, Halogen Free, Lead Free |




| Size | |
|---|---|
| Length(L) | 4.50mm ±0.40mm |
| Width(W) | 3.20mm ±0.40mm |
| Thickness(T) | 3.20mm ±0.30mm |
| Terminal Width(B) | 0.20mm Min. |
| Recommended Land Pattern (PA) | 3.10mm to 3.70mm |
| Recommended Land Pattern (PB) | 1.20mm to 1.40mm |
| Recommended Land Pattern (PC) | 2.40mm to 3.20mm |

| Electrical Characteristics | |
|---|---|
| Capacitance | 100nF ±5% |
| Rated Voltage | 100VDC |
| Temperature Characteristic | C0G(0±30ppm/°C) |
| Q (Min.) | 1000 |
| Insulation Resistance (Min.) | 5000MΩ |
| Allowable Voltage | |
| Allowable Current | |

| Other | |
|---|---|
| Operating Temp. Range | -55 to 125°C |
| Soldering Method | Reflow |
| AEC-Q200 | NO |
| Packing | Embossed (Plastic)Taping [180mm Reel] |
| Package Quantity | 500pcs |
| FIT (Failure In Time) | This will be displayed when you login to myTDK |

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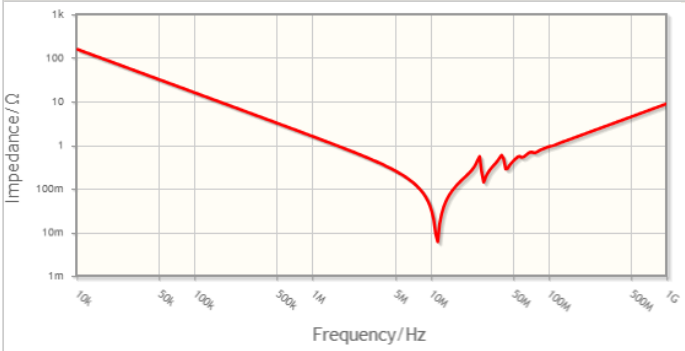
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# Characteristic Graphs(This is reference data, and does not guarantee the products characteristics.)

## Impedance



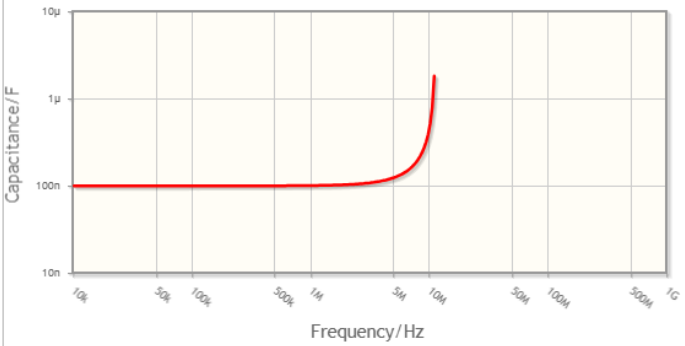

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## ESR



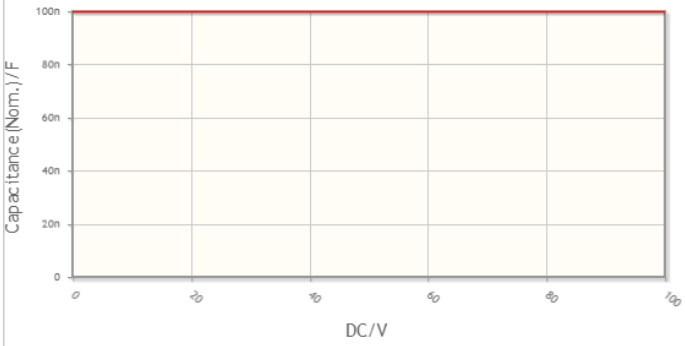

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## Capacitance



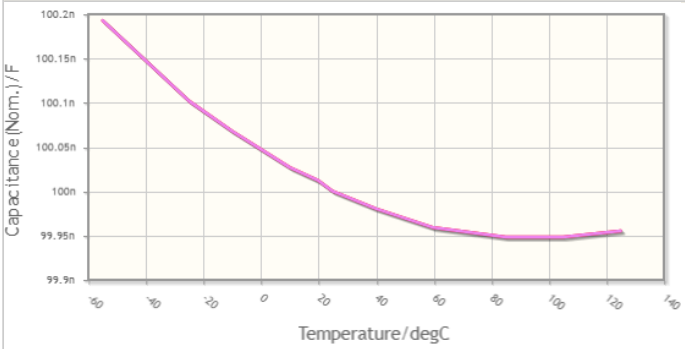

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## DC Bias Characteristic



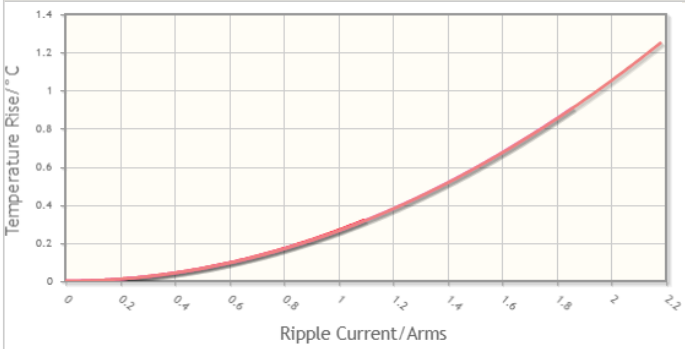

C4532C0G2A104J320KA

## Temperature Characteristic




C4532C0G2A104J320KA(No Bias) C4532C0G2A104J320KA(DC Bias = 50V )

## Ripple Temperature Rising



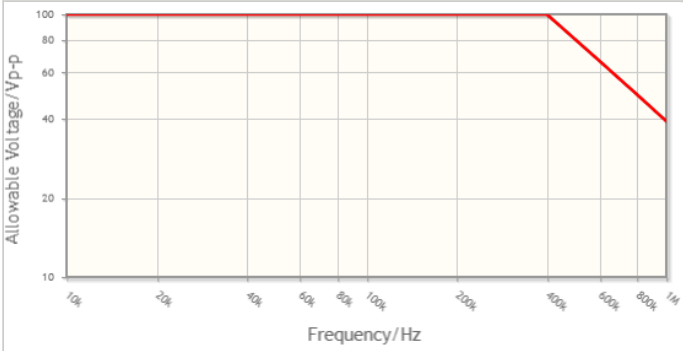

C4532C0G2A104J320KA(50kHz) C4532C0G2A104J320KA(85kHz) C4532C0G2A104J320KA(100kHz)

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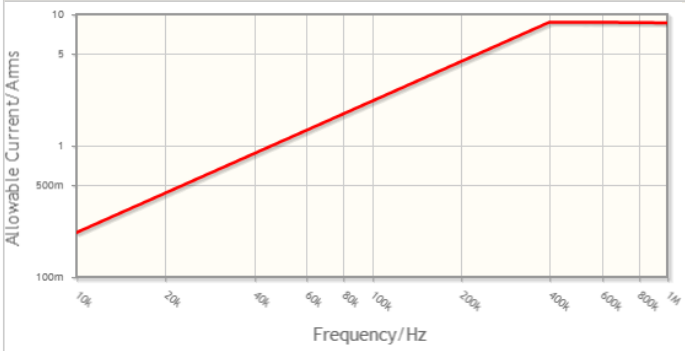
# Characteristic Graphs(This is reference data, and does not guarantee the products characteristics.)

**Max. Allowable Voltage**




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**Max. Allowable Current**




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## Associated Images

Land Pattern (Terminal Connection)




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