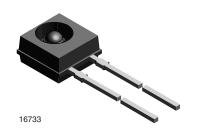
TEKT5400S

Vishay Semiconductors



Silicon NPN Phototransistor



DESCRIPTION

TEKT5400S is a silicon NPN phototransistor with high radiant sensitivity, molded in a plastic package with side view lens and daylight blocking filter. Filter bandwidth is matched with 950 nm IR emitters.

FEATURES

- Package type: leaded
- Package form: side view lens
- Dimensions (L x W x H in mm): 5 x 2.65 x 5
- High radiant sensitivity
- Daylight blocking filter matched with 940 nm emitters
- Fast response times
- Angle of half sensitivity: $\varphi = \pm 37^{\circ}$
- Package matched with IR emitter series TSKS5400S
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

Note

** Please see document "Vishay Material Category Policy": www.vishay.com/doc?99902

APPLICATIONS

• Detector in electronic control and drive circuits

PRODUCT SUMMARY				
COMPONENT	I _{ca} (mA)	φ (deg)	λ _{0.5} (nm)	
TEKT5400S	4	± 37	850 to 980	

Note

Test condition see table "Basic Characteristics"

ORDERING INFORMATION				
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM	
TEKT5400S	Bulk	MOQ: 2000 pcs, 2000 pcs/bulk	Side view lens	

Note

• MOQ: minimum order quantity

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Collector emitter voltage		V _{CEO}	70	V	
Emitter collector voltage		V _{ECO}	7	V	
Collector current		Ι _C	100	mA	
Collector peak current	$t_p/T \le 0.5, t_p \le 10 \text{ ms}$	I _{CM}	200	mA	
Power dissipation	$T_{amb} \le 40 \ ^{\circ}C$	Pv	150	mW	
Junction temperature		Тj	100	°C	
Operating temperature range		T _{amb}	- 40 to + 85	°C	
Storage temperature range		T _{stg}	- 40 to + 100	°C	
Soldering temperature	t ≤ 5 s	T _{sd}	260	°C	
Thermal resistance junction/ambient	J-STD-051, soldered on PCB	R _{thJA}	270	K/W	



(5-2008)**

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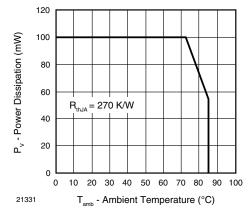


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

BASIC CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Collector emitter voltage	I _C = 1 mA	V _{CEO}	70			V
Emitter collector voltage	I _E = 100 μA	V _{ECO}	7			V
Collector dark current	$V_{CE} = 20 V, E = 0$	I _{CEO}		1	100	nA
Collector emitter capacitance	$V_{CE} = 5 V, f = 1 MHz, E = 0$	C _{CEO}		6		pF
Collector ligth current	E_e = 1 mW/cm ² , λ = 950 nm, V _{CE} = 5 V	I _{ca}	2	4		mA
Angle of half sensitivity		φ		± 37		deg
Wavelength of peak sensitivity		λ _p		920		nm
Range of spectral bandwidth		λ _{0.5}		850 to 980		nm
Collector emitter saturation voltage	$E_e = 1 \text{ mW/cm}^2$, $\lambda = 950 \text{ nm}$, $I_C = 0.1 \text{ mA}$	V _{CEsat}			0.3	V
Turn-on time	V_{S} = 5 V, I_{C} = 5 mA, R_{L} = 100 Ω	t _{on}		6		μs
Turn-off time	V_{S} = 5 V, I_{C} = 5 mA, R_{L} = 100 Ω	t _{off}		5		μs
Cut-off frequency	V_{S} = 5 V, I_{C} = 5 mA, R_{L} = 100 Ω	f _c		110		kHz

BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

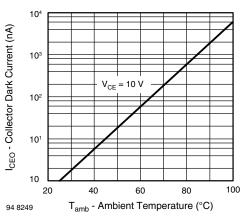


Fig. 1 - Collector Dark Current vs. Ambient Temperature

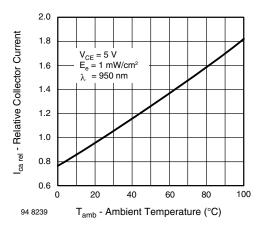


Fig. 2 - Relative Collector Current vs. Ambient Temperature

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2 For technical questions, contact: <u>detectortechsupport@vishay.com</u>

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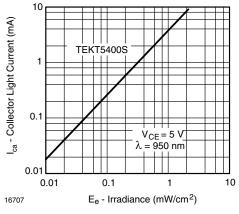


Fig. 3 - Collector Light Current vs. Irradiance

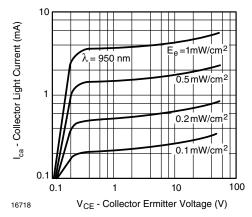


Fig. 4 - Collector Light Current vs. Collector Emitter Voltage

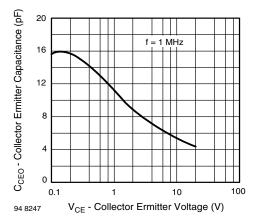


Fig. 5 - Collector Emitter Capacitance vs. Collector Emitter Voltage

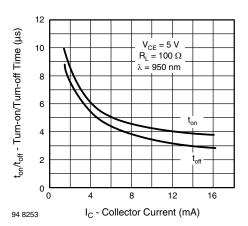


Fig. 6 - Turn-on/Turn-off Time vs. Collector Current

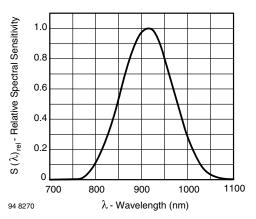


Fig. 7 - Relative Spectral Sensitivity vs. Wavelength

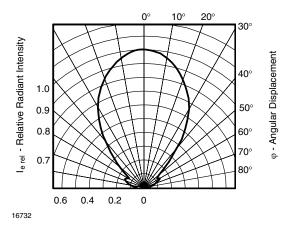


Fig. 8 - Relative Radiant Intensity vs. Angular Displacement

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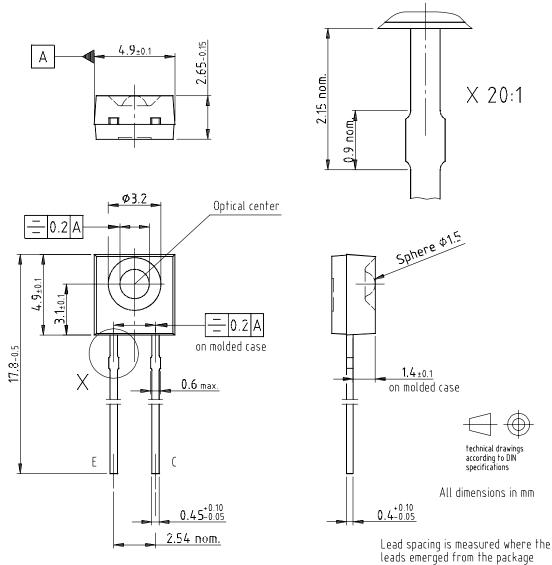
3 For technical questions, contact: <u>detectortechsupport@vishay.com</u> Document Number: 81569

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PACKAGE DIMENSIONS in millimeters



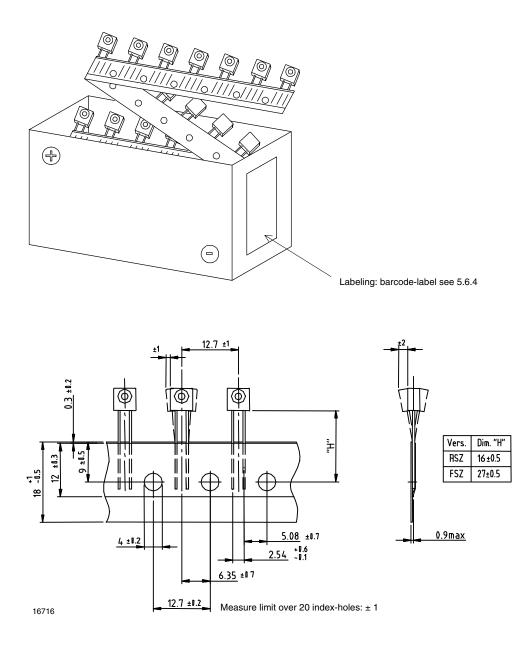
Drawing-No.: 6.544-5347.01-4 Issue: 2: 09.04.03

Protruded resin area where the leads emerged from the package 0.8 max.

16706



TAPE AND AMMOPACK STANDARDS Dimensions in millimeters



5

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