

# MOSFET

## 600V CoolMOS™ P6 Power Transistor

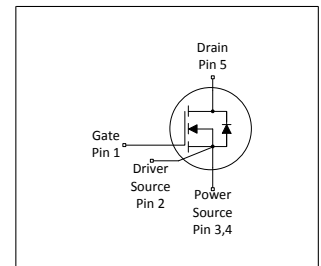
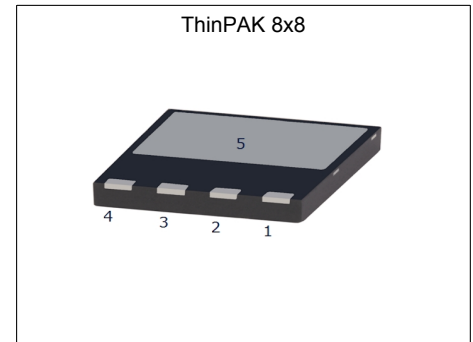
CoolMOS™ is a revolutionary technology for high voltage power MOSFETs, designed according to the superjunction (SJ) principle and pioneered by Infineon Technologies. CoolMOS™ P6 series combines the experience of the leading SJ MOSFET supplier with high class innovation. The offered devices provide all benefits of a fast switching SJ MOSFET while not sacrificing ease of use. Extremely low switching and conduction losses make switching applications even more efficient, more compact, lighter and cooler.

### Features

- Increased MOSFET  $dv/dt$  ruggedness
- Extremely low losses due to very low FOM  $R_{DS(on)} \cdot Q_g$  and  $E_{oss}$
- Very high commutation ruggedness
- Easy to use/drive
- Pb-free plating, Halogen free mold compound
- Qualified for industrial grade applications according to JEDEC (J-STD20 and JESD22)

### Potential applications

PFC stages, hard switching PWM stages and resonant switching stages for e.g. PC Silverbox, Adapter, LCD & PDP TV, Lighting, Server, Telecom and UPS.



**Table 1 Key Performance Parameters**

| Parameter            | Value | Unit |
|----------------------|-------|------|
| $V_{DS} @ T_{j,max}$ | 650   | V    |
| $R_{DS(on),max}$     | 255   | mΩ   |
| $Q_{g,typ}$          | 31    | nC   |
| $I_{D,pulse}$        | 43    | A    |
| $E_{oss} @ 400V$     | 4.2   | μJ   |
| Body diode $di_F/dt$ | 500   | A/μs |

| Type / Ordering Code | Package   | Marking | Related Links  |
|----------------------|-----------|---------|----------------|
| IPL60R255P6          | PG-VSON-4 | 6R255P6 | see Appendix A |

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## 1 Maximum ratings

at  $T_j = 25^\circ\text{C}$ , unless otherwise specified

**Table 2 Maximum ratings**

| Parameter                              | Symbol        | Values |      |              | Unit             | Note / Test Condition                                                                 |
|----------------------------------------|---------------|--------|------|--------------|------------------|---------------------------------------------------------------------------------------|
|                                        |               | Min.   | Typ. | Max.         |                  |                                                                                       |
| Continuous drain current <sup>1)</sup> | $I_D$         | -      | -    | 15.9<br>10.1 | A                | $T_C=25^\circ\text{C}$<br>$T_C=100^\circ\text{C}$                                     |
| Pulsed drain current <sup>2)</sup>     | $I_{D,pulse}$ | -      | -    | 43           | A                | $T_C=25^\circ\text{C}$                                                                |
| Avalanche energy, single pulse         | $E_{AS}$      | -      | -    | 352          | mJ               | $I_D=2.8\text{A}$ ; $V_{DD}=50\text{V}$ ; see table 10                                |
| Avalanche energy, repetitive           | $E_{AR}$      | -      | -    | 0.53         | mJ               | $I_D=2.8\text{A}$ ; $V_{DD}=50\text{V}$ ; see table 10                                |
| Avalanche current, repetitive          | $I_{AR}$      | -      | -    | 2.8          | A                | -                                                                                     |
| MOSFET dv/dt ruggedness                | dv/dt         | -      | -    | 100          | V/ns             | $V_{DS}=0\dots400\text{V}$                                                            |
| Gate source voltage (static)           | $V_{GS}$      | -20    | -    | 20           | V                | static;                                                                               |
| Gate source voltage (dynamic)          | $V_{GS}$      | -30    | -    | 30           | V                | AC ( $f>1\text{ Hz}$ )                                                                |
| Power dissipation                      | $P_{tot}$     | -      | -    | 126          | W                | $T_C=25^\circ\text{C}$                                                                |
| Storage temperature                    | $T_{stg}$     | -40    | -    | 150          | $^\circ\text{C}$ | -                                                                                     |
| Operating junction temperature         | $T_j$         | -40    | -    | 150          | $^\circ\text{C}$ | -                                                                                     |
| Continuous diode forward current       | $I_S$         | -      | -    | 13.8         | A                | $T_C=25^\circ\text{C}$                                                                |
| Diode pulse current <sup>2)</sup>      | $I_{S,pulse}$ | -      | -    | 43           | A                | $T_C=25^\circ\text{C}$                                                                |
| Reverse diode dv/dt <sup>3)</sup>      | dv/dt         | -      | -    | 15           | V/ns             | $V_{DS}=0\dots400\text{V}$ , $I_{SD}\leq I_S$ , $T_j=25^\circ\text{C}$<br>see table 8 |
| Maximum diode commutation speed        | di/dt         | -      | -    | 500          | A/ $\mu\text{s}$ | $V_{DS}=0\dots400\text{V}$ , $I_{SD}\leq I_S$ , $T_j=25^\circ\text{C}$<br>see table 8 |

<sup>1)</sup> Limited by  $T_{j,max}$ . Maximum duty cycle  $D=0.75$

<sup>2)</sup> Pulse width  $t_p$  limited by  $T_{j,max}$

<sup>3)</sup> Identical low side and high side switch with identical  $R_G$

## 2 Thermal characteristics

**Table 3 Thermal characteristics**

| Parameter                                              | Symbol     | Values |      |      | Unit | Note / Test Condition                                                                                                                                                   |
|--------------------------------------------------------|------------|--------|------|------|------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
|                                                        |            | Min.   | Typ. | Max. |      |                                                                                                                                                                         |
| Thermal resistance, junction - case                    | $R_{thJC}$ | -      | -    | 0.99 | °C/W | -                                                                                                                                                                       |
| Thermal resistance, junction - ambient                 | $R_{thJA}$ | -      | -    | 62   | °C/W | device on PCB, minimal footprint                                                                                                                                        |
| Thermal resistance, junction - ambient for SMD version | $R_{thJA}$ | -      | -    | 45   | °C/W | Device on 40mm*40mm*1.5mm epoxy PCB FR4 with 6cm² (one layer, 70µm thickness) copper area for drain connection and cooling. PCB is vertical without air stream cooling. |
| Soldering temperature, wave & reflow soldering allowed | $T_{sold}$ | -      | -    | 260  | °C   | reflow MSL2a                                                                                                                                                            |

### 3 Electrical characteristics

at  $T_j=25^{\circ}\text{C}$ , unless otherwise specified

**Table 4 Static characteristics**

| Parameter                        | Symbol        | Values |                |            | Unit          | Note / Test Condition                                                                                                                     |
|----------------------------------|---------------|--------|----------------|------------|---------------|-------------------------------------------------------------------------------------------------------------------------------------------|
|                                  |               | Min.   | Typ.           | Max.       |               |                                                                                                                                           |
| Drain-source breakdown voltage   | $V_{(BR)DSS}$ | 600    | -              | -          | V             | $V_{GS}=0\text{V}$ , $I_D=1\text{mA}$                                                                                                     |
| Gate threshold voltage           | $V_{(GS)th}$  | 3.5    | 4.0            | 4.5        | V             | $V_{DS}=V_{GS}$ , $I_D=0.53\text{mA}$                                                                                                     |
| Zero gate voltage drain current  | $I_{DSS}$     | -      | -              | 1          | $\mu\text{A}$ | $V_{DS}=600$ , $V_{GS}=0\text{V}$ , $T_j=25^{\circ}\text{C}$<br>$V_{DS}=600$ , $V_{GS}=0\text{V}$ , $T_j=150^{\circ}\text{C}$             |
| Gate-source leakage current      | $I_{GSS}$     | -      | -              | 100        | nA            | $V_{GS}=20\text{V}$ , $V_{DS}=0\text{V}$                                                                                                  |
| Drain-source on-state resistance | $R_{DS(on)}$  | -      | 0.230<br>0.597 | 0.255<br>- | $\Omega$      | $V_{GS}=10\text{V}$ , $I_D=6.4\text{A}$ , $T_j=25^{\circ}\text{C}$<br>$V_{GS}=10\text{V}$ , $I_D=6.4\text{A}$ , $T_j=150^{\circ}\text{C}$ |
| Gate resistance                  | $R_G$         | -      | 4.4            | -          | $\Omega$      | $f=1\text{MHz}$ , open drain                                                                                                              |

**Table 5 Dynamic characteristics**

| Parameter                                                  | Symbol       | Values |      |      | Unit | Note / Test Condition                                                                           |
|------------------------------------------------------------|--------------|--------|------|------|------|-------------------------------------------------------------------------------------------------|
|                                                            |              | Min.   | Typ. | Max. |      |                                                                                                 |
| Input capacitance                                          | $C_{iss}$    | -      | 1450 | -    | pF   | $V_{GS}=0\text{V}$ , $V_{DS}=100\text{V}$ , $f=1\text{MHz}$                                     |
| Output capacitance                                         | $C_{oss}$    | -      | 64   | -    | pF   | $V_{GS}=0\text{V}$ , $V_{DS}=100\text{V}$ , $f=1\text{MHz}$                                     |
| Effective output capacitance, energy related <sup>1)</sup> | $C_{o(er)}$  | -      | 52   | -    | pF   | $V_{GS}=0\text{V}$ , $V_{DS}=0\ldots400\text{V}$                                                |
| Effective output capacitance, time related <sup>2)</sup>   | $C_{o(tr)}$  | -      | 220  | -    | pF   | $I_D=\text{constant}$ , $V_{GS}=0\text{V}$ , $V_{DS}=0\ldots400\text{V}$                        |
| Turn-on delay time                                         | $t_{d(on)}$  | -      | 12   | -    | ns   | $V_{DD}=400\text{V}$ , $V_{GS}=13\text{V}$ , $I_D=8\text{A}$ ,<br>$R_G=3.4\Omega$ ; see table 9 |
| Rise time                                                  | $t_r$        | -      | 7    | -    | ns   | $V_{DD}=400\text{V}$ , $V_{GS}=13\text{V}$ , $I_D=8\text{A}$ ,<br>$R_G=3.4\Omega$ ; see table 9 |
| Turn-off delay time                                        | $t_{d(off)}$ | -      | 38   | -    | ns   | $V_{DD}=400\text{V}$ , $V_{GS}=13\text{V}$ , $I_D=8\text{A}$ ,<br>$R_G=3.4\Omega$ ; see table 9 |
| Fall time                                                  | $t_f$        | -      | 6    | -    | ns   | $V_{DD}=400\text{V}$ , $V_{GS}=13\text{V}$ , $I_D=8\text{A}$ ,<br>$R_G=3.4\Omega$ ; see table 9 |

**Table 6 Gate charge characteristics**

| Parameter             | Symbol        | Values |      |      | Unit | Note / Test Condition                                               |
|-----------------------|---------------|--------|------|------|------|---------------------------------------------------------------------|
|                       |               | Min.   | Typ. | Max. |      |                                                                     |
| Gate to source charge | $Q_{gs}$      | -      | 9    | -    | nC   | $V_{DD}=400\text{V}$ , $I_D=8\text{A}$ , $V_{GS}=0$ to $10\text{V}$ |
| Gate to drain charge  | $Q_{gd}$      | -      | 11   | -    | nC   | $V_{DD}=400\text{V}$ , $I_D=8\text{A}$ , $V_{GS}=0$ to $10\text{V}$ |
| Gate charge total     | $Q_g$         | -      | 31   | -    | nC   | $V_{DD}=400\text{V}$ , $I_D=8\text{A}$ , $V_{GS}=0$ to $10\text{V}$ |
| Gate plateau voltage  | $V_{plateau}$ | -      | 6.1  | -    | V    | $V_{DD}=400\text{V}$ , $I_D=8\text{A}$ , $V_{GS}=0$ to $10\text{V}$ |

<sup>1)</sup>  $C_{o(er)}$  is a fixed capacitance that gives the same stored energy as  $C_{oss}$  while  $V_{DS}$  is rising from 0 to 400V

<sup>2)</sup>  $C_{o(tr)}$  is a fixed capacitance that gives the same charging time as  $C_{oss}$  while  $V_{DS}$  is rising from 0 to 400V

**Table 7 Reverse diode characteristics**

| Parameter                     | Symbol    | Values |      |      | Unit    | Note / Test Condition                                      |
|-------------------------------|-----------|--------|------|------|---------|------------------------------------------------------------|
|                               |           | Min.   | Typ. | Max. |         |                                                            |
| Diode forward voltage         | $V_{SD}$  | -      | 0.9  | -    | V       | $V_{GS}=0V$ , $I_F=8A$ , $T_J=25^{\circ}C$                 |
| Reverse recovery time         | $t_{rr}$  | -      | 282  | -    | ns      | $V_R=400V$ , $I_F=8A$ , $di_F/dt=100A/\mu s$ ; see table 8 |
| Reverse recovery charge       | $Q_{rr}$  | -      | 3.4  | -    | $\mu C$ | $V_R=400V$ , $I_F=8A$ , $di_F/dt=100A/\mu s$ ; see table 8 |
| Peak reverse recovery current | $I_{rrm}$ | -      | 24   | -    | A       | $V_R=400V$ , $I_F=8A$ , $di_F/dt=100A/\mu s$ ; see table 8 |

## 4 Electrical characteristics diagrams

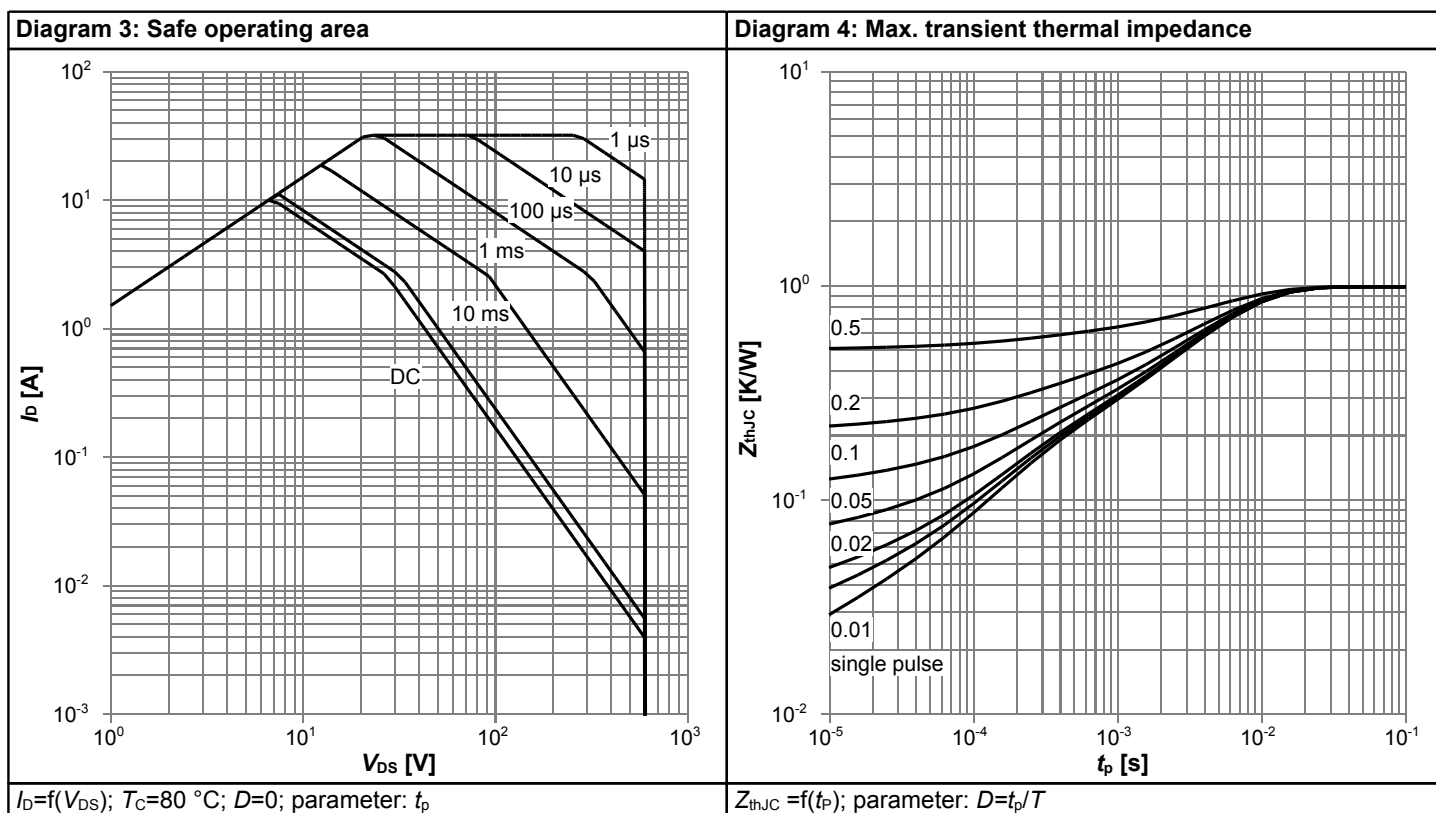
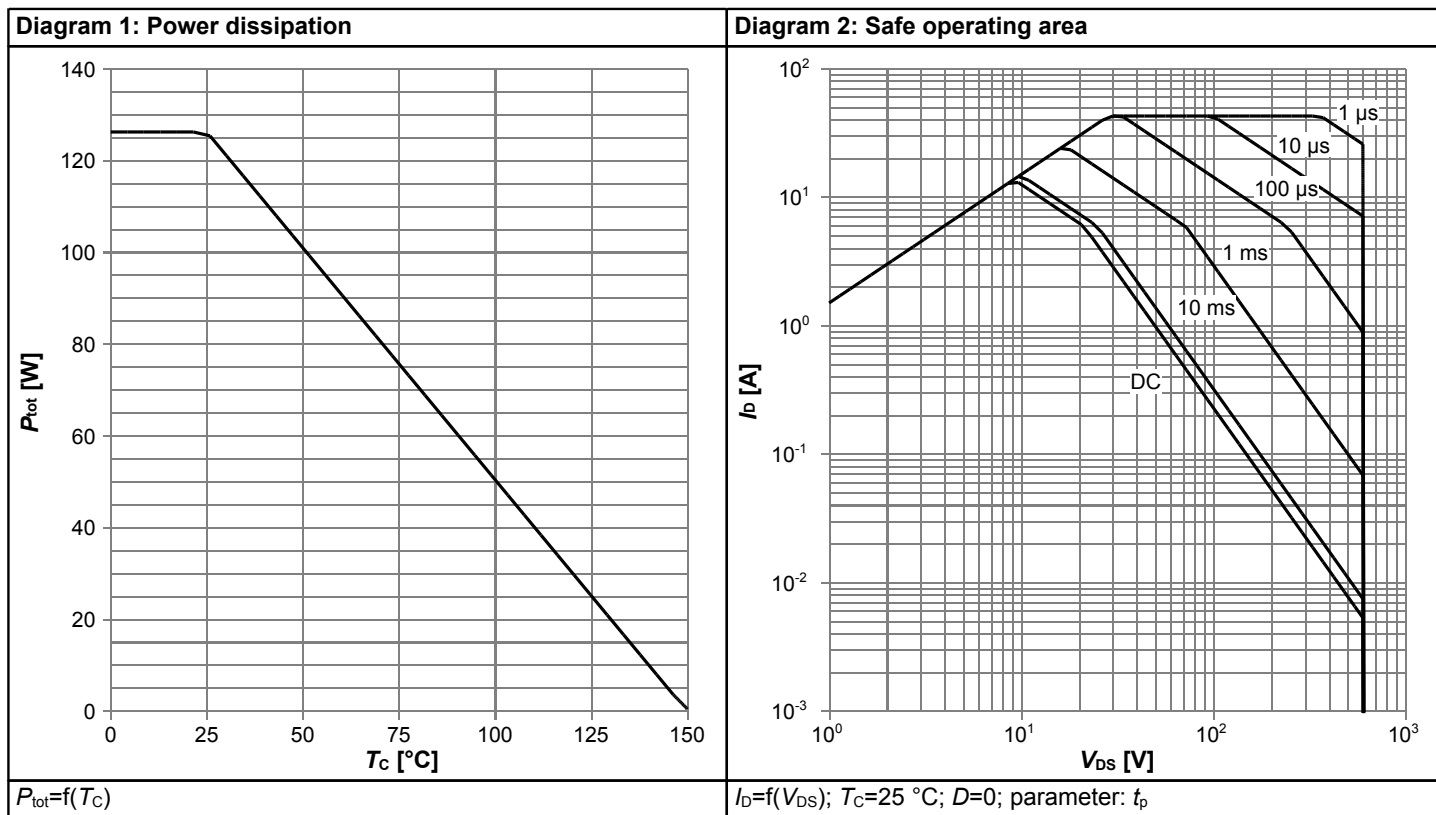
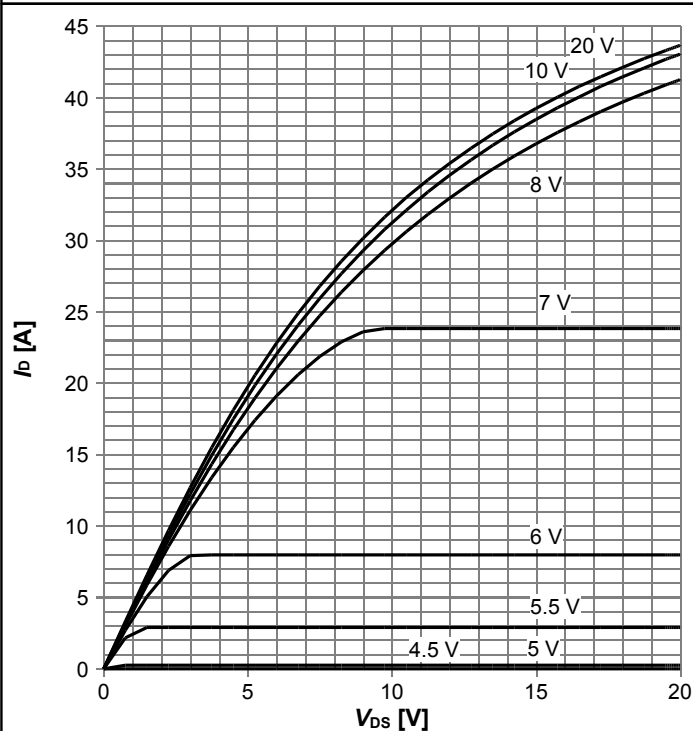
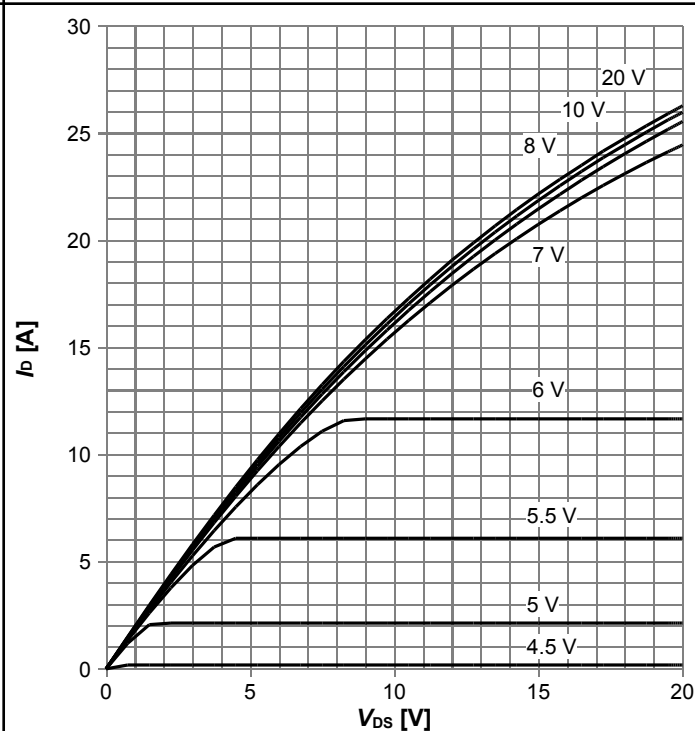


Diagram 5: Typ. output characteristics



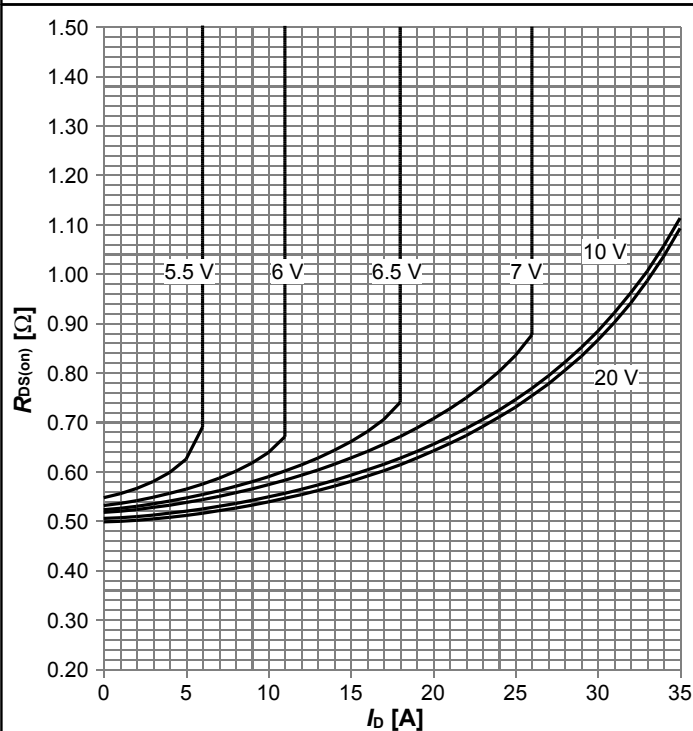
$I_D = f(V_{DS})$ ;  $T_J = 25^\circ\text{C}$ ; parameter:  $V_{GS}$

Diagram 6: Typ. output characteristics



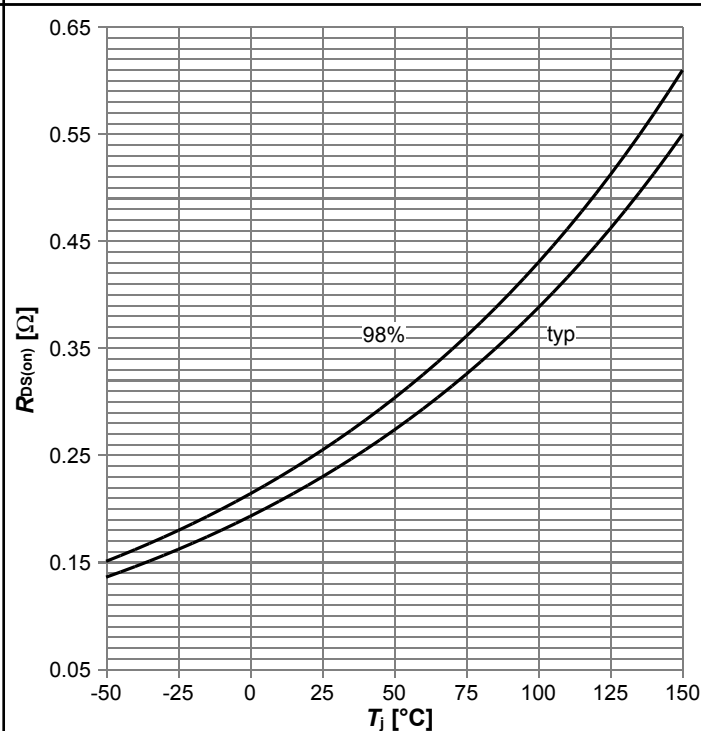
$I_D = f(V_{DS})$ ;  $T_J = 125^\circ\text{C}$ ; parameter:  $V_{GS}$

Diagram 7: Typ. drain-source on-state resistance



$R_{DS(on)} = f(I_D)$ ;  $T_J = 125^\circ\text{C}$ ; parameter:  $V_{GS}$

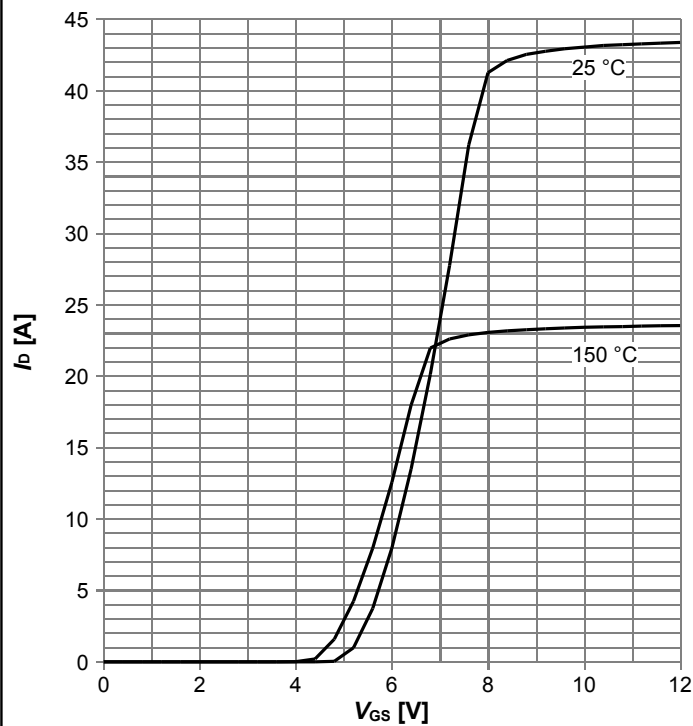
Diagram 8: Drain-source on-state resistance



$R_{DS(on)} = f(T_J)$ ;  $I_D = 6.4\text{ A}$ ;  $V_{GS} = 10\text{ V}$

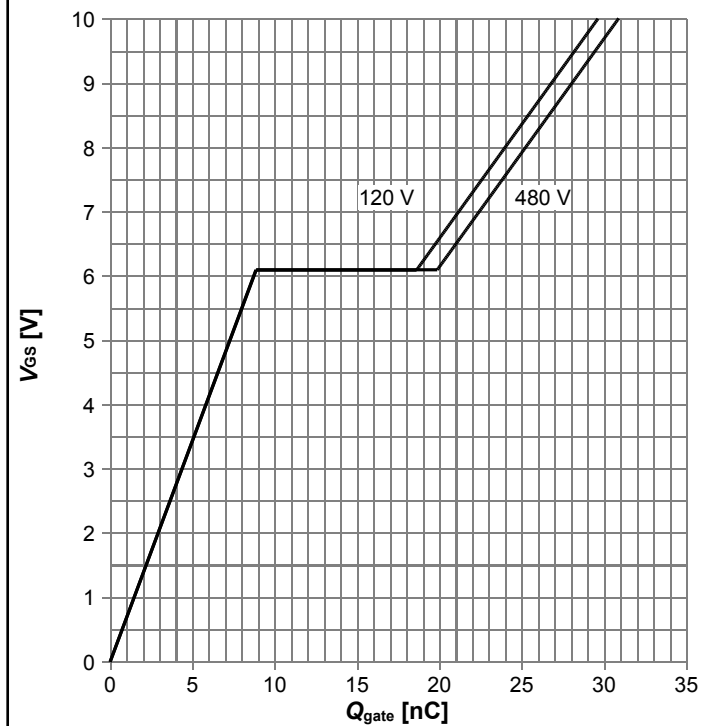


**Diagram 9: Typ. transfer characteristics**



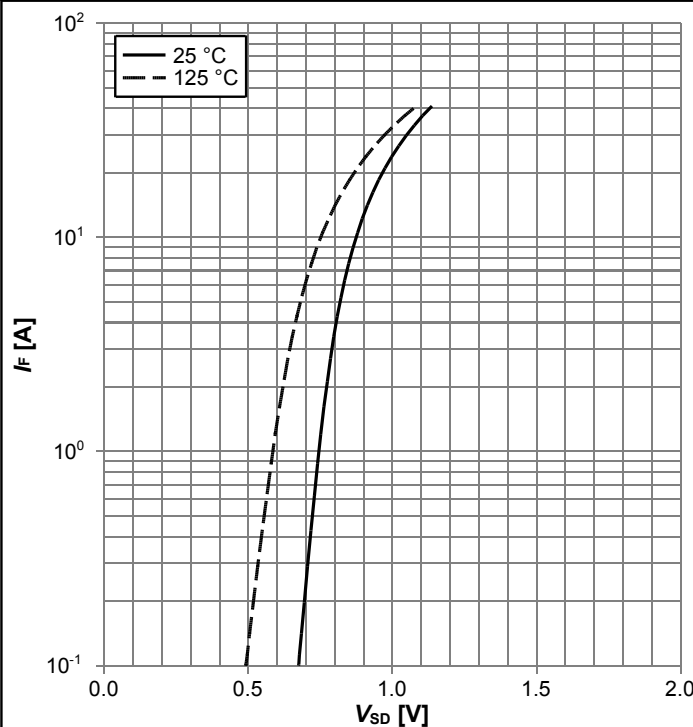
$I_D = f(V_{GS})$ ;  $V_{DS} = 20$  V; parameter:  $T_j$

**Diagram 10: Typ. gate charge**



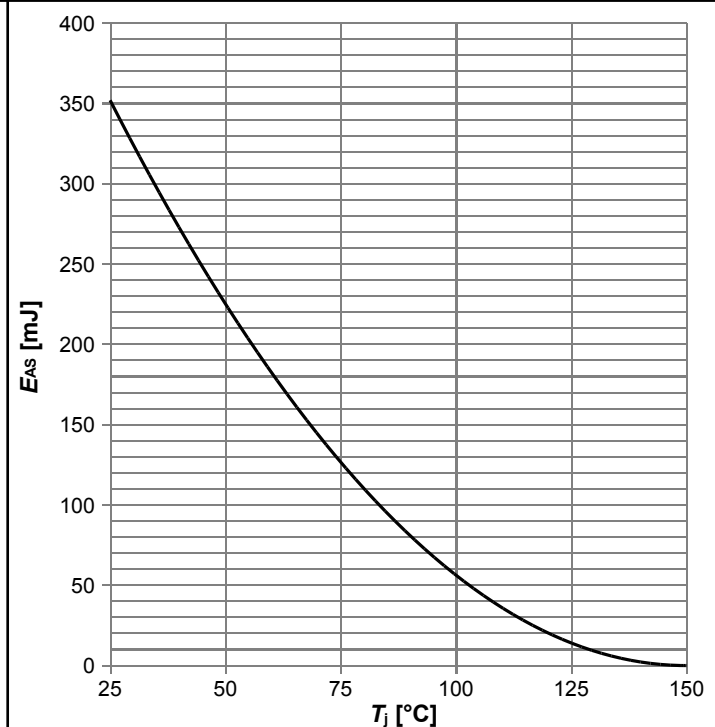
$V_{GS} = f(Q_{gate})$ ;  $I_D = 8.0$  A pulsed; parameter:  $V_{DD}$

**Diagram 11: Forward characteristics of reverse diode**



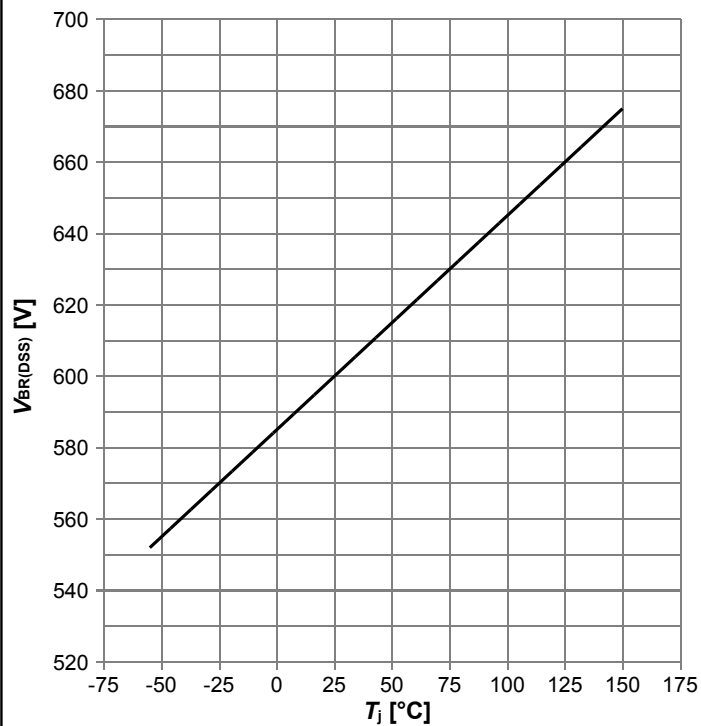
$I_F = f(V_{SD})$ ; parameter:  $T_j$

**Diagram 12: Avalanche energy**



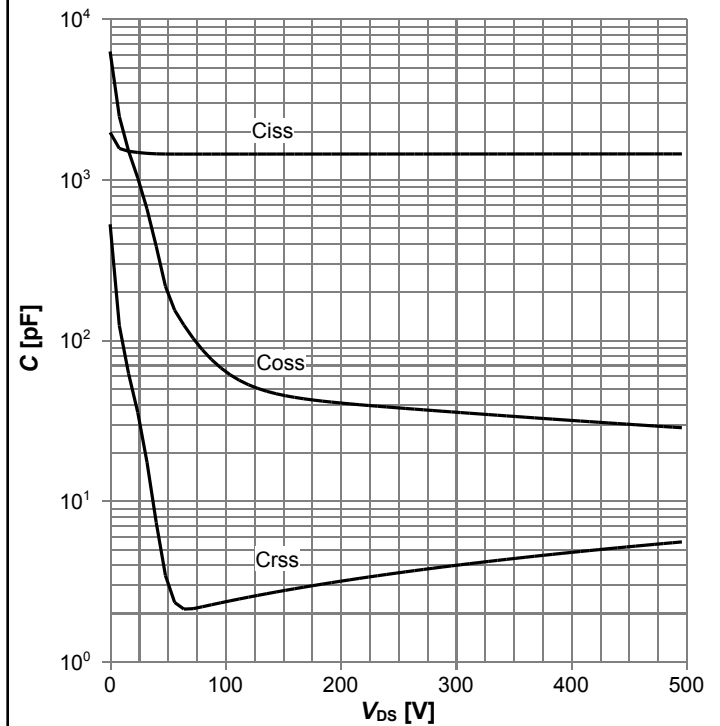
$E_{AS} = f(T_j)$ ;  $I_D = 2.8$  A;  $V_{DD} = 50$  V

**Diagram 13: Drain-source breakdown voltage**



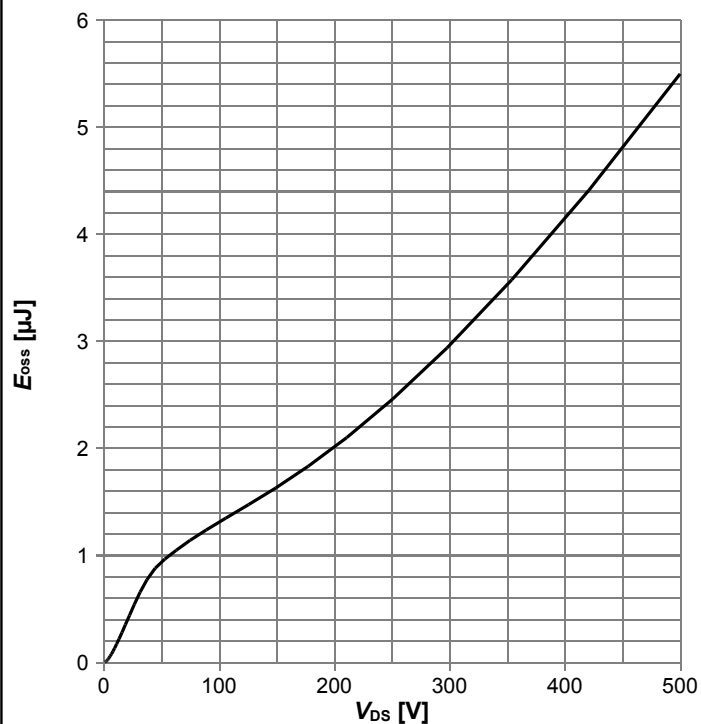
$V_{BR(DSS)} = f(T_j); I_D = 1 \text{ mA}$

**Diagram 14: Typ. capacitances**



$C = f(V_{DS}); V_{GS} = 0 \text{ V}; f = 1 \text{ MHz}$

**Diagram 15: Typ. Coss stored energy**



$E_{oss} = f(V_{DS})$

## 5 Test Circuits

Table 8 Diode characteristics

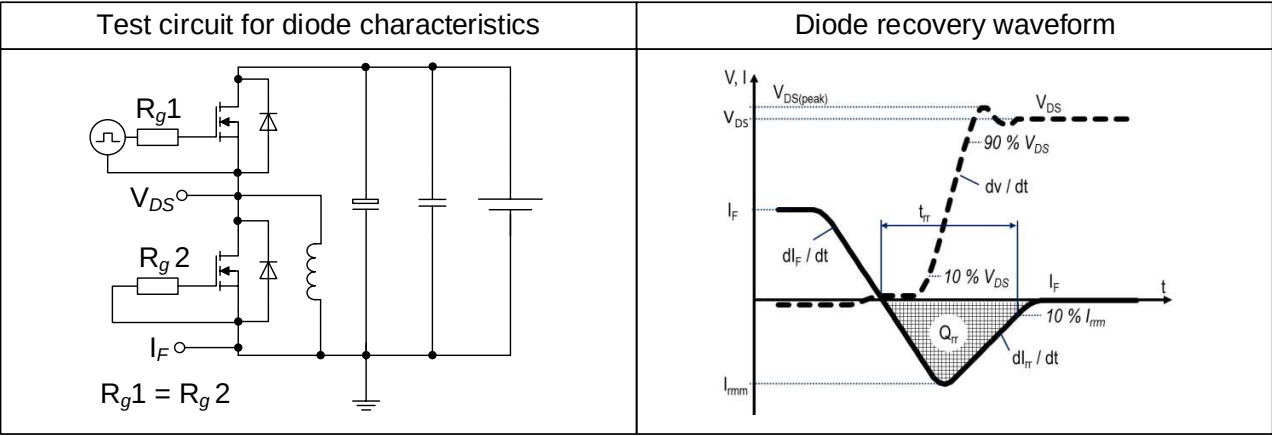


Table 9 switching times (ss)

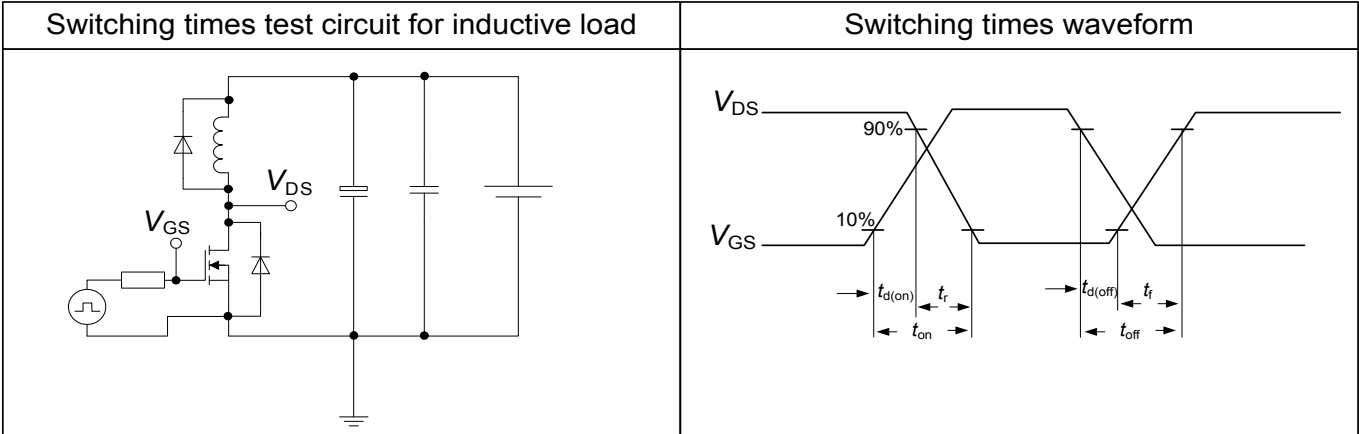
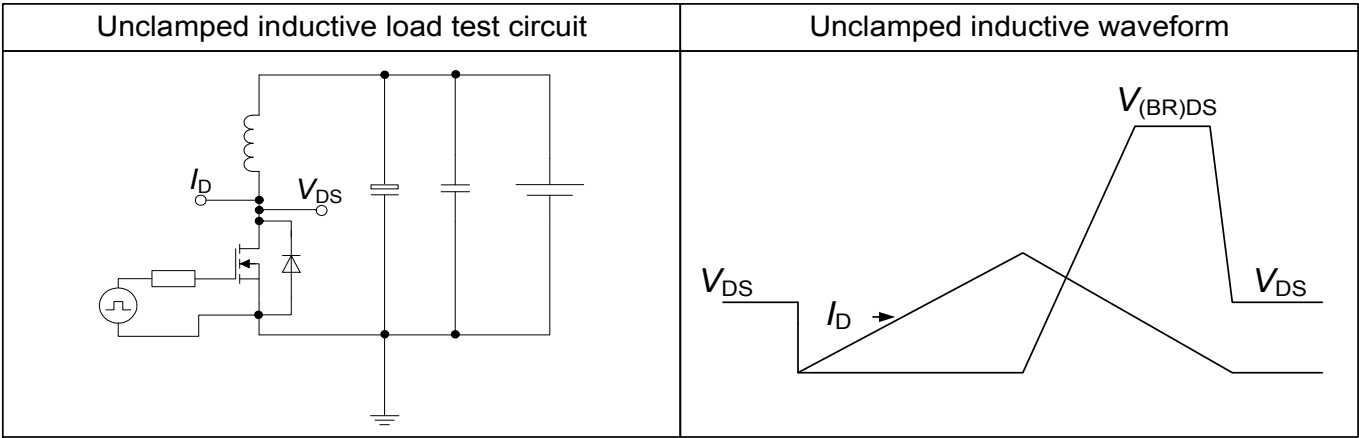


Table 10 Unclamped inductive load (ss)



## 6 Package Outlines

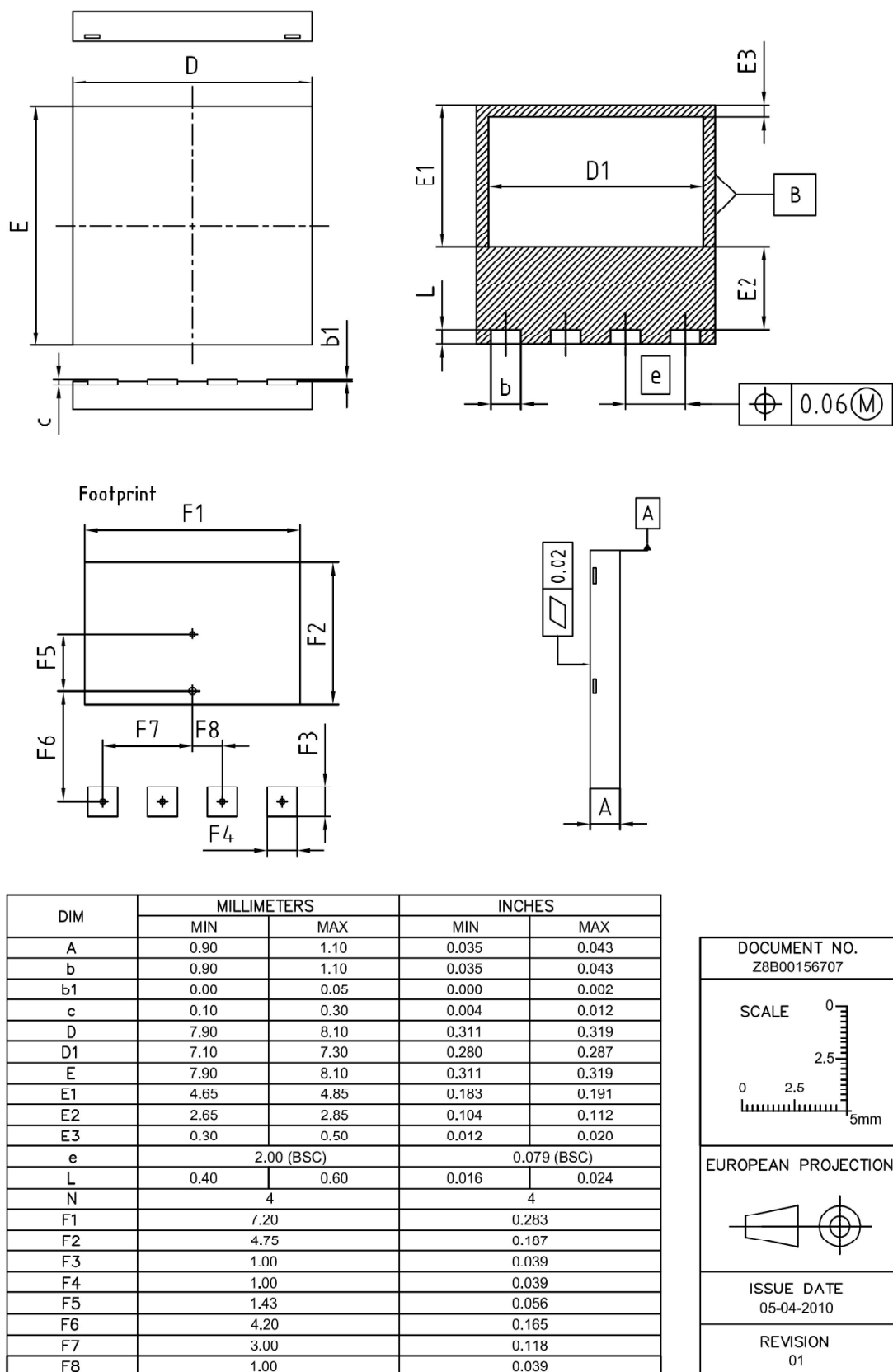


Figure 1 Outline PG-VSON-4, dimensions in mm/inches

## **7 Appendix A**

### **Table 11 Related Links**

- IFX CoolMOS™ P6 Webpage: [www.infineon.com](http://www.infineon.com)
- IFX CoolMOS™ P6 application note: [www.infineon.com](http://www.infineon.com)
- IFX CoolMOS™ P6 simulation model: [www.infineon.com](http://www.infineon.com)
- IFX Design tools: [www.infineon.com](http://www.infineon.com)

## Revision History

IPL60R255P6

**Revision: 2017-08-30, Rev. 2.1**

Previous Revision

| Revision | Date       | Subjects (major changes since last revision) |
|----------|------------|----------------------------------------------|
| 2.0      | 2014-05-16 | Release of final version                     |
| 2.1      | 2017-08-30 | Updated MSL; style updated                   |

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