

# EZ-BLE™ PSoC® EVALUATION BOARD

## CYBLE-014008-EVAL



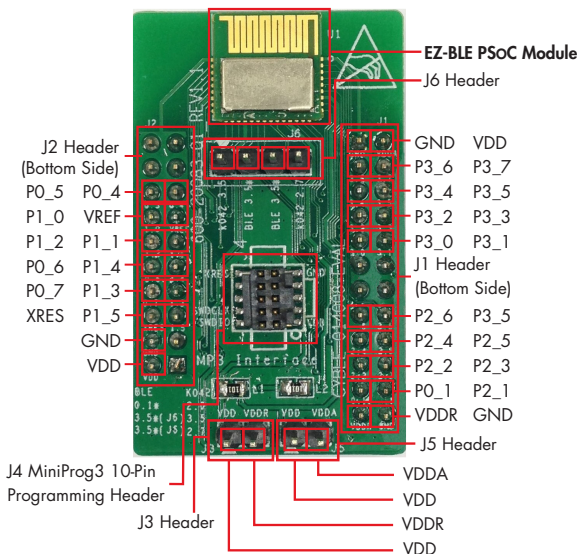
The EZ-BLE™ PSoC® Evaluation Board (CYBLE-014008-EVAL) enables you to evaluate and develop applications on the EZ-BLE PSoC Module (CYBLE-014008-00). The EZ-BLE PSoC Module is a fully-integrated, fully-certified, 11 mm x 11 mm x 1.8 mm, programmable, Bluetooth® Smart module designed to reduce your time-to-market.

For more information visit:

[www.cypress.com/EZ-BLEModule](http://www.cypress.com/EZ-BLEModule) - The EZ-BLE Module home page.

[www.cypress.com/EZ-BLEEval](http://www.cypress.com/EZ-BLEEval) - The EZ-BLE PSoC Evaluation Board kit page.

[www.cypress.com/AN96841](http://www.cypress.com/AN96841) - The getting started guide.



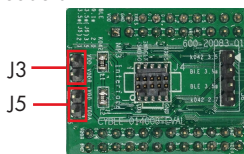
**Fig 1: CYBLE-014008-00-EVAL Pinout Description**

Note: To use the CYBLE-014008-EVAL you must separately purchase either the CY8CKIT-002 MINIPROG3 or the CY8CKIT-042-BLE BLE PIONEER KIT.

# EZ-BLE™ PSoC® EVALUATION BOARD

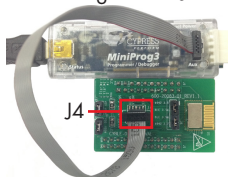
**1:** Connect the CYBLE-014008-00-EVAL to the CY8CKIT-002 (MiniProg3)

Connect a jumper on the J3 and J5 headers



**Fig 2: J3 and J5 Headers**

Connect the MiniProg3 to the J4 10-pin header



**Fig 3: Programming/Debugging with MiniProg3**

**OR**

Plug the CYBLE-014008-EVAL on the CY8CKIT-042-BLE baseboard.

Note: Jumpers on CYBLE-014008-EVAL's J3 or J5 headers can be connected or disconnected.



**Fig 4: Programming and debugging with CY8CKIT-042-BLE BLE Pioneer Kit Baseboard**

Pin Mapping between CY8CKIT-042-BLE (Base) and CYBLE-014008-00 EVAL (Eval)

| (Base)     | (Eval) | (Base) | (Eval) | (Base)            | (Eval)            | (Base)            | (Eval)            |
|------------|--------|--------|--------|-------------------|-------------------|-------------------|-------------------|
| GND        | GND    | P0_7   | P0_7   | P2_1              | P2_1              | P3_1              | P3_1              |
| VDDD, VDDA | VDD    | P1_0   | P1_0   | P2_2              | P2_2              | P3_2              | P3_2              |
| VDDR       | VDDR   | P1_1   | P1_1   | P2_3              | P2_3              | P3_3              | P3_3              |
| XRES       | XRES   | P1_2   | P1_2   | P2_4              | P2_4              | P3_4              | P3_4              |
| VREF       | VREF   | P1_3   | P1_3   | P2_5              | P2_5              | P3_5 <sup>1</sup> | P3_5 <sup>1</sup> |
| PO_4       | PO_4   | P1_4   | P1_4   | P2_6              | P2_6              | P3_6              | P3_6              |
| PO_5       | PO_5   | P1_5   | P1_5   | P2_7 <sup>1</sup> | P3_5 <sup>1</sup> | P3_7              | P3_7              |
| PO_6       | PO_6   | P2_0   | P0_1   | P3_0              | P3_0              |                   |                   |

<sup>1</sup> The J6 header provides flexibility to route P3\_5 of the EZ-BLE Module to the P2\_7 and/or P3\_5 connections of the Pioneer Baseboard.

**2:** Create a project using the CYBLE-014008-00 MPN and program/debug using PSoC® Creator™ 3.3

The EZ-BLE PSoC Module is qualified for the Bluetooth 4.1 specification and is certified for the 2.4 GHz unlicensed frequency range in USA (FCC), Canada (IC), Europe (CE), Japan (TELEC) and Korea (KC).

Visit [www.cypress.com/support](http://www.cypress.com/support) for technical support