

REVISIONS			
LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED
A	Add vendor CAGE 01295. Add case outline 2. Make change to 1.2.2, 1.3, FIGURE 1, TABLE I, and 6.4. In accordance with NOR 5962-R302-92.	92-12-02	M. A. FRYE
B	Add generic part number LT1007 as device type 02. Make changes to 1.2.1, FIGURE 1, and TABLE I. Redrawn.	95-05-04	M. A. FRYE
C	Drawing updated to reflect current requirements. - ro	05-04-25	R. MONNIN
D	Modify drawing to current requirement. -rrp	10-10-20	C. SAFFLE

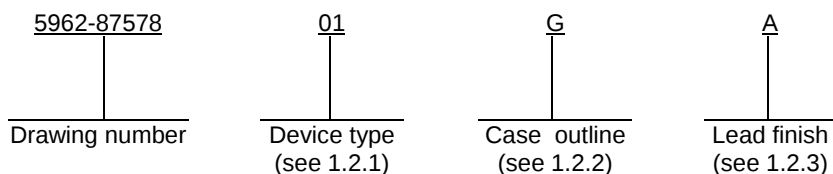
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PMIC N/A				PREPARED BY DONALD R. OSBORNE						DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990 http://www.dscclia.mil																			
STANDARD MICROCIRCUIT DRAWING THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE AMSC N/A				CHECKED BY D. A. DiCENZO																									
				APPROVED BY N. A. HAUCK																									
				DRAWING APPROVAL DATE 87-08-21																									
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1. SCOPE

1.1 Scope. This drawing describes device requirements for MIL-STD-883 compliant, non-JAN class level B microcircuits in accordance with MIL-PRF-38535, appendix A.

1.2 Part or Identifying Number (PIN). The complete PIN is as shown in the following example:



1.2.1 Device type(s). The device type(s) identify the circuit function as follows:

<u>Device type</u>	<u>Generic number</u>	<u>Circuit function</u>
01	LT1007A	Low noise, high speed precision operational amplifier
02	LT1007	Low noise, high speed precision operational amplifier

1.2.2 Case outline(s). The case outline(s) are as designated in MIL-STD-1835 and as follows:

<u>Outline letter</u>	<u>Descriptive designator</u>	<u>Terminals</u>	<u>Package style</u>
G	MACY1-X8	8	Can
P	GDIP1-T8 or CDIP2-T8	8	Dual-in-line
2	CQCC1-N20	20	Square leadless chip carrier

1.2.3 Lead finish. The lead finish is as specified in MIL-PRF-38535, appendix A.

1.3 Absolute maximum ratings.

Supply voltage	±22.0 V dc
Input voltage	±22.0 V dc
Differential input	±25 mA
Junction temperature (T _J)	+150°C
Lead temperature (soldering, 10 seconds)	+300°C
Storage temperature range	-65°C to +125°C
Thermal resistance, junction-to-case (θ _{JC})	See MIL-STD-1835
Thermal resistance, junction-to-ambient (θ _{JA}):	
Case G	150°C/W
Case P	110°C/W
Case 2	65°C/W

1.4 Recommended operating conditions.

Supply voltage	±15.0 V dc
Ambient operating temperature range (T _A)	-55°C to +125°C

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2. APPLICABLE DOCUMENTS

2.1 Government specification, standards, and handbooks. The following specification, standards, and handbooks form a part of this drawing to the extent specified herein. Unless otherwise specified, the issues of these documents are those cited in the solicitation or contract.

DEPARTMENT OF DEFENSE SPECIFICATION

MIL-PRF-38535 - Integrated Circuits, Manufacturing, General Specification for.

DEPARTMENT OF DEFENSE STANDARDS

MIL-STD-883 - Test Method Standard Microcircuits.

MIL-STD-1835 - Interface Standard Electronic Component Case Outlines.

DEPARTMENT OF DEFENSE HANDBOOKS

MIL-HDBK-103 - List of Standard Microcircuit Drawings.

MIL-HDBK-780 - Standard Microcircuit Drawings.

(Copies of these documents are available online at <https://assist.daps.dla.mil/quicksearch/> or from the Standardization Document Order Desk, 700 Robbins Avenue, Building 4D, Philadelphia, PA 19111-5094.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with MIL-PRF-38535, appendix A for non-JAN class level B devices and as specified herein. Product built to this drawing that is produced by a Qualified Manufacturer Listing (QML) certified and qualified manufacturer or a manufacturer who has been granted transitional certification to MIL-PRF-38535 may be processed as QML product in accordance with the manufacturers approved program plan and qualifying activity approval in accordance with MIL-PRF-38535. This QML flow as documented in the Quality Management (QM) plan may make modifications to the requirements herein. These modifications shall not affect form, fit, or function of the device. These modifications shall not affect the PIN as described herein. A "Q" or "QML" certification mark in accordance with MIL-PRF-38535 is required to identify when the QML flow option is used.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-PRF-38535, appendix A and herein.

3.2.1 Case outlines. The case outlines shall be in accordance with 1.2.2 herein.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 1.

3.3 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full ambient operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.

3.5 Marking. Marking shall be in accordance with MIL-PRF-38535, appendix A. The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked. For packages where marking of the entire SMD PIN number is not feasible due to space limitations, the manufacturer has the option of not marking the "5962-" on the device.

3.5.1 Certification/compliance mark. A compliance indicator "C" shall be marked on all non-JAN devices built in compliance to MIL-PRF-38535, appendix A. The compliance indicator "C" shall be replaced with a "Q" or "QML" certification mark in accordance with MIL-PRF-38535 to identify when the QML flow option is used.

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TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions -55°C ≤ T _A ≤ +125°C ±V _S = ±15 V unless otherwise specified	Group A subgroups	Device type	Limits <u>1/</u>		Unit
					Min	Max	
Input offset voltage	V _{OS}	<u>2/</u>	4	01		25	μV
			2,3			60	
			4	02		60	
			2,3			160	
Long term input offset voltage stability	V _{OS} / time	<u>3/ 4/</u>	1,2,3	All		1.0	μV/Mo
Average input offset drift	V _{OS} / temp	<u>4/</u>	1,2,3	01		0.6	μV/°C
				02		1.0	
Input offset current	I _{OS}		1	01		30	nA
			2,3			50	
			1	02		50	
			2,3			85	
Input bias current	I _{IB}		1	01	-35	+35	nA
			2,3		-60	+60	
			1	02	-55	+55	
			2,3		-95	+95	
Power dissipation	P _D		1	01		120	mW
			2,3			150	
			1	02		140	
			2,3			170	
Input voltage range	V _{IN}	<u>4/</u>	1	All	-11	+11	V
			2,3		-10.3	+10.3	
Maximum output voltage swing	V _{OUT}	R _L ≥ 2 kΩ	1,2,3	01	-12.5	+12.5	V
				02	-12.0	+12.0	

See footnotes at end of table.

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TABLE I. Electrical performance characteristics – Continued.

Test	Symbol	Conditions $-55^{\circ}\text{C} \leq T_A \leq +125^{\circ}\text{C}$ $\pm V_S = \pm 15\text{ V}$ unless otherwise specified	Group A subgroups	Device type	Limits <u>1/</u>		Unit
					Min	Max	
Common mode rejection ratio	CMRR	$V_{CM} = \pm 11\text{ V}$	1	01	117		dB
				02	110		
		$V_{CM} = \pm 10.3\text{ V}$	2,3	01	112		
				02	104		
Power supply rejection ratio	PSRR	$V_S = \pm 4.5\text{ V to } \pm 18\text{ V}$	1,2,3	01	104		dB
				02	100		
Input noise voltage	e_N	0.1 Hz to 10 Hz, <u>4/</u> $T_A = +25^{\circ}\text{C}$	7	All		0.13	μV_{PP}
Input noise voltage density	e_{ND}	$f_O = 10\text{ Hz}$, <u>4/</u> $T_A = +25^{\circ}\text{C}$	7	All		4.5	$\text{nV} / \sqrt{\text{Hz}}$
Input noise current density	I_{ND}	$f_O = 10\text{ Hz}$, <u>4/</u> $T_A = +25^{\circ}\text{C}$	7	All		4.0	$\text{pA} / \sqrt{\text{Hz}}$
Slew rate	SR	$R_L = 2\text{ k}\Omega$, $AV_{CL} \geq 1$, $T_A = +25^{\circ}\text{C}$	7	All	1.7		$\text{V}/\mu\text{s}$
Gain bandwidth products	GBWP	$f_O = 100\text{ kHz}$, <u>4/</u> $T_A = +25^{\circ}\text{C}$	7	All	5.0		MHz
Open loop output resistance	Z_O	$V_O = 0\text{ V}$, $I_O = 0\text{ mA}$, <u>4/</u> $T_A = +25^{\circ}\text{C}$	7			2000	Ω
Large signal voltage gain	A_{VOL}	$V_{OUT} = \pm 10\text{ V}$, $R_L \geq 1\text{ k}\Omega$	4,5,6	01	2.0		$\text{V}/\mu\text{V}$
				02	1.5		

1/ The algebraic convention, whereby the most negative value is a minimum and the most positive is a maximum, is used in this table. Negative current shall be defined as conventional current flow out of a device terminal.

2/ Input offset voltage measurements are performed by automatic test equipment approximately 0.5 second after application of power.

3/ Long term input offset stability refers to the average trend line of offset voltage vs. time over extended periods after the first 30 days of operations. Excluding the first hour of operation, changes in V_{OS} during the first 30 days are typically $2.5\text{ }\mu\text{V}$.

4/ If not tested, shall be guaranteed to the limits specified in table I herein.

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Device types	01 and 02	
Case outlines	G and P	2
Terminal number	Terminal symbol	
1	V _{OS} TRIM	NC
2	-INPUT	V _{OS} TRIM
3	+INPUT	NC
4	-V _S	NC
5	NC	-INPUT
6	OUTPUT	NC
7	+V _S	+INPUT
8	V _{OS} TRIM	NC
9	---	NC
10	---	-V _S
11	---	NC
12	---	NC
13	---	NC
14	---	NC
15	---	OUTPUT
16	---	NC
17	---	+V _S
18	---	NC
19	---	NC
20	---	V _{OS} TRIM

NC = No connection

FIGURE 1. Terminal connections.

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3.6 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in MIL-HDBK-103 (see 6.6 herein). The certificate of compliance submitted to DLA Land and Maritime -VA prior to listing as an approved source of supply shall affirm that the manufacturer's product meets the requirements of MIL-PRF-38535, appendix A and the requirements herein.

3.7 Certificate of conformance. A certificate of conformance as required in MIL-PRF-38535, appendix A shall be provided with each lot of microcircuits delivered to this drawing.

3.8 Notification of change. Notification of change to DLA Land and Maritime -VA shall be required for any change that affects this drawing.

3.9 Verification and review. DLA Land and Maritime, DLA Land and Maritime 's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

4. VERIFICATION

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-PRF-38535, appendix A.

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1015 of MIL-STD-883.

(2) $T_A = +125^{\circ}\text{C}$, minimum.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

4.3.1 Group A inspection.

a. Tests shall be as specified in table II herein.

b. Subgroups 8, 9, 10, and 11 in table I, method 5005 of MIL-STD-883 shall be omitted.

4.3.2 Groups C and D inspections.

a. End-point electrical parameters shall be as specified in table II herein.

b. Steady-state life test conditions, method 1005 of MIL-STD-883.

(1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to the preparing or acquiring activity upon request. The test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in method 1005 of MIL-STD-883.

(2) $T_A = +125^{\circ}\text{C}$, minimum.

(3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

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TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (in accordance with MIL-STD-883, method 5005, table I)
Interim electrical parameters (method 5004)	---
Final electrical test parameters (method 5004)	1*,2,3,4,5,6
Group A test requirements (method 5005)	1,2,3,4,5,6,7
Groups C and D end-point electrical parameters (method 5005)	1

* PDA applies to subgroup 1.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-PRF-38535, appendix A.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished using DD Form 1692, Engineering Change Proposal.

6.4 Record of users. Military and industrial users shall inform DLA Land and Maritime when a system application requires configuration control and the applicable SMD to that system. DLA Land and Maritime will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronics devices (FSC 5962) should contact DLA Land and Maritime -VA, telephone (614) 692-0547.

6.5 Comments. Comments on this drawing should be directed to DLA Land and Maritime -VA, Columbus, Ohio 43218-3990, or telephone (614) 692-0540.

6.6 Approved sources of supply. Approved sources of supply are listed in MIL-HDBK-103. The vendors listed in MIL-HDBK-103 have agreed to this drawing and a certificate of compliance (see 3.6 herein) has been submitted to and accepted by DLA Land and Maritime -VA.

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STANDARD MICROCIRCUIT DRAWING BULLETIN

DATE: 10-10-20

Approved sources of supply for SMD 5962-87578 are listed below for immediate acquisition information only and shall be added to MIL-HDBK-103 and QML-38535 during the next revision. MIL-HDBK-103 and QML-38535 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DLA Land and Maritime -VA. This information bulletin is superseded by the next dated revision of MIL-HDBK-103 and QML-38535. DLA Land and Maritime maintains an online database of all current sources of supply at <http://www.dsccl.dla.mil/Programs/Smcr/>.

Standard microcircuit drawing PIN <u>1/</u>	Vendor CAGE number	Vendor similar PIN <u>2/</u>
5962-8757801GA	60264	MTLT1007AQH
	<u>3/</u>	LT1007AMH/883B
	<u>3/</u>	LT1007AMLB
5962-8757801GC	<u>3/</u>	MTL1007AMH/883
5962-8757801PA	60264	MTLT1007AQD8
	<u>3/</u>	MTL1007AMJ8/883
	<u>3/</u>	LT1007AMJGB
	<u>3/</u>	LT1007AMJ8/883B
5962-8757801PC	<u>3/</u>	MTL1007AMD8/883
	<u>3/</u>	MTL1007AMJ8/883
5962-87578012A	60264	MTLT1007AQLS
	<u>3/</u>	LT1007AMFKB
5962-87578012C	<u>3/</u>	MTL1007AM2/883
5962-8757802GA	60264	MTLT1007QH
	<u>3/</u>	LT1007MH
5962-8757802GC	<u>3/</u>	MTL1007MH/883
5962-8757802PA	60264	MTLT1007QD8
	<u>3/</u>	MTL1007MJ8/883
	<u>3/</u>	LT1007MJGB
	<u>3/</u>	LT1007MJ8
5962-8757802PC	<u>3/</u>	MTL1007MD8/883
	<u>3/</u>	MTL1007MJ8/883

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Standard microcircuit drawing PIN <u>1/</u>	Vendor CAGE number	Vendor similar PIN <u>2/</u>
5962-87578022A	60264	MTLT1007QLS
	<u>3/</u>	LT1007MFKB
5962-87578022C	<u>3/</u>	MTL1007AM2/883

- 1/ The lead finish shown for each PIN representing a hermetic package is the most readily available from the manufacturer listed for that part. If the desired lead finish is not listed contact the vendor to determine its availability.
- 2/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.
- 3/ Not available from an approved source of supply.

Vendor CAGE
number

60264

Vendor name
and address

Minco Technology Labs, Inc.
1805 Rutherford Lane
Austin, TX 78754-5101

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