

Film Capacitors

Capacitors for DC Link

Series/Type: **B3277*D/E/G/J/T**

Date: June 2026

Typical applications

- Frequency converters
- Industrial and high-end power supplies
- Solar inverters

Climatic

- Max. operating temperature: 105 °C (case)
- Climatic category (IEC 60068-1:2013): 40/105/56

Construction

- Dielectric: polypropylene (MKP)
- Plastic case (UL 94 V-0)
- Epoxy resin sealing (UL 94 V-0)

Features

- Capacitance values up to 480 μF
- High CV product, compact
- Good self-healing properties
- Over-voltage capability
- Low losses with high current capability
- High reliability
- Long useful life
- RoHS-compatible

Terminals

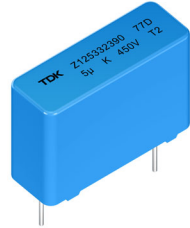
- Parallel wire leads, lead-free tinned
- 2-pin, 4-pin and 12-pin versions
- Standard lead lengths: 6 – 1 mm

Marking

Manufacturer's logo and lot number, date code, rated capacitance (coded), capacitance tolerance (code letter) and rated DC voltage

Delivery mode

Bulk (untaped)



Capacitors for DC Link

B3277*D/E/G/J/T

MKP DC link – high density series up to 480 µF

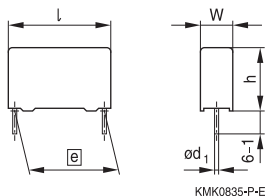
Dimensional drawings

Number of wires	Lead spacing $e \pm 0.4$	Lead diameter $d_1 \pm 0.05$	Type
2-pin	27.5	0.8	B32774D
2-pin	37.5	1.0	B32776E
2-pin	37.5	1.0	B32776T
4-pin	37.5	1.2	B32776G
4-pin	37.5	1.2	B32776T
4-pin	52.5	1.2	B32778T
4-pin	52.5	1.2	B32778G
12-pin	52.5	1.2	B32778J

Dimensions in mm

Dimensional drawings 2-pin versions

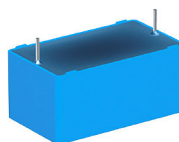
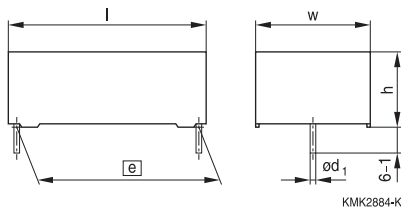
B32774D, B32776E



	B32774D	B32776E
Lead spacing $e \pm 0.4$:	27.5	37.5
Lead diameter d_1 :	0.8	1.0

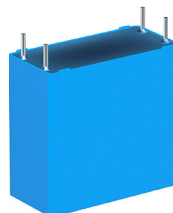
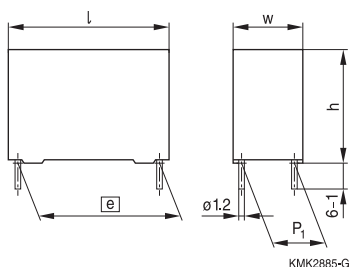
Dimensions in mm

B32776T (low profile)



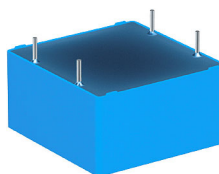
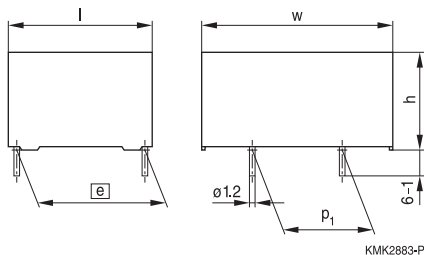
Lead spacing $e \pm 0.4$:	37.5
Lead diameter d_1 :	1.0

Dimensions in mm

Dimensional drawings 4-pin versions
B32776G, B32778G


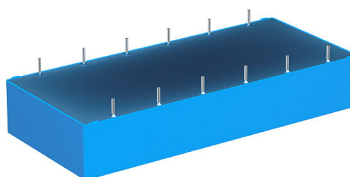
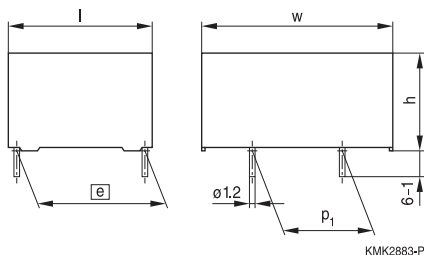
	B32776G	B32778G
Lead spacing $e \pm 0.4$:	37.5	52.5
Lead diameter d_1 :	1.2	1.2

Dimensions in mm

B32776T, B32778T (low profile)


	B32776T	B32778T
Lead spacing $e \pm 0.4$:	37.5	52.5
Lead diameter d_1 :	1.2	1.2

Dimensions in mm

Dimensional drawing 12-pin version
B32778J


Lead spacing $e \pm 0.4$:	52.5
Lead diameter d_1 :	1.2

Dimensions in mm

Overview of available types

Lead spacing	27.5 mm				37.5 mm					
Type	B32774				B32776					
Page	7				8					
V _R (V DC)	450	800	1100	1300	450	575	800	900	1100	1300
C _R (µF)										
1.5										
2.0										
2.7										
3.0										
3.3										
3.5										
3.9										
5.0										
6.8										
7.0										
7.5										
8.0										
8.5										
9.0										
10										
12										
13										
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30										
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40										
45										
50										
60										
65										

Capacitors for DC Link

B3277*D/E/G/J/T

MKP DC link – high density series up to 480 µF

Overview of available types

Lead spacing	52.5 mm					
Type	B32778					
Page	11					
V _R (V DC)	450	575	800	900	1100	1300
C _R (µF)						
14						
20						
25						
27						
30						
35						
38						
40						
42						
45						
50						
55						
58						
60						
70						
75						
80						
90						
100						
110						
120						
130						
150						
170						
180						
200						
210						
270						
360						
480						

Capacitors for DC Link

B3277*D/E/G/J/T

MKP DC link – high density series up to 480 µF

Ordering codes and packing units (lead spacing 27.5 mm)



$C_R^{1)}$	Max. dimensions w x h x l	Ordering code (composition see below)	$I_{RMS,max}^{2)}$ 70 °C 10 kHz A	ESR_{typ} 70 °C 10 kHz mΩ	$ESL_{typ}^{3)}$ nH	$\tan \delta$ 1 kHz 10 ⁻³	$\tan \delta$ 10 kHz 10 ⁻³	Un- taped pcs./ MOQ
µF	mm							
$V_{R,70\text{ °C}} = 450\text{ V DC}, V_{op,85\text{ °C}} = 450\text{ V DC}$								
5.0	11.0 x 21.0 x 31.5	B32774D4505+000	5.0	21.1	19.0	1.2	10.7	4704
10.0	15.0 x 24.5 x 31.5	B32774D4106+000	8.0	10.9	24.0	1.2	11.0	3360
22.0	22.0 x 36.5 x 31.5	B32774D4226+000	14.5	5.4	30.0	1.3	12.1	1568
$V_{R,70\text{ °C}} = 800\text{ V DC}, V_{op,85\text{ °C}} = 700\text{ V DC}$								
3.0	11.0 x 21.0 x 31.5	B32774D8305+000	4.5	24.8	19.0	0.9	7.6	4704
5.0	14.0 x 24.5 x 31.5	B32774D8505+000	6.5	15.3	23.0	0.9	7.7	3696
12.0	22.0 x 36.5 x 31.5	B32774D8126+000	13.0	6.8	34.0	1.0	8.3	1568
$V_{R,70\text{ °C}} = 1100\text{ V DC}, V_{op,85\text{ °C}} = 920\text{ V DC}$								
2.0	12.5 x 21.5 x 31.5	B32774D0205+000	4.5	26.3	19.0	0.7	5.3	4200
3.3	18.0 x 27.5 x 31.5	B32774D0335+000	7.0	16.2	22.0	0.7	5.4	2856
5.0	19.0 x 30.0 x 31.5	B32774D0505+000	9.0	10.9	27.0	0.7	5.5	1792
7.0	22.0 x 36.5 x 31.5	B32774D0705+000	12.0	8.1	30.0	0.7	5.8	1568
$V_{R,70\text{ °C}} = 1300\text{ V DC}, V_{op,85\text{ °C}} = 1100\text{ V DC}$								
1.5	12.5 x 21.5 x 31.5	B32774D1155K000	4.4	31.3	20.0	0.6	4.8	4200
3.0	18.0 x 27.5 x 31.5	B32774D1305K000	7.0	16.0	24.0	0.6	4.9	2856
5.0	22.0 x 36.5 x 31.5	B32774D1505K000	10.5	9.8	33.0	0.7	5.1	1568

MOQ = Minimum Order Quantity, consisting of 8 packing units.

Further intermediate capacitance values are available on request.

Composition of ordering code

+ = Capacitance tolerance code:

J = ±5%

K = ±10%

Packing code:

000 = untaped (lead length 6 –1 mm)

1) Capacitance value measured at 1 kHz

2) Max ripple current I_{RMS} at 70 °C, 10 kHz for $\Delta T \leq 20\text{ °C}$ at $\Delta ESR_{typ} \leq \pm 5\%$

3) Typical ESL value measured at resonance frequency (see specific graphs of Z versus frequency)

Capacitors for DC Link

B3277*D/E/G/J/T

MKP DC link – high density series up to 480 µF

Ordering codes and packing units (lead spacing 37.5 mm)



$C_R^{1)}$	Max. dimensions w x h x l	P_1	Ordering code (composition see below)	$I_{RMS,max}^{2)}$ 70 °C 10 kHz A	ESR_{typ} 70 °C 10 kHz mΩ	$ESL_{typ}^{3)}$ nH	$\tan \delta$ 10 ⁻³	$\tan \delta$ 10 ⁻³	Un- taped pcs./ MOQ
µF	mm	mm							
$V_{R,70\text{ °C}} = 450\text{ V DC}, V_{op,85\text{ °C}} = 450\text{ V DC}$									
12	24.0 x 15.0 x 41.5	—	B32776T4126K000	7.0	17.1	19.0	2.2	21.0	2080
16	24.0 x 19.0 x 41.5	—	B32776T4166K000	8.0	13.0	18.0	2.3	21.2	1560
30	20.0 x 39.5 x 42.0	10.2	B32776G4306+000	14.0	7.0	11.0	2.3	21.3	1280
30	20.0 x 39.5 x 42.0	—	B32776E4306+000	14.0	7.3	28.0	2.4	22.3	1280
35	28.0 x 37.0 x 42.0	10.2	B32776G4356+000	16.5	6.0	10.0	2.3	21.4	880
35	28.0 x 37.0 x 42.0	—	B32776E4356+000	16.0	6.4	24.0	2.4	22.6	880
40	28.0 x 37.0 x 42.0	10.2	B32776G4406+000	17.5	5.3	11.0	2.3	21.4	880
40	28.0 x 37.0 x 42.0	—	B32776E4406+000	17.0	5.6	26.0	2.4	22.7	880
40	43.0 x 22.0 x 41.5	20.3	B32776T4406K000	17.0	5.2	13.0	2.3	21.2	560
50	28.0 x 42.5 x 42.0	10.2	B32776G4506+000	20.0	4.3	12.0	2.3	21.7	880
50	28.0 x 42.5 x 42.0	—	B32776E4506+000	19.0	4.7	30.0	2.5	23.8	880
60	30.0 x 45.0 x 42.0	20.3	B32776G4606+000	23.5	3.6	14.0	2.4	22.3	800
60	30.0 x 45.0 x 42.0	—	B32776E4606+000	22.0	4.0	32.0	2.5	24.2	800
65	33.0 x 48.0 x 42.0	20.3	B32776G4656+000	25.5	3.3	14.0	2.3	22.2	360
$V_{R,70\text{ °C}} = 575\text{ V DC}, V_{op,85\text{ °C}} = 500\text{ V DC}$									
8.5	24.0 x 15.0 x 41.5	—	B32776T5855+000	6.5	19.9	19.0	1.9	17.2	2080
12	24.0 x 19.0 x 41.5	—	B32776T5126K000	8.0	14.4	18.0	1.9	17.4	1560
25	20.0 x 39.5 x 42.0	10.2	B32776G5256K000	14.0	7.0	12.0	1.9	17.5	1280
25	20.0 x 39.5 x 42.0	—	B32776E5256K000	13.5	7.4	28.0	2.0	18.3	1280
27	43.0 x 22.0 x 41.5	20.3	B32776T5276K000	15.5	6.4	14.0	1.9	17.5	560
30	28.0 x 37.0 x 42.0	10.2	B32776G5306K000	16.5	5.8	11.0	1.9	17.6	880
30	28.0 x 37.0 x 42.0	—	B32776E5306K000	16.5	6.1	26.0	2.0	18.5	880
35	28.0 x 42.5 x 42.0	10.2	B32776G5356+000	19.0	5.0	12.0	1.9	17.8	880
35	28.0 x 42.5 x 42.0	—	B32776E5356+000	18.0	5.3	29.0	2.0	19.0	880
45	30.0 x 45.0 x 42.0	20.3	B32776G5456K000	22.0	4.0	13.0	1.9	17.9	800
45	30.0 x 45.0 x 42.0	—	B32776E5456K000	21.0	4.4	32.0	2.1	19.7	800
50	33.0 x 48.0 x 42.0	20.3	B32776G5506K000	25.0	3.5	14.0	2.0	18.1	360

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K = ±10%

Packing code:

000 = untaped (lead length 6 –1 mm)

1) Capacitance value measured at 1 kHz

2) Max ripple current I_{RMS} at 70 °C, 10 kHz for $\Delta T \leq 20\text{ °C}$ at $\Delta ESR_{typ} \leq \pm 5\%$

3) Typical ESL value measured at resonance frequency (see specific graphs of Z versus frequency)

Capacitors for DC Link

B3277*D/E/G/J/T

MKP DC link – high density series up to 480 µF

Ordering codes and packing units (lead spacing 37.5 mm)



C _R ¹⁾	Max. dimensions w x h x l	P ₁	Ordering code (composition see below)	I _{RMS,max} ²⁾ 70 °C 10 kHz A	ESR _{typ} 70 °C 10 kHz mΩ	ESL _{typ} ³⁾ nH	tan δ	tan δ	Un- taped pcs./ MOQ
µF	mm	mm					10 ⁻³	10 ⁻³	
V _{R,70 °C} = 800 V DC, V _{op,85 °C} = 700 V DC									
6.8	24.0 x 15.0 x 41.5	–	B32776T8685+000	6.0	22.1	18.0	1.7	15.1	2080
8.5	24.0 x 19.0 x 41.5	–	B32776T8855+000	7.5	17.8	18.0	1.7	15.1	1560
14	18.0 x 32.5 x 41.5	–	B32776E8146+000	10.0	11.5	23.0	1.8	16.3	1440
15	20.0 x 39.5 x 42.0	10.2	B32776G8156+000	12.0	9.6	10.0	1.7	15.2	1280
15	20.0 x 39.5 x 42.0	–	B32776E8156+000	11.5	10.3	24.0	1.7	15.7	1280
20	28.0 x 37.0 x 42.0	10.2	B32776G8206+000	14.5	7.5	10.0	1.7	15.3	880
20	28.0 x 37.0 x 42.0	–	B32776E8206+000	14.5	7.8	24.0	1.7	15.9	880
20	43.0 x 22.0 x 41.5	20.3	B32776T8206K000*	14.5	7.2	9.0	1.7	15.1	560
22	28.0 x 37.0 x 42.0	10.2	B32776G8226+000	15.5	6.8	11.0	1.7	15.3	880
22	28.0 x 37.0 x 42.0	–	B32776E8226+000	15.0	7.1	25.0	1.7	16.0	880
25	28.0 x 42.5 x 42.0	10.2	B32776G8256+000	17.0	6.1	11.0	1.7	15.4	880
25	28.0 x 42.5 x 42.0	–	B32776E8256+000	16.5	6.4	28.0	1.8	16.3	880
30	30.0 x 45.0 x 42.0	20.3	B32776G8306+000	19.5	5.1	12.0	1.7	15.6	800
30	30.0 x 45.0 x 42.0	–	B32776E8306+000	19.0	5.5	30.0	1.8	16.7	800
35	33.0 x 48.0 x 42.0	20.3	B32776G8356+000	22.0	4.3	14.0	1.7	15.7	360
V _{R,70 °C} = 900 V DC, V _{op,85 °C} = 800 V DC									
5	24.0 x 15.0 x 41.5	–	B32776T9505+000	5.5	26.1	19.0	1.5	13.4	2080
7.5	24.0 x 19.0 x 41.5	–	B32776T9755K000	7.5	17.8	18.0	1.5	13.5	1560
15	20.0 x 39.5 x 42.0	10.2	B32776G9156K000	12.5	9.1	12.0	1.5	13.6	1280
15	20.0 x 39.5 x 42.0	–	B32776E9156K000	12.0	9.4	28.0	1.5	14.1	1280
16	43.0 x 22.0 x 41.5	20.3	B32776T9166K000*	14.0	8.1	9.0	1.5	13.5	560
20	28.0 x 37.0 x 42.0	10.2	B32776G9206K000	15.0	7.0	11.0	1.5	13.6	880
20	28.0 x 37.0 x 42.0	–	B32776E9206K000	15.0	7.3	26.0	1.6	14.2	880
22	28.0 x 42.5 x 42.0	10.2	B32776G9226K000	17.0	6.3	12.0	1.5	13.7	880
22	28.0 x 42.5 x 42.0	–	B32776E9226K000	16.5	6.6	29.0	1.6	14.5	880
25	30.0 x 45.0 x 42.0	20.3	B32776G9256+000	19.0	5.5	13.0	1.5	13.8	800
25	30.0 x 45.0 x 42.0	–	B32776E9256+000	18.5	5.9	32.0	1.6	14.7	800
30	33.0 x 48.0 x 42.0	20.3	B32776G9306+000	21.5	4.7	14.0	1.5	13.9	360

MOQ = Minimum Order Quantity, consisting of 8 packing units.

Further intermediate capacitance values are available on request.

Composition of ordering code

+ = Capacitance tolerance code:

J = ±5%

K = ±10%

Packing code:

000 = untaped (lead length 6 –1 mm)

1) Capacitance value measured at 1 kHz

2) Max ripple current I_{RMS} at 70 °C, 10 kHz for ΔT ≤20 °C at ΔESR_{typ} ≤±5%

3) Typical ESL value measured at resonance frequency (see specific graphs of Z versus frequency)

* This part is affected by "Dual Use" regulations according to the law of the country the production site is located in. Deliveries of such products are subject to prior approval of the respective local authorities based on customer declarations. The delivery to certain countries may be restricted.

Capacitors for DC Link

B3277*D/E/G/J/T

MKP DC link – high density series up to 480 µF

Ordering codes and packing units (lead spacing 37.5 mm)



C _R ¹⁾	Max. dimensions w x h x l	P ₁	Ordering code (composition see below)	I _{RMS,max} ²⁾ 70 °C 10 kHz A	ESR _{typ} 70 °C 10 kHz mΩ	ESL _{typ} ³⁾ nH	tan δ	tan δ	Un- taped pcs./ MOQ
µF	mm	mm					10 ⁻³	10 ⁻³	
V _{R,70 °C} = 1100 V DC, V _{op,85 °C} = 920 V DC									
3.9	24.0 x 15.0 x 41.5	—	B32776T0395+000	5.0	30.5	18.0	1.4	12.1	2080
5	24.0 x 19.0 x 41.5	—	B32776T0505+000	6.5	23.6	18.0	1.4	12.1	1560
12	20.0 x 39.5 x 42.0	10.2	B32776G0126+000	12.0	10.2	12.0	1.4	12.2	1280
12	20.0 x 39.5 x 42.0	—	B32776E0126+000	11.5	10.5	28.0	1.4	12.6	1280
13	43.0 x 22.0 x 41.5	20.3	B32776T0136K000*	13.0	8.9	9.0	1.4	12.1	560
14	28.0 x 37.0 x 42.0	10.2	B32776G0146+000	13.5	8.7	21.0	1.4	12.2	880
14	28.0 x 37.0 x 42.0	—	B32776E0146+000	13.5	9.0	25.0	1.4	12.6	880
16	28.0 x 42.5 x 42.0	10.2	B32776G0166+000	15.5	7.4	12.0	1.4	12.3	880
16	28.0 x 42.5 x 42.0	—	B32776E0166+000	15.0	7.8	30.0	1.4	12.9	880
20	30.0 x 45.0 x 42.0	20.3	B32776G0206+000	18.0	6.0	14.0	1.4	12.4	800
20	30.0 x 45.0 x 42.0	—	B32776E0206+000	17.5	6.5	32.0	1.4	13.1	800
22	33.0 x 48.0 x 42.0	20.3	B32776G0226+000	21.0	4.9	15.0	1.3	11.4	360
V _{R,70 °C} = 1300 V DC, V _{op,85 °C} = 1100 V DC									
2.7	24.0 x 15.0 x 41.5	—	B32776T1275+000	5.0	34.7	19.0	1.1	9.6	2080
3.5	24.0 x 19.0 x 41.5	—	B32776T1355+000	6.0	27.4	18.0	1.1	9.7	1560
8.0	20.0 x 39.5 x 42.0	10.2	B32776G1805+000	11.0	12.1	12.0	1.1	9.7	1280
8.0	20.0 x 39.5 x 42.0	—	B32776E1805+000	10.5	12.4	24.0	1.2	10.0	1280
9.0	43.0 x 22.0 x 41.5	20.3	B32776T1905K000*	12.0	10.7	9.0	1.1	9.7	560
10	28.0 x 37.0 x 42.0	10.2	B32776G1106+000	13.0	9.6	11.0	1.1	9.7	880
10	28.0 x 37.0 x 42.0	—	B32776E1106+000	12.5	9.9	26.0	1.2	10.0	880
12	28.0 x 42.5 x 42.0	10.2	B32776G1126+000	14.5	8.1	12.0	1.1	9.8	880
12	28.0 x 42.5 x 42.0	—	B32776E1126+000	14.0	8.5	28.0	1.2	10.1	880
14	30.0 x 45.0 x 42.0	20.3	B32776G1146+000	17.0	6.8	14.0	1.1	10.1	800
14	30.0 x 45.0 x 42.0	—	B32776E1146+000	16.5	7.3	32.0	1.2	10.4	800
16	33.0 x 48.0 x 42.0	20.3	B32776G1166+000	19.0	6.0	15.0	1.1	9.9	360

MOQ = Minimum Order Quantity, consisting of 8 packing units.

Further intermediate capacitance values are available on request.

Composition of ordering code

+ = Capacitance tolerance code:

J = ±5%

K = ±10%

Packing code:

000 = untaped (lead length 6 – 1 mm)

1) Capacitance value measured at 1 kHz

2) Max ripple current I_{RMS} at 70 °C, 10 kHz for ΔT ≤ 20 °C at ΔESR_{typ} ≤ ±5%

3) Typical ESL value measured at resonance frequency (see specific graphs of Z versus frequency)

* This part is affected by "Dual Use" regulations according to the law of the country the production site is located in. Deliveries of such products are subject to prior approval of the respective local authorities based on customer declarations. The delivery to certain countries may be restricted.

Capacitors for DC Link

B3277*D/E/G/J/T

MKP DC link – high density series up to 480 µF

Ordering codes and packing units (lead spacing 52.5 mm, P₁ = 20.3 mm)



C _R ¹⁾	Max. dimensions w x h x l	Ordering code (composition see below)	I _{RMS,max} ²⁾ 70 °C 10 kHz A	ESR _{typ} 70 °C 10 kHz mΩ	ESL _{typ} ³⁾ nH	tan δ	tan δ	Un- taped pcs./ MOQ
µF	mm					10 ⁻³	10 ⁻³	
V _{R,70 °C} = 450 V DC, V _{op,85 °C} = 450 V DC								
55	43.0 x 24.0 x 57.5	B32778T4556K000	16.5	7.2	13.0	4.3	41.7	1120
75	30.0 x 45.0 x 57.5	B32778G4756+000	21.0	5.6	12.0	4.4	42.6	560
80	30.0 x 45.0 x 57.5	B32778G4806+000	21.5	5.3	13.0	4.4	42.7	560
100	35.0 x 50.0 x 57.5	B32778G4107+000	26.0	4.3	14.0	4.5	43.3	216
110	35.0 x 50.0 x 57.5	B32778G4117K000	27.0	3.9	15.0	4.5	43.6	216
150	130.0 x 24.0 x 57.5	B32778J4157K000	43.5	2.7	4.0	4.4	42.1	160
170	45.0 x 57.0 x 57.5	B32778G4177+000	36.5	2.6	17.0	4.6	45.7	280
180	60.0 x 45.0 x 57.5	B32778G4187+000	39.0	2.5	19.0	4.6	44.6	400
480	130.0 x 58.0 x 57.5	B32778J4487K000	79.5	0.9	6.0	4.8	45.4	80
V _{R,70 °C} = 575 V DC, V _{op,85 °C} = 500 V DC								
40	43.0 x 24.0 x 57.5	B32778T5406K000	15.5	8.5	13.0	3.6	34.5	1120
60	30.0 x 45.0 x 57.5	B32778G5606+000	20.5	5.8	13.0	3.7	35.3	560
80	35.0 x 50.0 x 57.5	B32778G5806+000	25.5	4.4	15.0	3.7	36.0	216
110	130.0 x 24.0 x 57.5	B32778J5117K000	40.5	3.0	5.0	3.6	34.5	160
120	45.0 x 57.0 x 57.5	B32778G5127+000	34.5	3.1	17.0	3.8	37.2	280
130	60.0 x 45.0 x 57.5	B32778G5137+000	36.5	2.8	19.0	3.8	36.7	400
360	130.0 x 58.0 x 57.5	B32778J5367K000	75.0	1.0	6.0	4.0	37.3	80

MOQ = Minimum Order Quantity, consisting of 8 packing units.

Further intermediate capacitance values are available on request.

Composition of ordering code

+ = Capacitance tolerance code:

J = ±5%

K = ±10%

Packing code:

000 = untaped (lead length 6 –1 mm)

1) Capacitance value measured at 1 kHz

2) Max ripple current I_{RMS} at 70 °C, 10 kHz for ΔT ≤20 °C at ΔESR_{typ} ≤±5%

3) Typical ESL value measured at resonance frequency (see specific graphs of Z versus frequency)

Capacitors for DC Link

B3277*D/E/G/J/T

MKP DC link – high density series up to 480 µF

Ordering codes and packing units (lead spacing 52.5 mm, P₁ = 20.3 mm)



C _R ¹⁾	Max. dimensions w x h x l	Ordering code (composition see below)	I _{RMS,max} ²⁾ 70 °C 10 kHz A	ESR _{typ} 70 °C 10 kHz mΩ	ESL _{typ} ³⁾ nH	tan δ	tan δ	Un- taped pcs./ MOQ
µF	mm					10 ⁻³	10 ⁻³	
V _{R,70 °C} = 800 V DC, V _{op,85 °C} = 700 V DC								
30	43.0 x 24.0 x 57.5	B32778T8306K000*	14.5	9.8	9.0	3.2	30.2	1120
45	30.0 x 45.0 x 57.5	B32778G8456+000	19.5	6.6	14.0	3.2	30.9	560
50	30.0 x 45.0 x 57.5	B32778G8506+000	20.0	6.3	13.0	3.2	30.9	560
55	35.0 x 50.0 x 57.5	B32778G8556+000	23.0	5.6	14.0	3.2	31.1	216
60	35.0 x 50.0 x 57.5	B32778G8606+000	23.5	5.1	15.0	3.3	31.2	216
80	130.0 x 24.0 x 57.5	B32778J8806K000*	37.5	3.6	4.0	3.2	30.2	160
90	45.0 x 57.0 x 57.5	B32778G8906+000	32.5	3.5	17.0	3.3	32.2	280
100	60.0 x 45.0 x 57.5	B32778G8107+000	34.5	3.2	19.0	3.3	31.9	400
270	130.0 x 58.0 x 57.5	B32778J8277K000*	70.5	1.2	6.0	3.5	32.4	80
V _{R,70 °C} = 900 V DC, V _{op,85 °C} = 800 V DC								
25	43.0 x 24.0 x 57.5	B32778T9256K000*	13.5	10.7	9.0	2.8	26.8	1120
35	30.0 x 45.0 x 57.5	B32778G9356+000	18.0	7.7	13.0	2.9	27.3	560
50	35.0 x 50.0 x 57.5	B32778G9506K000	22.5	5.6	15.0	2.9	27.7	216
70	45.0 x 57.0 x 57.5	B32778G9706+000	31.0	3.8	18.0	3.0	28.5	280
70	130.0 x 24.0 x 57.5	B32778J9706K000*	36.0	3.8	4.0	2.9	27.2	160
75	60.0 x 45.0 x 57.5	B32778G9756+000	32.5	3.6	20.0	2.9	28.2	400
210	130.0 x 58.0 x 57.5	B32778J9217K000*	66.0	1.3	6.0	3.1	28.6	80

MOQ = Minimum Order Quantity, consisting of 8 packing units.

Further intermediate capacitance values are available on request.

Composition of ordering code

+ = Capacitance tolerance code:

J = ±5%

K = ±10%

Packing code:

000 = untaped (lead length 6 –1 mm)

1) Capacitance value measured at 1 kHz

2) Max ripple current I_{RMS} at 70 °C, 10 kHz for ΔT ≤20 °C at ΔESR_{typ} ≤±5%

3) Typical ESL value measured at resonance frequency (see specific graphs of Z versus frequency)

* This part is affected by "Dual Use" regulations according to the law of the country the production site is located in. Deliveries of such products are subject to prior approval of the respective local authorities based on customer declarations. The delivery to certain countries may be restricted.

Capacitors for DC Link

B3277*D/E/G/J/T

MKP DC link – high density series up to 480 µF

Ordering codes and packing units (lead spacing 52.5 mm, P1 = 20.3 mm)



$C_R^{1)}$	Max. dimensions w x h x l	Ordering code (composition see below)	$I_{RMS,max}^{2)}$ 70 °C 10 kHz A	ESR_{typ} 70 °C 10 kHz mΩ	$ESL_{typ}^{3)}$ nH	$\tan \delta$ 10 ⁻³	$\tan \delta$ 10 ⁻³	Un- taped pcs./ MOQ
µF	mm							
$V_{R,70\text{ °C}} = 1100\text{ V DC}$, $V_{op,85\text{ °C}} = 920\text{ V DC}$								
20	43.0 x 24.0 x 57.5	B32778T0206K000*	13.0	11.9	9.0	2.6	24.1	1120
30	30.0 x 45.0 x 57.5	B32778G0306+000	17.5	8.2	13.0	2.6	24.5	560
40	35.0 x 50.0 x 57.5	B32778G0406+000	21.5	6.2	15.0	2.7	25.9	216
58	45.0 x 57.0 x 57.5	B32778G0586+000	29.0	4.3	17.0	2.7	25.4	280
60	60.0 x 45.0 x 57.5	B32778G0606+000	30.5	4.0	19.0	2.7	25.2	400
60	130.0 x 24.0 x 57.5	B32778J0606K000*	34.5	4.1	4.0	2.7	25.1	160
200	130.0 x 58.0 x 57.5	B32778J0207K000*	66.0	1.4	6.0	3.0	26.8	80
$V_{R,70\text{ °C}} = 1300\text{ V DC}$, $V_{op,85\text{ °C}} = 1100\text{ V DC}$								
14	43.0 x 24.0 x 57.5	B32778T1146K000*	12.0	13.8	9.0	2.1	19.5	1120
20	30.0 x 45.0 x 57.5	B32778G1206+000	16.0	9.7	13.0	2.1	19.8	560
25	35.0 x 50.0 x 57.5	B32778G1256+000	19.0	7.8	15.0	2.1	19.9	216
27	35.0 x 50.0 x 57.5	B32778G1276+000	19.5	7.3	15.0	2.1	20.0	216
38	130.0 x 24.0 x 57.5	B32778J1386K000*	31.5	5.1	4.0	2.1	19.5	160
40	45.0 x 57.0 x 57.5	B32778G1406+000	26.5	5.0	17.0	2.2	20.3	280
42	60.0 x 45.0 x 57.5	B32778G1426+000	28.0	4.7	19.0	2.2	20.2	400
120	130.0 x 58.0 x 57.5	B32778J1127K000*	58.5	1.7	6.0	2.3	20.5	80

MOQ = Minimum Order Quantity, consisting of 8 packing units.

Further intermediate capacitance values are available on request.

Composition of ordering code

+ = Capacitance tolerance code:

J = ±5%

K = ±10%

Packing code:

000 = untaped (lead length 6 –1 mm)

1) Capacitance value measured at 1 kHz

2) Max ripple current I_{RMS} at 70 °C, 10 kHz for $\Delta T \leq 20\text{ °C}$ at $\Delta ESR_{typ} \leq \pm 5\%$

3) Typical ESL value measured at resonance frequency (see specific graphs of Z versus frequency)

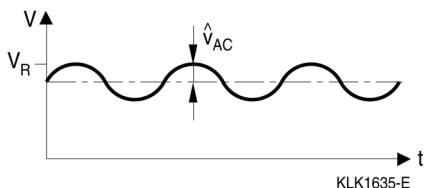
* This part is affected by "Dual Use" regulations according to the law of the country the production site is located in. Deliveries of such products are subject to prior approval of the respective local authorities based on customer declarations. The delivery to certain countries may be restricted.

Technical data

Reference standard: IEC 61071:2007. All data given at T = 20 °C, unless otherwise specified.

Operating temperature range (case)	Max. operating temperature, $T_{op,max}$ +105 °C Upper category temperature T_{max} +105 °C Lower category temperature T_{min} -40 °C					
Insulation resistance R_{ins} given as time constant $\tau = C_R \cdot R_{ins}$, rel. humidity ≤ 65% (minimum as-delivered values)	$\tau > 10\,000$ s (after 1 min) For $V_R \geq 500$ V measured at 500 V For $V_R < 500$ V measured at V_R					
DC test voltage between terminals (10 s)	$1.5 \cdot V_R$					
Voltage test terminal to case (10 s)	2110 V AC, 50 Hz					
Pulse Handling Capability (V/µs)	I_P (A) / C (µF)					
Reliability: Failure rate λ	10 fit ($\leq 10 \cdot 10^{-9}$ /h) at $0.5 \cdot V_R$, 40 °C For conversion to other operating conditions and temperatures, refer to chapter "Quality, 2 Reliability".					
Service life t_{SL}	100 000 h at V_R and 70 °C					
V_R (V DC)	450	575	800	900	1100	1300
Continuous operating voltage V_{op} (V DC) at 70 °C	450	575	800	900	1100	1300
Continuous operating voltage V_{op} (V DC) at 85 °C	450	500	700	800	920	1100
For temperatures between 85 °C and 105 °C	1.33%/°C of V_{op} derating compared to V_{op} at 85 °C					

Typical waveforms



Restrictions:

V_R : Maximum operating peak voltage of either polarity but of a non-reversing waveform, for which the capacitor has been designed for continuous operation.

Capacitors for DC Link

B3277*D/E/G/J/T

MKP DC link – high density series up to 480 µF

$$\hat{V}_{AC} \leq 0.2 \cdot V_R$$

Overvoltage	Maximum duration within one day	Observation
$1.1 \cdot V_R$	30% of on-load duration	System regulation
$1.15 \cdot V_R$	30 min	System regulation
$1.2 \cdot V_R$	5 min	System regulation
$1.3 \cdot V_R$	1 min	System regulation

NOTE 1 An overvoltage equal to $1.5 \cdot V_R$ for 30 ms is permitted 1000 times during the life of the capacitor.

The amplitudes of the overvoltages that may be tolerated without significant reduction in the life time of the capacitor depend on their duration, the number of application and the capacitor temperature.

In addition these values assume that the overvoltages may appear when the internal temperature of the capacitor is less than 0 °C but within the temperature category.

NOTE 2 The average applied voltage must not be higher than the specified voltage.

Pulse handling capability

"dV/dt" represents the maximum permissible voltage change per unit of time for non-sinusoidal voltages, expressed in V/µs.

Note:

The values of dV/dt provided below must not be exceeded in order to avoid damaging the capacitor.

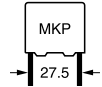
dV/dt values

Lead spacing	27.5 mm				37.5 mm					
Type	B32774				B32776					
V_R (V DC)	450	800	1100	1300	450	575	800	900	1100	1300
dV/dt in V/µs	30	40	75	100	21	22	22	35	54	73

Lead spacing	52.5 mm					
Type	B32778					
V_R (V DC)	450	575	800	900	1100	1300
dV/dt in V/µs	14	14	15	22	35	50

Characteristics curves

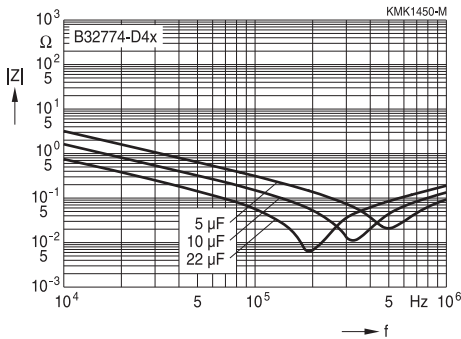
Additional technical information can be found under "Design support" on www.tdk-electronics.tdk.com.



Impedance Z versus frequency f (typical values)

Lead spacing 27.5 mm

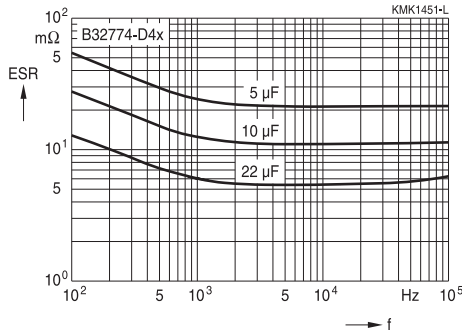
450 V DC



ESR versus frequency f (typical values)

Lead spacing 27.5 mm

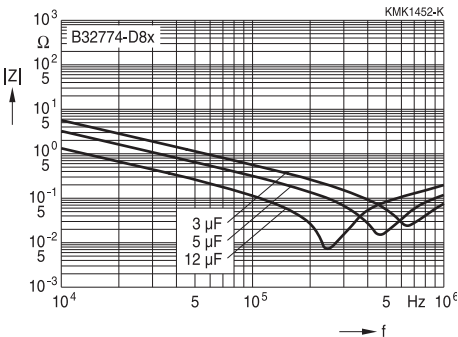
450 V DC



Impedance Z versus frequency f (typical values)

Lead spacing 27.5 mm

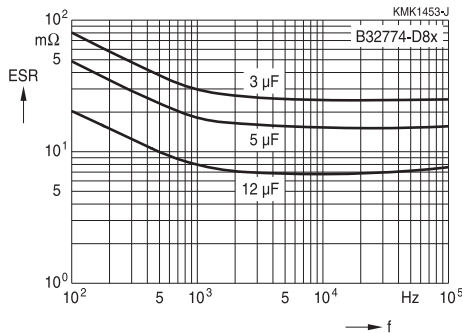
800 V DC



ESR versus frequency f (typical values)

Lead spacing 27.5 mm

800 V DC



Characteristics curves

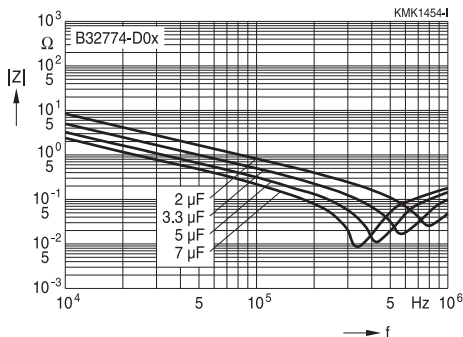
Additional technical information can be found under "Design support" on www.tdk-electronics.tdk.com.



Impedance Z versus frequency f (typical values)

Lead spacing 27.5 mm

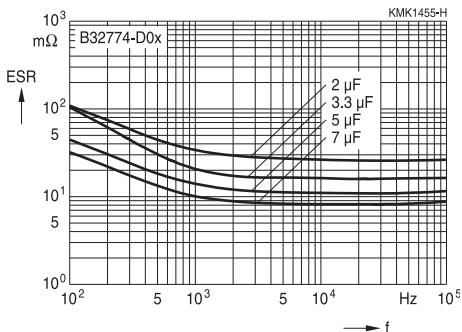
1100 V DC



ESR versus frequency f (typical values)

Lead spacing 27.5 mm

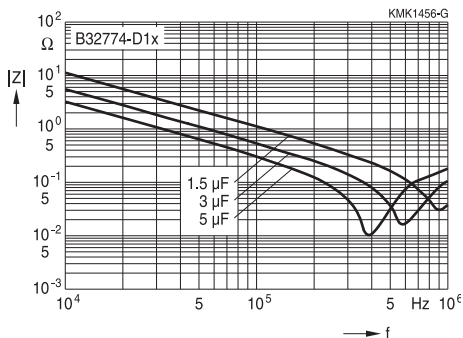
1100 V DC



Impedance Z versus frequency f (typical values)

Lead spacing 27.5 mm

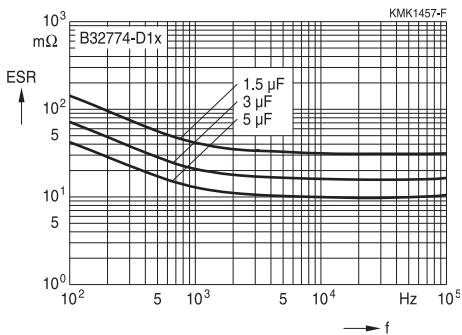
1300 V DC



ESR versus frequency f (typical values)

Lead spacing 27.5 mm

1300 V DC



Characteristics curves

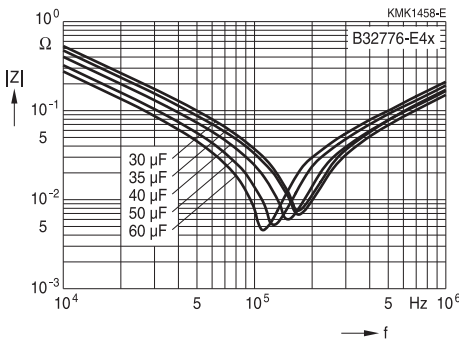
Additional technical information can be found under "Design support" on www.tdk-electronics.tdk.com.



Impedance Z versus frequency f (typical values)

Lead spacing 37.5 mm

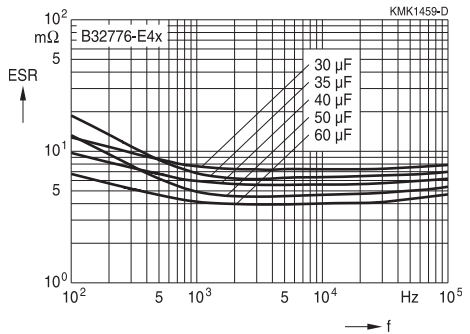
450 V DC



ESR versus frequency f (typical values)

Lead spacing 37.5 mm

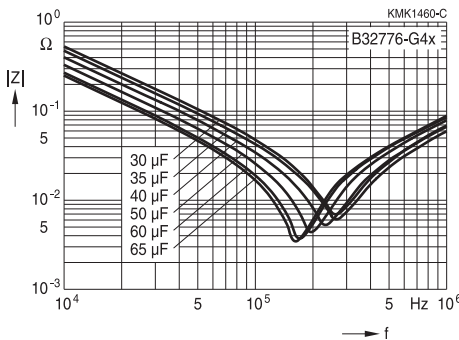
450 V DC



Impedance Z versus frequency f (typical values)

Lead spacing 37.5 mm

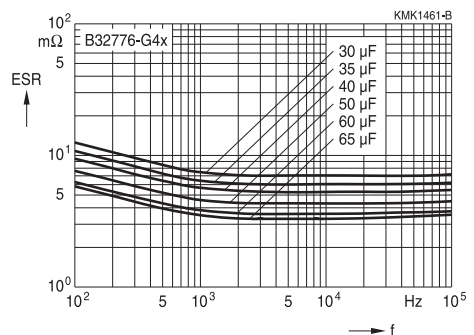
450 V DC



ESR versus frequency f (typical values)

Lead spacing 37.5 mm

450 V DC



Characteristics curves

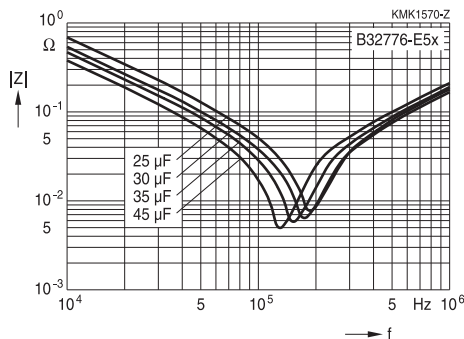
Additional technical information can be found under "Design support" on www.tdk-electronics.tdk.com.



Impedance Z versus frequency f (typical values)

Lead spacing 37.5 mm

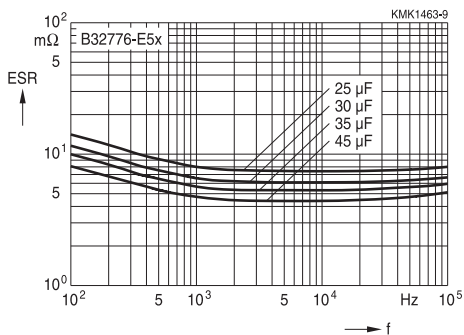
575 V DC



ESR versus frequency f (typical values)

Lead spacing 37.5 mm

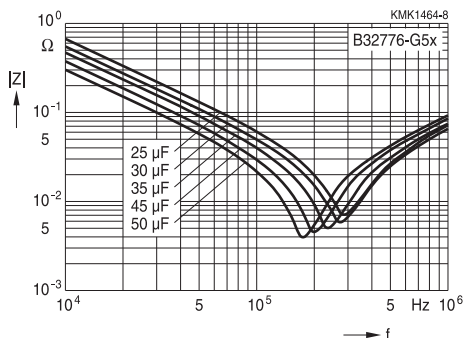
575 V DC



Impedance Z versus frequency f (typical values)

Lead spacing 37.5 mm

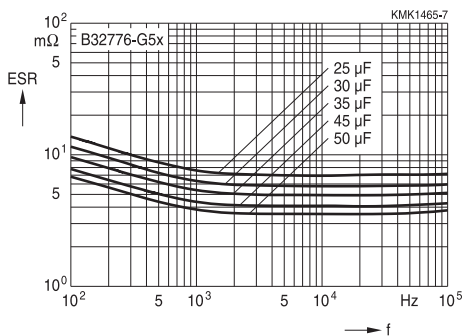
575 V DC



ESR versus frequency f (typical values)

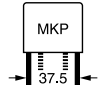
Lead spacing 37.5 mm

575 V DC



Characteristics curves

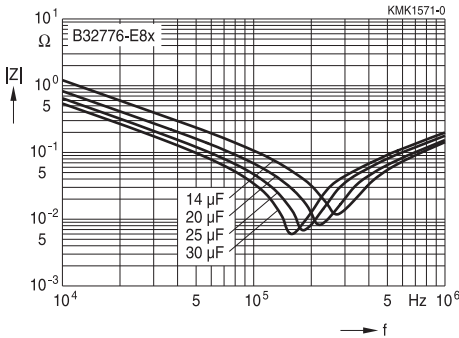
Additional technical information can be found under "Design support" on www.tdk-electronics.tdk.com.



Impedance Z versus frequency f (typical values)

Lead spacing 37.5 mm

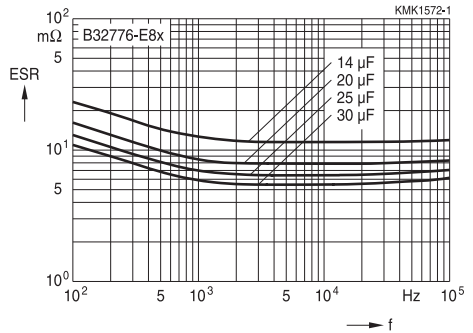
B32776E8*/800 V DC



ESR versus frequency f (typical values)

Lead spacing 37.5 mm

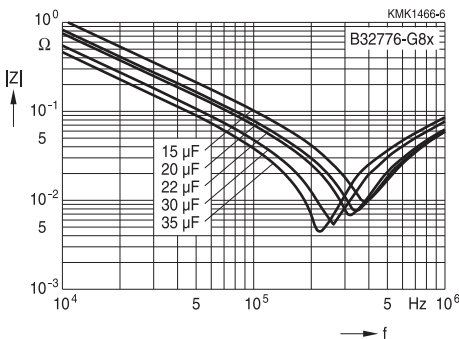
B32776E8*/800 V DC



Impedance Z versus frequency f (typical values)

Lead spacing 37.5 mm

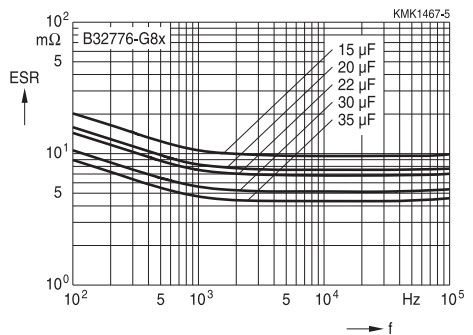
800 V DC



ESR versus frequency f (typical values)

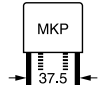
Lead spacing 37.5 mm

800 V DC



Characteristics curves

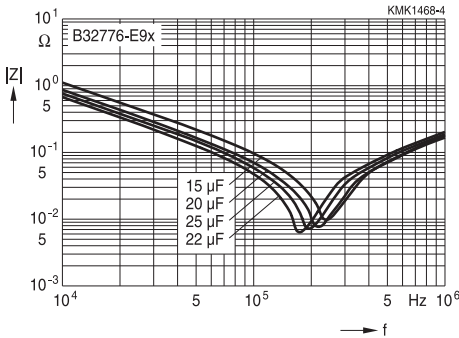
Additional technical information can be found under "Design support" on www.tdk-electronics.tdk.com.



Impedance Z versus frequency f (typical values)

Lead spacing 37.5 mm

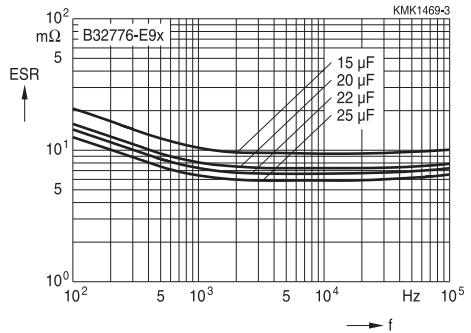
900 V DC



ESR versus frequency f (typical values)

Lead spacing 37.5 mm

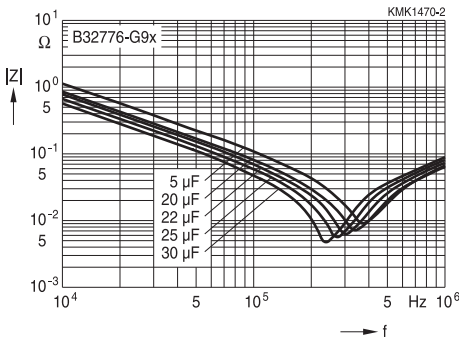
900 V DC



Impedance Z versus frequency f (typical values)

Lead spacing 37.5 mm

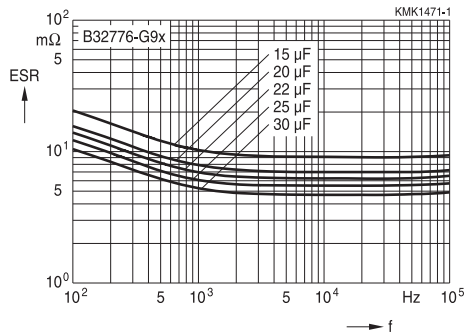
900 V DC



ESR versus frequency f (typical values)

Lead spacing 37.5 mm

900 V DC



Characteristics curves

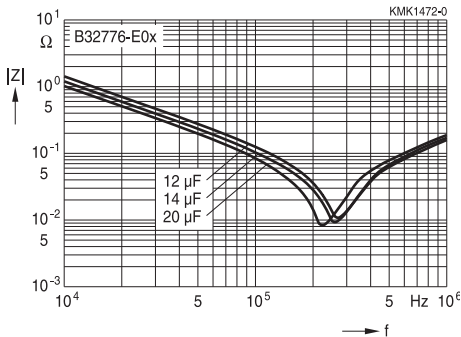
Additional technical information can be found under "Design support" on www.tdk-electronics.tdk.com.



Impedance Z versus frequency f (typical values)

Lead spacing 37.5 mm

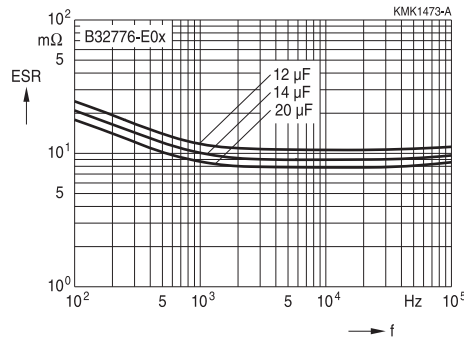
1100 V DC



ESR versus frequency f (typical values)

Lead spacing 37.5 mm

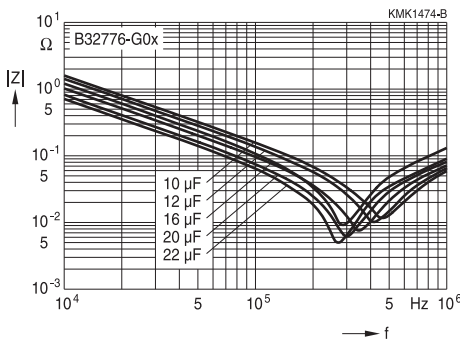
1100 V DC



Impedance Z versus frequency f (typical values)

Lead spacing 37.5 mm

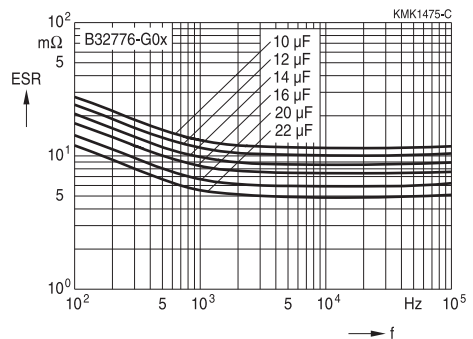
1100 V DC



ESR versus frequency f (typical values)

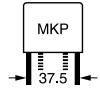
Lead spacing 37.5 mm

1100 V DC



Characteristics curves

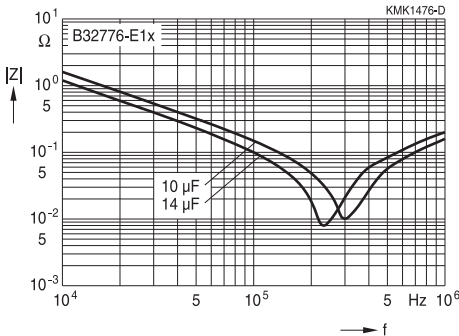
Additional technical information can be found under "Design support" on www.tdk-electronics.tdk.com.



Impedance Z versus frequency f (typical values)

Lead spacing 37.5 mm

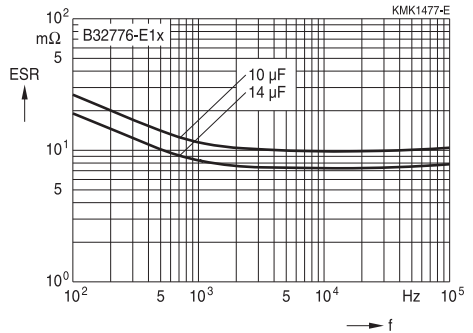
1300 V DC



ESR versus frequency f (typical values)

Lead spacing 37.5 mm

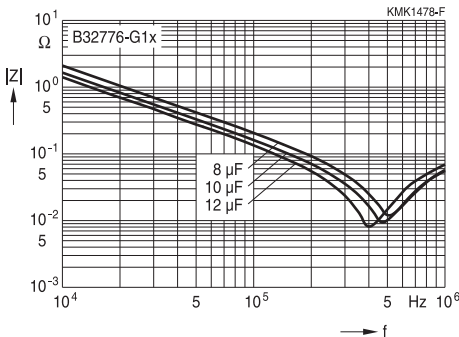
1300 V DC



Impedance Z versus frequency f (typical values)

Lead spacing 37.5 mm

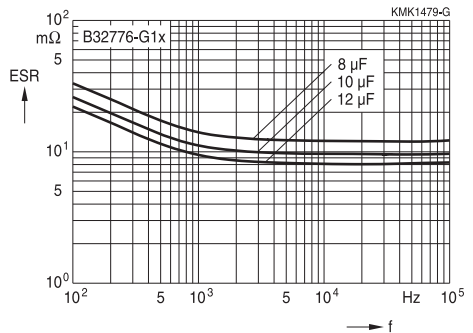
1300 V DC



ESR versus frequency f (typical values)

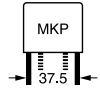
Lead spacing 37.5 mm

1300 V DC



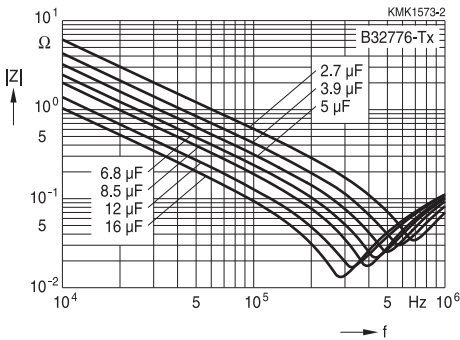
Characteristics curves

Additional technical information can be found under "Design support" on www.tdk-electronics.tdk.com.



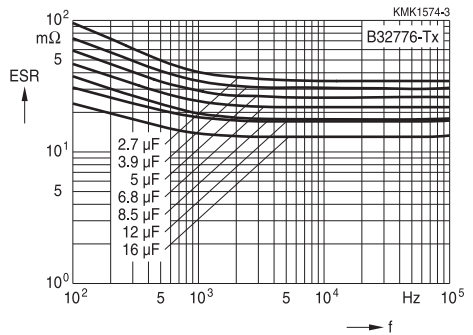
Impedance Z versus frequency f (typical values)

Lead spacing 37.5 mm (low profile, 2 pins)



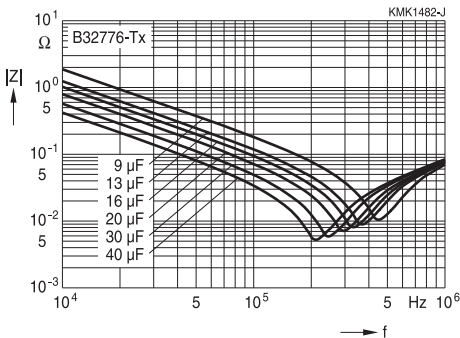
ESR versus frequency f (typical values)

Lead spacing 37.5 mm (low profile, 2 pins)



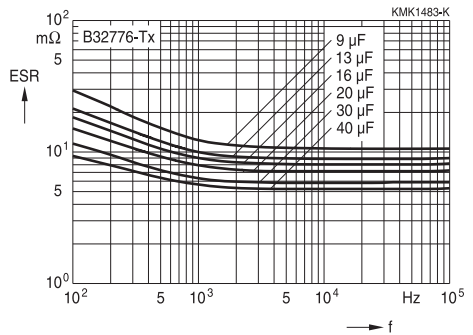
Impedance Z versus frequency f (typical values)

Lead spacing 37.5 mm (low profile, 4 pins)



ESR versus frequency f (typical values)

Lead spacing 37.5 mm (low profile, 4 pins)



Characteristics curves

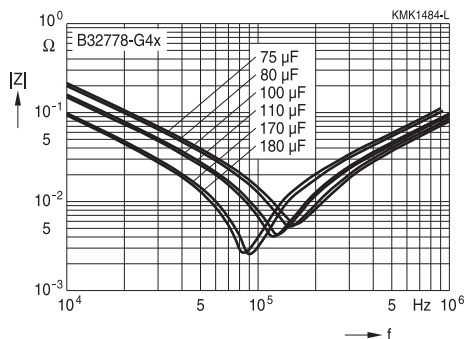
Additional technical information can be found under "Design support" on www.tdk-electronics.tdk.com.



Impedance Z versus frequency f (typical values)

Lead spacing 52.5 mm

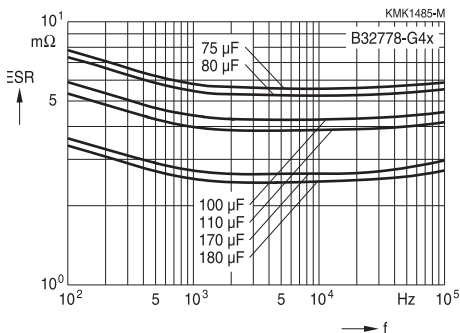
450 V DC



ESR versus frequency f (typical values)

Lead spacing 52.5 mm

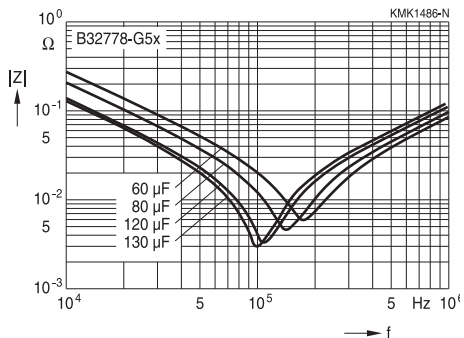
450 V DC



Impedance Z versus frequency f (typical values)

Lead spacing 52.5 mm

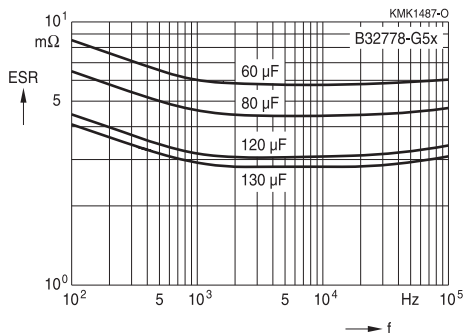
575 V DC



ESR versus frequency f (typical values)

Lead spacing 52.5 mm

575 V DC



Characteristics curves

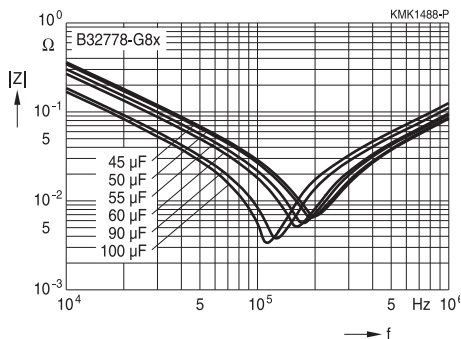
Additional technical information can be found under "Design support" on www.tdk-electronics.tdk.com.



Impedance Z versus frequency f (typical values)

Lead spacing 52.5 mm

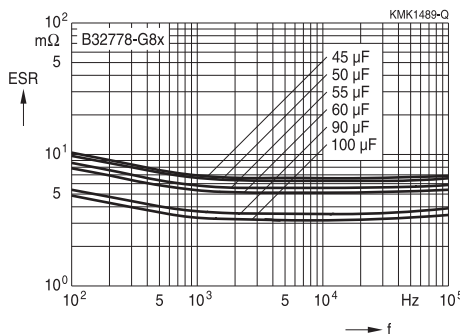
800 V DC



ESR versus frequency f (typical values)

Lead spacing 52.5 mm

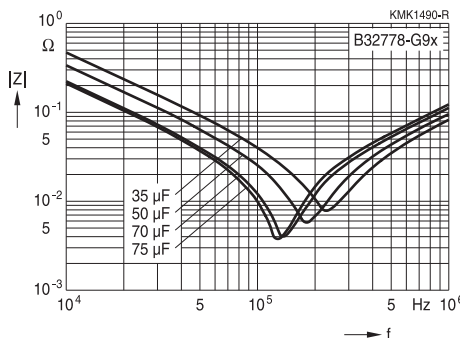
800 V DC



Impedance Z versus frequency f (typical values)

Lead spacing 52.5 mm

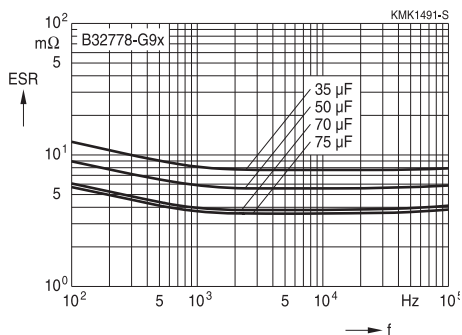
900 V DC



ESR versus frequency f (typical values)

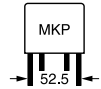
Lead spacing 52.5 mm

900 V DC



Characteristics curves

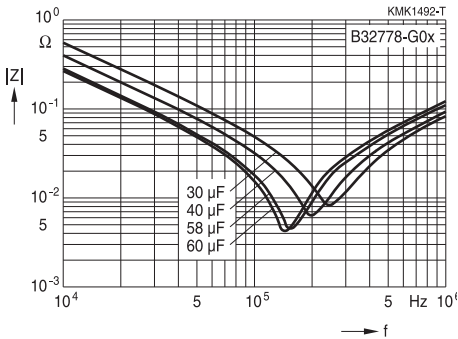
Additional technical information can be found under "Design support" on www.tdk-electronics.tdk.com.



Impedance Z versus frequency f (typical values)

Lead spacing 52.5 mm

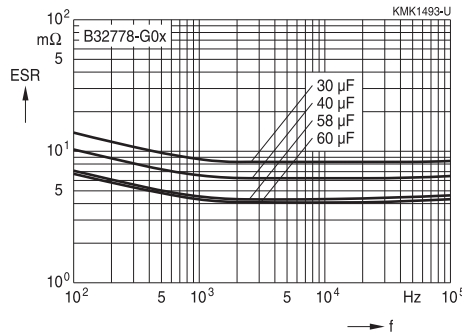
B32778G0*/1100 V DC



ESR versus frequency f (typical values)

Lead spacing 52.5 mm

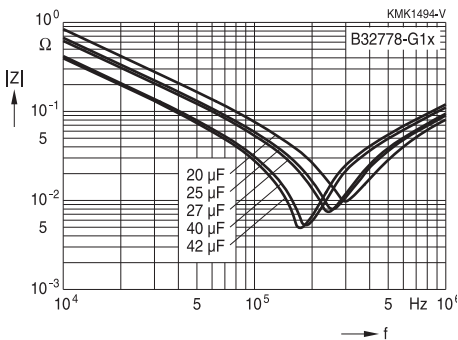
B32778G0*/1100 V DC



Impedance Z versus frequency f (typical values)

Lead spacing 52.5 mm

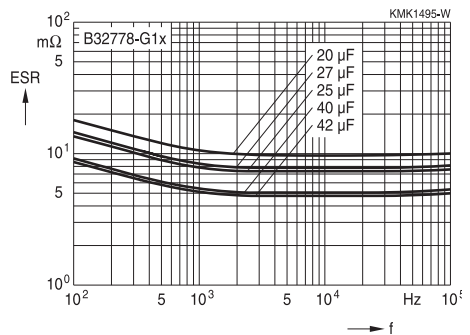
1300 V DC



ESR versus frequency f (typical values)

Lead spacing 52.5 mm

1300 V DC



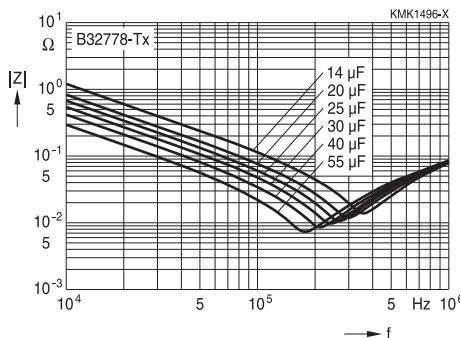
Characteristics curves

Additional technical information can be found under "Design support" on www.tdk-electronics.tdk.com.



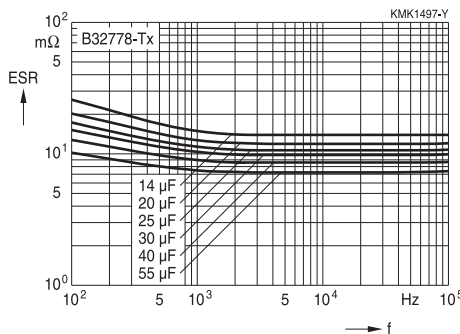
Impedance Z versus frequency f (typical values)

Lead spacing 52.5 mm (low profile)



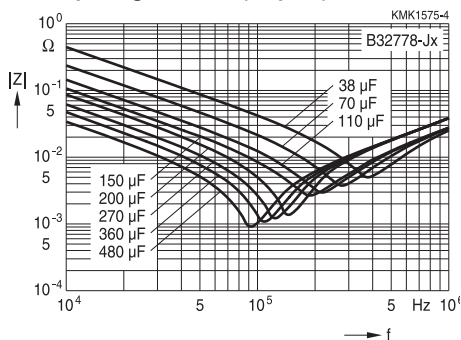
ESR versus frequency f (typical values)

Lead spacing 52.5 mm (low profile)



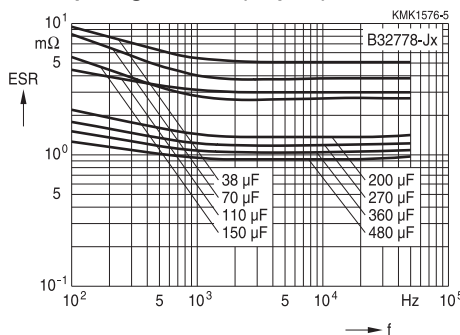
Impedance Z versus frequency f (typical values)

Lead spacing 52.5 mm (12 pins)



ESR versus frequency f (typical values)

Lead spacing 52.5 mm (12 pins)

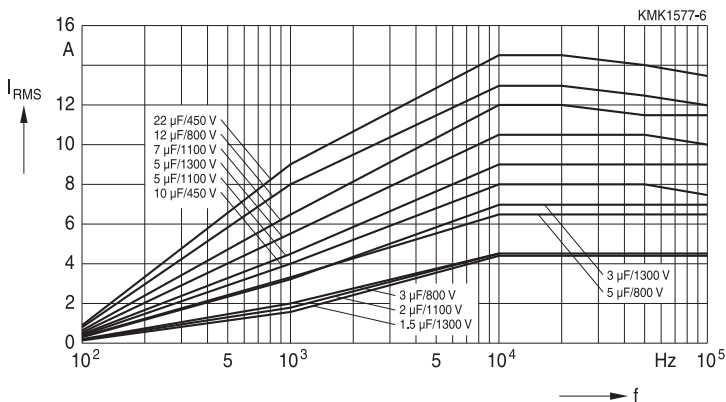


Characteristics curves

Permissible current I_{RMS} versus frequency f at 70 °C

Lead spacing 27.5 mm

B32774D*



MKP DC link – high density series up to 480 µF

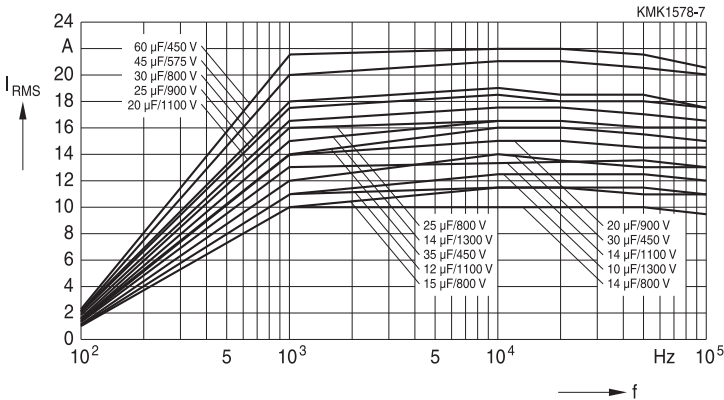
Characteristics curves

Permissible current I_{RMS} versus frequency f at 70 °C

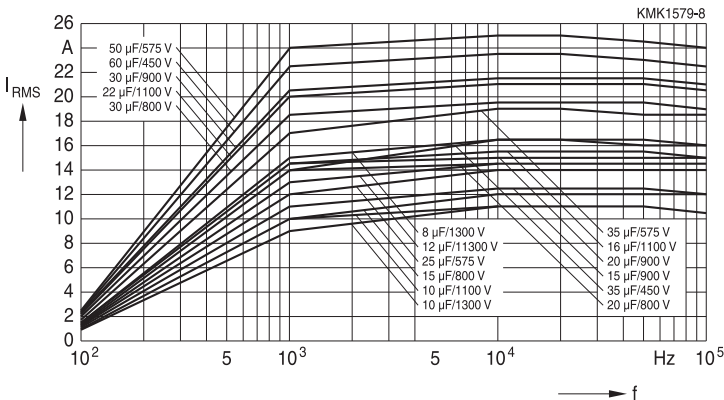


Lead spacing 37.5 mm

B32776E*



B32776G*

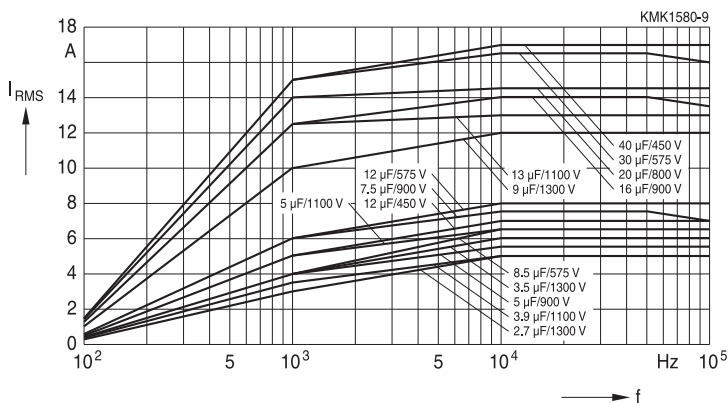


Characteristics curves

Permissible current I_{RMS} versus frequency f at 70 °C

Lead spacing 37.5 mm

B32776T*

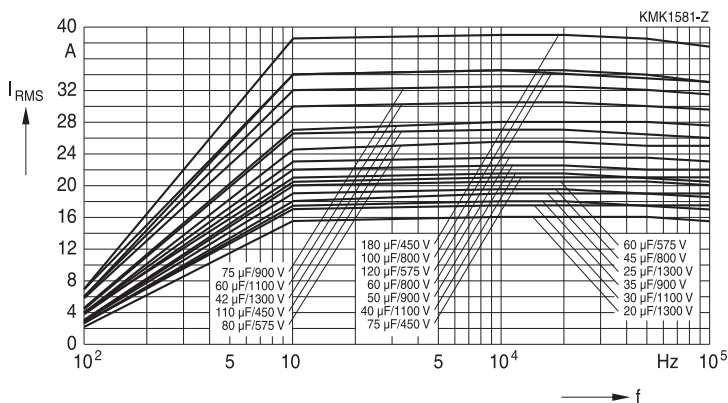


Characteristics curves

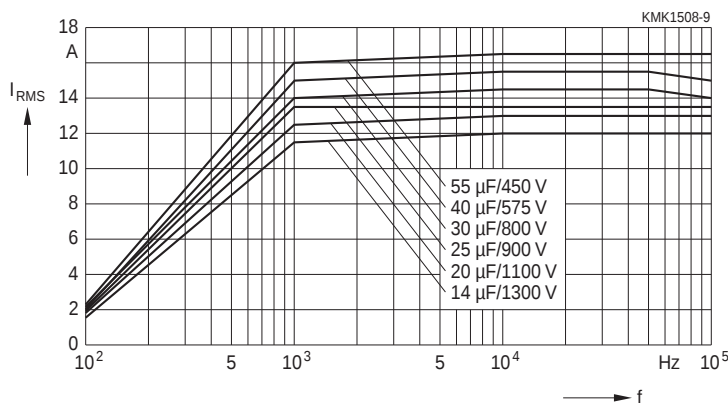
Permissible current I_{RMS} versus frequency f at 70 °C

Lead spacing 52.5 mm

B32778G* (4 pins)



B32778T* (4 pins)

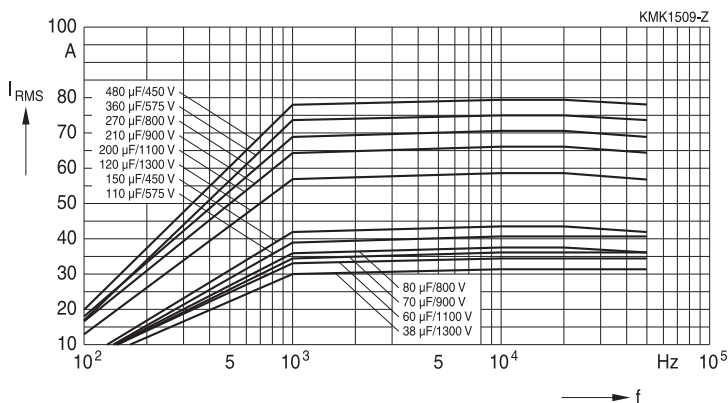


Characteristics curves

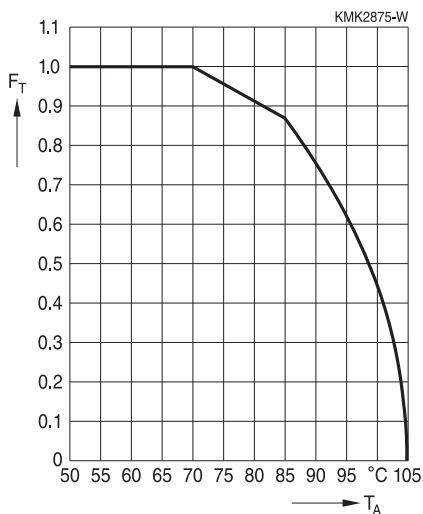
Permissible current I_{RMS} versus frequency f at 70 °C

Lead spacing 52.5 mm

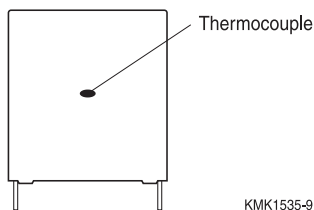
B32778J* (12 pins)



Curves characteristics (I_{RMS} derating versus temperature)



Maximum I_{RMS} current as function of the ambient temperature: $I_{RMS}(T_A) = \text{Factor} \times I_{RMS}(70\text{ °C})$

Heat transference for self heating calculation

Figure 1

Box dimensions			Equivalent heat coefficient
w (mm)	h (mm)	l (mm)	G (mW/°C)
11.0	19.0	31.5	25
11.0	21.0	31.5	28
12.5	21.5	31.5	30
13.5	23.0	31.5	32
14.0	24.5	31.5	35
15.0	24.5	31.5	36
16.0	32.0	31.5	45
18.0	27.5	31.5	44
18.0	33.0	31.5	48
19.0	30.0	31.5	48
20.0	11.0	31.5	65
21.0	31.0	31.5	51
22.0	36.5	31.5	58
12.0	22.0	41.5	70
14.0	25.0	41.5	43
16.0	28.5	41.5	50
18.0	32.5	41.5	59
20.0	39.5	41.5	72
24.0	19.0	41.5	50
24.0	15.0	41.5	44
28.0	37.0	42.0	83
28.0	42.5	41.5	90
30.0	45.0	42.0	100
33.0	48.0	42.0	100
43.0	22.0	41.5	80
30.0	45.0	57.5	125
35.0	50.0	57.5	145
43.0	24.0	57.5	103
45.0	57.0	57.5	185
60.0	45.0	57.5	192
130.0	24.0	57.5	200
130.0	58.0	57.5	300

The equivalent heat coefficient "**G (mW/°C)**" is given for measuring the temperature on the lateral surface of the plastic box as Figure 1 shows. By using a thermocouple and avoiding effect of radiation and convection the temperature measured during operation conditions should be a result of the dissipated power divided by the equivalent heat coefficient.

Self Heating by power dissipation & equivalent heat coefficient

The I_{RMS} and consequently the power dissipation must be limited during operation in order to not exceed the maximum limit of ΔT allowed for this series. ΔT_{max} given for this series is equal or lower than 20 °C at rated temperature (70 °C), for higher ambient temperatures $\Delta T_{max}(T)$ will have the same derating factor than I_{RMS} versus temperature and then an equivalent derating as per: ambient temperatures $\Delta T_{max}(T)$ will have the same derating factor than I_{RMS} versus temperature and then an equivalent derating as per:

$$\Delta T_{max}(T) = (\text{Factor})^2 \times \Delta T(70\text{ °C}).$$

For any particular I_{RMS} the ΔT may be calculated by:

$$\Delta T(\text{°C}) = P_{dis}(\text{mW}) / G(\text{mW/°C}).$$

Where $\Delta T(\text{°C})$ is the difference between the temperature measured on the box (see Figure 1) and the ambient temperature when capacitor is working during normal operation;

$$\Delta T(\text{°C}) = T_{op}(\text{°C}) - T_A(\text{°C}).$$

It represents the increasing of temperature provoked by the I_{RMS} during operation.

$G(\text{mW/°C})$ is the equivalent heat coefficient described above and $P_{dis}(\text{mW})$ is the dissipated power defined by:

$$P_{dis}(\text{mW}) = ESR_{typ}(\text{m}\Omega) \times I_{RMS}^2(A_{RMS}).$$

Example for thermal calculation:

We will take as reference B32778G0306K (30 µF/1100 V) type for thermal calculation. Considering the following load and capacitor characteristics:

I_{RMS} : 12 A_{RMS} at 20 kHz T_A : 85 °C 30 × 45 × 57.5 box

$G(\text{mW/°C})$: 125

Then we have to find the ESR_{typ} at 20 kHz what is approx . 8.2 mΩ.

So according to:

$$P_{dis}(\text{mW}) = ESR_{typ}(\text{m}\Omega) \times I_{RMS}^2(A_{RMS})$$

we have the following:

$$P_{dis}(\text{mW}) = 8.2\text{ m}\Omega \times 12\text{ }A_{RMS}^2 = 1181\text{ mW}$$

and as per:

$$\Delta T(\text{°C}) = P_{dis}(\text{mW}) / G(\text{mW/°C})$$

we have the following:

$$\Delta T(\text{°C}) = 1181(\text{mW}) / 125(\text{mW/°C}) = 9.5\text{ °C}.$$

What is below of the

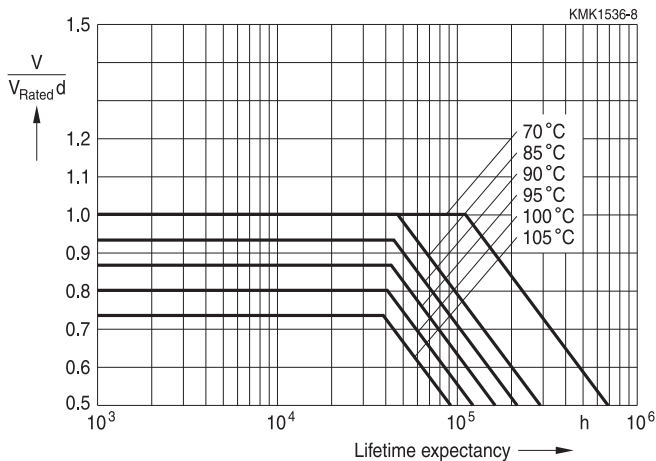
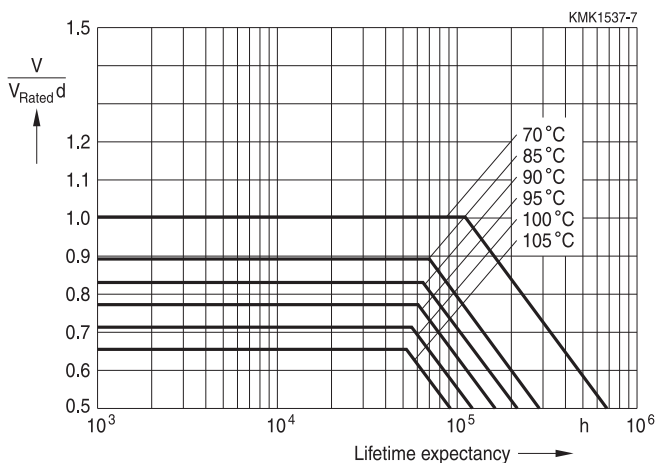
$$\Delta T_{max}(85\text{ °C}) = (\text{Factor})^2 \times \Delta T(70\text{ °C}) = (0.7)^2 \times 20\text{ °C} = 9.8\text{ °C}.$$

On the other hand we may confirm that max I_{RMS} at 20 kHz at 70 °C = 17.5 A_{RMS}

And then max I_{RMS} for 85 °C of ambient temperature is defined as follows:

$$I_{RMS}(85\text{ °C}) = \text{Factor} \times I_{RMS}(70\text{ °C}) = 0.7 \times 17.5\text{ }A_{RMS} = 12.25\text{ }A_{RMS}.$$

What confirms once again that I_{RMS} (12 A_{RMS} at 20 kHz) is below the max specified for such frequency and ambient temperature.

Life time expectancy - typical curves
B3277x-4 (450 V DC)

B3277x-5/8/9/0/1 (575 V DC / 800 V DC / 900 V DC / 1100 V DC / 1300 V DC)

Note: Confidence level of 95%

Testing and Standards

Test	Reference	Conditions of test	Performance requirements
Electrical parameters (Routine test)	IEC61071:2007	Voltage between terminals, 1.5 V _R , during 10 s Insulation resistance, R _{ins} at V _R if V _R < 500 V or 500 V if V _R ≥ 500 V Capacitance, C at 1 kHz (room temperature) Dissipation factor, tan δ at 1/10 kHz (room temperature)	Within specified limits
Robustness of terminations (Type test)	IEC 60068-2-21:2006	Tensile strength (test Ua1) Wire diameter Tensile force 0.5 < d ₁ ≤ 0.8 mm 10 N 0.8 < d ₁ ≤ 1.25 mm 20 N	Capacitance and tan δ within specified limits
Resistance to soldering heat (Type test)	IEC 60068-2-20:2008, test Tb, method 1A	Solder bath temperature at 260 ± 5 °C, immersion for 10 seconds	ΔC/C ₀ ≤ 2% Δ tan δ ≤ 0.002
Rapid change of temperature (Type test)	IEC 60384-16:2005	T _A = lower category temperature T _B = upper category temperature Five cycles, duration t = 30 min	ΔC/C ₀ ≤ 2% Δ tan δ ≤ 0.002 R _{ins} ≥ 50% of initial limit
Vibration (Type test)	IEC 60384-16:2005	Test F _C : vibration sinusoidal Displacement: 0.75 mm Acceleration: 98 m/s ² Frequency: 10 Hz ... 500 Hz Test duration: 3 orthogonal axes, 2 hours each axe	No visible damage
Bump (Type test)	IEC 60384-16:2005	Test Eb: Total 4000 bumps with 390 m/s ² mounted on PCB 6 ms duration	No visible damage ΔC/C ₀ ≤ 2% Δ tan δ ≤ 0.002 R _{ins} ≥ 50% of initial limit
Climatic sequence (Type test)	IEC 60384-16:2005	Dry heat Tb / 16 h Damp heat cyclic, 1 st cycle +55 °C / 24 h / 95% ... 100% RH Cold Ta / 2 h Damp heat cyclic, 5 cycles +55 °C / 24 h / 95% ... 100% RH	No visible damage ΔC/C ₀ ≤ 3% Δ tan δ ≤ 0.001 R _{ins} ≥ 50% of initial limit

Capacitors for DC Link

B3277*D/E/G/J/T

MKP DC link – high density series up to 480 µF

Test	Reference	Conditions of test	Performance requirements
Damp heat, steady state (Type test)	IEC 60384-16:2005	Test Ca 40 °C / 93% RH / 56 days	No visible damage $ \Delta C/C_0 \leq 5\%$ $ \Delta \tan \delta \leq 0.005$ $R_{ins} \geq 50\%$ of initial limit
Endurance (Type test)	IEC 60384-16:2005	70 °C / 1.25 V_R / 1000 hours or 85 °C / 1.25 V_{op} / 1000 hours or 100 °C / 1.25 V_{op} / 1000 hours	No visible damage $ \Delta C/C_0 \leq 5\%$ at 1 kHz $ \Delta \tan \delta \leq 0.005$ $R_{ins} \geq 50\%$ of initial limit

Mounting guidelines

1 Soldering

1.1 Solderability of leads

The solderability of terminal leads is tested to IEC 60068-2-20:2021, test Ta, method 1.

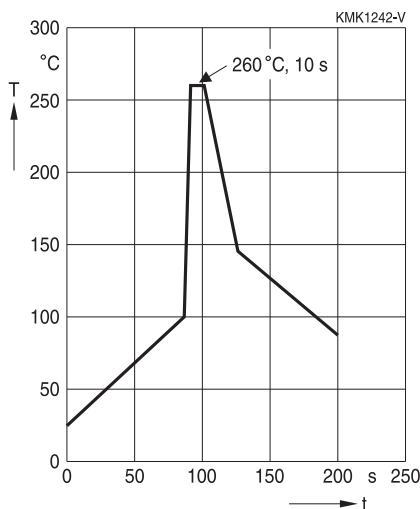
Before a solderability test is carried out, terminals are subjected to accelerated ageing (to IEC 60068-2-2:2007, test B: 4 h exposure to dry heat at 155 °C). Since the ageing temperature is far higher than the upper category temperature of the capacitors, the terminal wires should be cut off from the capacitor before the ageing procedure to prevent the solderability being impaired by the products of any capacitor decomposition that might occur.

Solder bath temperature	245 ±3 °C
Soldering time	3.0 ±0.3 s
Immersion depth	2.0 +0/–0.5 mm from capacitor body or seating plane
Evaluation criteria:	
Visual inspection	Wetting of wire surface by new solder ≥95%, free-flowing solder

1.2 Resistance to soldering heat

Resistance to soldering heat is tested to IEC 60068-2-20:2021, test Tb, method 1. Conditions:

Series		Solder bath temperature	Soldering time
MKT	boxed (except 2.5 × 6.5 × 7.2 mm) coated uncoated (lead spacing >10 mm)	260 ±5 °C	10 ±1 s
MFP			
MKP	(lead spacing >7.5 mm)		
MKT	boxed (case 2.5 × 6.5 × 7.2 mm)		5 ±0.5 s
MKP	(lead spacing ≤7.5 mm)		<4 s recommended soldering profile for MKT uncoated (lead spacing ≤10 mm) and insulated (B32559)
MKT	uncoated (lead spacing ≤10 mm) insulated (B32559)		



Immersion depth	2.0 to 2.5 mm from capacitor body or seating plane
Shield	Heat-absorbing board, (1.5 ±0.5) mm thick, between capacitor body and liquid solder
Evaluation criteria:	
Visual inspection	No visible damage
$\Delta C/C_0$	2% for MKT/MKP/MFP 5% for EMI suppression capacitors
$\tan \delta$	As specified in sectional specification

1.3 General notes on soldering

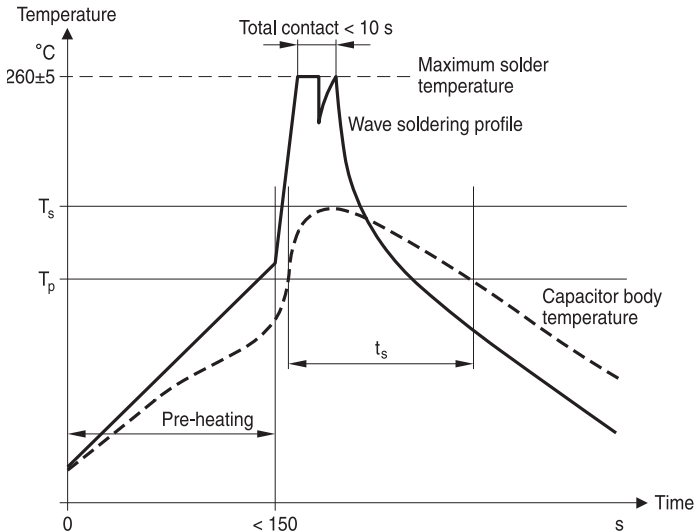
Permissible heat exposure loads on film capacitors are primarily characterized by the upper category temperature $T_{\max}$. Long exposure to temperatures above this type-related temperature limit can lead to changes in the plastic dielectric and thus change irreversibly a capacitor's electrical characteristics. For short exposures (as in practical soldering processes) the heat load (and thus the possible effects on a capacitor) will also depend on other factors like:

- Pre-heating temperature and time
- Forced cooling immediately after soldering
- Terminal characteristics:
diameter, length, thermal resistance, special configurations (e.g. crimping)
- Height of capacitor above solder bath
- Shadowing by neighboring components
- Additional heating due to heat dissipation by neighboring components
- Use of solder-resist coatings

The overheating associated with some of these factors can usually be reduced by suitable countermeasures. For example, if a pre-heating step cannot be avoided, an additional or reinforced cooling process may possibly have to be included.

TDK Recommendations

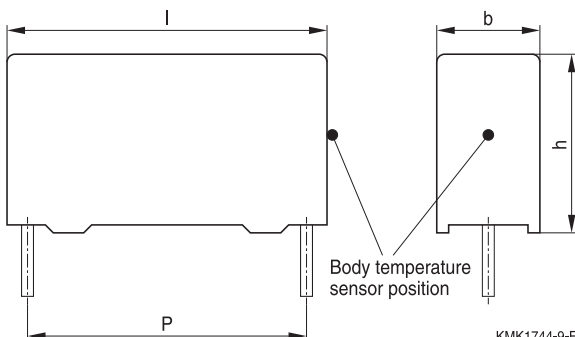
As a reference, the recommended wave soldering profile for our film capacitors is as follows:



T_s : Capacitor body maximum temperature at wave soldering

T_p : Capacitor body maximum temperature at pre-heating

KMK1745-A-E



KMK1744-9-E

Body temperature should follow the description below:

- MKP capacitor
 - During pre-heating: $T_p \leq 110\text{ }^{\circ}\text{C}$
 - During soldering: $T_s \leq 120\text{ }^{\circ}\text{C}$, $t_s \leq 45\text{ s}$
- MKT capacitor
 - During pre-heating: $T_p \leq 125\text{ }^{\circ}\text{C}$
 - During soldering: $T_s \leq 160\text{ }^{\circ}\text{C}$, $t_s \leq 45\text{ s}$

When SMD components are used together with leaded ones, the film capacitors should not pass into the SMD adhesive curing oven. The leaded components should be assembled after the SMD curing step.

Leaded film capacitors are not suitable for reflow soldering.

In order to ensure proper conditions for manual or selective soldering, the body temperature of the capacitor (T_s) must be $\leq 120\text{ }^{\circ}\text{C}$.

One recommended condition for manual soldering is that the tip of the soldering iron should be $< 360\text{ }^{\circ}\text{C}$ and the soldering contact time should be no longer than 3 seconds.

For uncoated MKT capacitors with lead spacings $\leq 10\text{ mm}$ (B32560/B32561) the following measures are recommended:

- pre-heating to not more than $110\text{ }^{\circ}\text{C}$ in the preheater phase
- rapid cooling after soldering

Please refer to our Film Capacitors Data Book in case more details are needed.

Cautions and warnings

- Do not exceed the upper category temperature (UCT).
- Do not apply any mechanical stress to the capacitor terminals.
- Avoid any compressive, tensile or flexural stress.
- Do not move the capacitor after it has been soldered to the PC board.
- Do not pick up the PC board by the soldered capacitor.
- Do not place the capacitor on a PC board whose PTH hole spacing differs from the specified lead spacing.
- Do not exceed the specified time or temperature limits during soldering.
- Avoid external energy inputs, such as fire or electricity.
- Avoid overload of the capacitors.
- Consult us if application is with severe temperature and humidity condition.
- There are no serviceable or repairable parts inside the capacitor. Opening the capacitor or any attempts to open or repair the capacitor will void the warranty and liability of TDK Electronics.
- Please note that the standards referred to in this publication may have been revised in the meantime.

The table below summarizes the safety instructions that must always be observed. A detailed description can be found in the relevant sections of the chapters "General technical information" and "Mounting guidelines".

Topic	Safety information	Reference chapter "General technical information"
Storage conditions	Make sure that capacitors are stored within the specified range of time, temperature and humidity conditions.	4.5 "Storage conditions"
Flammability	Avoid external energy, such as fire or electricity (passive flammability), avoid overload of the capacitors (active flammability) and consider the flammability of materials.	5.3 "Flammability"
Resistance to vibration	Do not exceed the tested ability to withstand vibration. The capacitors are tested to IEC 60068-2-6:2007. TDK Electronics offers film capacitors specially designed for operation under more severe vibration regimes such as those found in automotive applications. Consult our catalog "Film Capacitors for Automotive Electronics".	5.2 "Resistance to vibration"
Soldering	Do not exceed the specified time or temperature limits during soldering.	1 "Soldering"
Cleaning	Use only suitable solvents for cleaning capacitors.	2 "Cleaning"
Embedding of capacitors in finished assemblies	When embedding finished circuit assemblies in plastic resins, chemical and thermal influences must be taken into account. Caution: Consult us first, if you also wish to embed other uncoated component types!	3 "Embedding of capacitors in finished assemblies"

Display of ordering codes for TDK Electronics products

The ordering code for one and the same product can be represented differently in data sheets, data books, other publications, on the company website, or in order-related documents such as shipping notes, order confirmations and product labels. The varying representations of the ordering codes are due to different processes employed and do not affect the specifications of the respective products.

Detailed information can be found on the Internet under www.tdk-electronics.tdk.com/orderingcodes.

Correlation of data sheet values and modelling tool outputs

Data sheet values and results of design tools may deviate as they have not been derived in the same context.

While data sheets show individual parameter statements without considering a possible dependency to other parameters. Tools model a complete given scenario as input and processed inside the tool.

Furthermore as we constantly strive to improve our models, the results of tools can change over time and be a non-binding indication only.

Symbols and terms

Symbol	English	German
α	Heat transfer coefficient	Wärmeübergangszahl
α_C	Temperature coefficient of capacitance	Temperaturkoeffizient der Kapazität
A	Capacitor surface area	Kondensatoroberfläche
β_C	Humidity coefficient of capacitance	Feuchtekoeffizient der Kapazität
C	Capacitance	Kapazität
C_R	Rated capacitance	Nennkapazität
ΔC	Absolute capacitance change	Absolute Kapazitätsänderung
$\Delta C/C$	Relative capacitance change (relative deviation of actual value)	Relative Kapazitätsänderung (relative Abweichung vom Ist-Wert)
$\Delta C/C_R$	Capacitance tolerance (relative deviation from rated capacitance)	Kapazitätstoleranz (relative Abweichung vom Nennwert)
dt	Time differential	Differentielle Zeit
Δt	Time interval	Zeitintervall
ΔT	Absolute temperature change (self-heating)	Absolute Temperaturänderung (Selbsterwärmung)
$\Delta \tan \delta$	Absolute change of dissipation factor	Absolute Änderung des Verlustfaktors
ΔV	Absolute voltage change	Absolute Spannungsänderung
dV/dt	Time differential of voltage function (rate of voltage rise)	Differentielle Spannungsänderung (Spannungsflankensteilheit)
$\Delta V/\Delta t$	Voltage change per time interval	Spannungsänderung pro Zeitintervall
E	Activation energy for diffusion	Aktivierungsenergie zur Diffusion
ESL	Self-inductance	Eigeninduktivität
ESR	Equivalent series resistance	Ersatz-Serienwiderstand
f	Frequency	Frequenz
f_1	Frequency limit for reducing permissible AC voltage due to thermal limits	Grenzfrequenz für thermisch bedingte Reduzierung der zulässigen Wechsel- spannung
f_2	Frequency limit for reducing permissible AC voltage due to current limit	Grenzfrequenz für strombedingte Redu- zierung der zulässigen Wechselspannung
f_r	Resonant frequency	Resonanzfrequenz
F_D	Thermal acceleration factor for diffusion	Therm. Beschleunigungsfaktor zur Diffusion
F_T	Derating factor	Deratingfaktor
i	Current (peak)	Stromspitze
I_C	Category current (max. continuous current)	Kategoriestrom (max. Dauerstrom)
I_{RMS}	(Sinusoidal) alternating current, root-mean- square value	(Sinusförmiger) Wechselstrom
i_z	Capacitance drift	Inkonstanz der Kapazität
k_0	Pulse characteristic	Impuls-kennwert
L_S	Series inductance	Serieninduktivität
λ	Failure rate	Ausfallrate
λ_0	Constant failure rate during useful service life	Konstante Ausfallrate in der Nutzungsphase

Symbol	English	German
λ_{test}	Failure rate, determined by tests	Experimentell ermittelte Ausfallrate
P_{diss}	Dissipated power	Abgegebene Verlustleistung
P_{gen}	Generated power	Erzeugte Verlustleistung
Q	Heat energy	Wärmeenergie
ρ	Density of water vapor in air	Dichte von Wasserdampf in Luft
R	Universal molar constant for gases	Allg. Molarkonstante für Gas
R	Ohmic resistance of discharge circuit	Ohmscher Widerstand des Entladekreises
R_i	Internal resistance	Innenwiderstand
R_{ins}	Insulation resistance	Isolationswiderstand
R_p	Parallel resistance	Parallelwiderstand
R_s	Series resistance	Serienwiderstand
S	severity (humidity test)	Schärfegrad (Feuchtetest)
t	Time	Zeit
T	Temperature	Temperatur
τ	Time constant	Zeitkonstante
$\tan \delta$	Dissipation factor	Verlustfaktor
$\tan \delta_D$	Dielectric component of dissipation factor	Dielektrischer Anteil des Verlustfaktors
$\tan \delta_p$	Parallel component of dissipation factor	Parallelanteil des Verlustfaktors
$\tan \delta_s$	Series component of dissipation factor	Serienanteil des Verlustfaktors
T_A	Temperature of the air surrounding the component	Temperatur der Luft, die das Bauteil umgibt
T_{max}	Upper category temperature	Obere Kategorietemperatur
T_{min}	Lower category temperature	Untere Kategorietemperatur
t_{OL}	Operating life at operating temperature and voltage	Betriebszeit bei Betriebstemperatur und -spannung
T_{op}	Operating temperature, $T_A + \Delta T$	Betriebstemperatur, $T_A + \Delta T$
T_R	Rated temperature	Nenntemperatur
T_{ref}	Reference temperature	Referenztemperatur
t_{SL}	Reference service life	Referenz-Lebensdauer
V_{AC}	AC voltage	Wechselspannung
V_C	Category voltage	Kategoriespannung
$V_{\text{C,RMS}}$	Category AC voltage	(Sinusförmige) Kategorie-Wechselspannung
V_{CD}	Corona-discharge onset voltage	Teilentlade-Einsatzspannung
V_{ch}	Charging voltage	Ladespannung
V_{DC}	DC voltage	Gleichspannung
V_{FB}	Fly-back capacitor voltage	Spannung (Flyback)
V_i	Input voltage	Eingangsspannung
V_o	Output voltage	Ausgangsspannung
V_{op}	Operating voltage	Betriebsspannung
V_p	Peak pulse voltage	Impuls-Spitzenspannung
V_{pp}	Peak-to-peak voltage Impedance	Spannungshub

Capacitors for DC Link

B3277*D/E/G/J/T

MKP DC link – high density series up to 480 µF

Symbol	English	German
V_R	Rated voltage	Nennspannung
$\hat{V}_R$	Amplitude of rated AC voltage	Amplitude der Nenn-Wechselspannung
V_{RMS}	(Sinusoidal) alternating voltage, root-mean-square value	(Sinusförmige) Wechselspannung
V_{SC}	S-correction voltage	Spannung bei Anwendung "S-correction"
V_{sn}	Snubber capacitor voltage	Spannung bei Anwendung "Beschaltung"
Z	Impedance	Scheinwiderstand
e	Lead spacing	Rastermaß

Important notes

The following applies to all products named in this publication:

1. Some parts of this publication contain **statements about the suitability of our products for certain areas of application**. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out **that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application**. As a rule we are either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether a product with the properties described in the product specification is suitable for use in a particular customer application.
2. We also point out that **in individual cases, a malfunction of electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified**. In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of an electronic component could endanger human life or health (e.g. in accident prevention or life-saving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of an electronic component.
3. **The warnings, cautions and product-specific notes must be observed.**
4. In order to satisfy certain technical requirements, **some of the products described in this publication may contain substances subject to restrictions in certain jurisdictions (e.g. because they are classed as hazardous)**. Useful information on this will be found in our Material Data Sheets on the Internet (www.tdk-electronics.tdk.com/material). Should you have any more detailed questions, please contact our sales offices.
5. We constantly strive to improve our products. Consequently, **the products described in this publication may change from time to time**. The same is true of the corresponding product specifications. Please check therefore to what extent product descriptions and specifications contained in this publication are still applicable before or when you place an order.
We also **reserve the right to discontinue production and delivery of products**. Consequently, we cannot guarantee that all products named in this publication will always be available. The aforementioned does not apply in the case of individual agreements deviating from the foregoing for customer-specific products.
6. Unless otherwise agreed in individual contracts, **all orders are subject to our General Terms and Conditions of Supply**.

Important notes

7. **Our manufacturing sites serving the automotive business apply the IATF 16949 standard.**
The IATF certifications confirm our compliance with requirements regarding the quality management system in the automotive industry. Referring to customer requirements and customer specific requirements ("CSR") TDK always has and will continue to have the policy of respecting individual agreements. Even if IATF 16949 may appear to support the acceptance of unilateral requirements, we hereby like to emphasize that **only requirements mutually agreed upon can and will be implemented in our Quality Management System**. For clarification purposes we like to point out that obligations from IATF 16949 shall only become legally binding if individually agreed upon.
8. The trade names EPCOS, CarXield, CeraCharge, CeraDiode, CeraLink, CeraPad, CeraPlas, CSMP, CTVS, DeltaCap, DigiSiMic, FilterCap, FormFit, InsuGate, LeaXield, MediPlas, MiniBlue, MiniCell, MKD, MKK, ModCap, MotorCap, PCC, PhaseCap, PhaseCube, PhaseMod, PhiCap, PiezoBrush, PlasmaBrush, PowerHap, PQSine, PQvar, SIFERRIT, SIFI, SIKOREL, SilverCap, SIMDAD, SiMic, SIMID, SineFormer, SIOV, SurfIND, ThermoFuse, WindCap, XieldCap are **trademarks registered or pending** in Europe and in other countries. Further information will be found on the Internet at www.tdk-electronics.tdk.com/trademarks.

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