

Product Summary

| BV _{SSS} | R _{SS(ON)} MAX | I _S T _A = +25°C |
|-------------------|---------------------------------|--|
| 12V | 6.5mΩ @ V _{GS} = 4.5V | 16.6A |
| | 11.4mΩ @ V _{GS} = 2.5V | 12.1A |

Description

This new generation MOSFET is designed to minimize the on-state resistance (R_{SS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Battery Management
- Load Switch
- Battery Protection

Features

- CSP with Footprint 2.70mm × 1.81mm
- Height = 0.21mm for Low Profile
- ESD Protection of Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- Halogen and Antimony Free. "Green" Device (Note 3)**

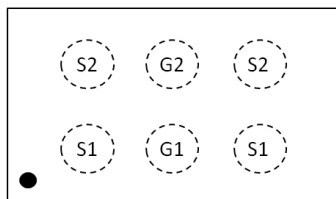
Mechanical Data

- Case: X3-DSN2718-6 (Type B)
- Terminal Connections: See Diagram Below
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish — NiPdAu. Solderable per MIL-STD-202, Method 208 (G4)

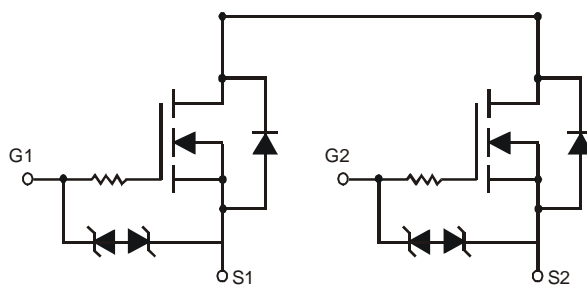


ESD PROTECTED

X3-DSN2718-6 (Type B)



Top View



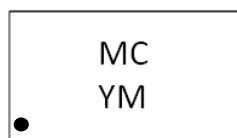
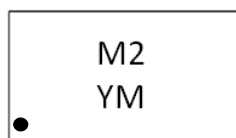
Equivalent Circuit

Ordering Information (Note 4)

| Part Number | Case | Packaging |
|---------------|-----------------------|------------------|
| DMN16M9UCA6-7 | X3-DSN2718-6 (Type B) | 3000/Tape & Reel |

- Notes:
- No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 - See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 - Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 - For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



M2 / MC = Product Type Marking Code
 YM = Date Code Marking
 Y or Y = Year (ex: E = 2017)
 M or M = Month (ex: 9 = September)

Date Code Key

| Year | 2015 | 2016 | 2017 | 2018 | 2019 | 2020 | 2021 |
|------|------|------|------|------|------|------|------|
| Code | C | D | E | F | G | H | I |

| Month | Jan | Feb | Mar | Apr | May | Jun | Jul | Aug | Sep | Oct | Nov | Dec |
|-------|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| Code | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | O | N | D |

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

| Characteristic | | | Symbol | Value | Unit |
|---|--------------|------------------------|------------------|-------|------|
| Source-Source Voltage | | | V _{SSS} | 12 | V |
| Gate-Source Voltage | | | V _{GSS} | ±12 | V |
| Continuous Source Current (Note 5) V _{GS} = 4.5V | Steady State | T _A = +25°C | I _S | 16.6 | A |
| | | T _A = +70°C | | 13.2 | |
| Continuous Source Current (Note 5) V _{GS} = 2.5V | Steady State | T _A = +25°C | I _S | 12.1 | A |
| | | T _A = +70°C | | 9.7 | |
| Pulsed Source Current (Note 6) | | | I _{SM} | 80 | A |

Thermal Characteristics

| Characteristic | Symbol | Value | Unit |
|--|-----------------------------------|-------------|------|
| Power Dissipation (Note 7) | P _D | 1.0 | W |
| Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 7) | R _{θJA} | 124.6 | °C/W |
| Power Dissipation (Note 5) | P _D | 2.4 | W |
| Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5) | R _{θJA} | 51.5 | °C/W |
| Operating and Storage Temperature Range | T _J , T _{STG} | -55 to +150 | °C |

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Min | Typ | Max | Unit | Test Condition |
|--|---------------------|-----|------|------|------|--|
| OFF CHARACTERISTICS (Note 8) | | | | | | |
| Source-Source Breakdown Voltage | BV _{SSS} | 12 | - | - | V | V _{GS} = 0V, I _S = 1mA |
| Zero Gate Voltage Drain Current T _J = +25°C | I _{SSS} | - | - | 1 | μA | V _{SS} = 10V, V _{GS} = 0V |
| Gate-Source Leakage | I _{GSS} | - | - | ±10 | μA | V _{GS} = ±8V, V _{SS} = 0V |
| ON CHARACTERISTICS (Note 8) | | | | | | |
| Gate Threshold Voltage | V _{GS(TH)} | 0.5 | - | 1.3 | V | V _{SS} = 6V, I _S = 1mA |
| Static Source-Source On-Resistance | R _{SS(ON)} | 2.3 | 5.0 | 6.5 | mΩ | V _{GS} = 4.5V, I _S = 3A |
| | | 2.5 | 5.2 | 6.8 | | V _{GS} = 4.0V, I _S = 3A |
| | | 2.6 | 5.3 | 6.9 | | V _{GS} = 3.8V, I _S = 3A |
| | | 2.8 | 5.5 | 8.8 | | V _{GS} = 3.1V, I _S = 3A |
| | | 3.0 | 6.0 | 11.4 | | V _{GS} = 2.5V, I _S = 3A |
| Diode Forward Voltage | V _{SS} | - | 0.7 | 1.2 | V | V _{GS} = 0V, I _S = 3A |
| DYNAMIC CHARACTERISTICS (Note 9) | | | | | | |
| Input Capacitance | C _{ISS} | - | 2360 | - | pF | V _{SS} = 6V, V _{GS} = 0V, f = 1.0MHz |
| Output Capacitance | C _{OSS} | - | 666 | - | | |
| Reverse Transfer Capacitance | C _{RSS} | - | 325 | - | | |
| Total Gate Charge | Q _g | - | 35.2 | - | nC | V _{SS} = 6V, V _{GS} = 4.5V, I _S = 18A |
| Gate-Source Charge | Q _{gs} | - | 7.0 | - | | |
| Gate-Drain Charge | Q _{gd} | - | 8.3 | - | | |
| Gate Charge at V _{TH} | Q _{g(TH)} | - | 4.2 | - | | |
| Turn-On Delay Time | t _{D(ON)} | - | 615 | - | ns | V _{SS} = 6V, V _{GS} = 4.5V, I _S = 3A |
| Turn-On Rise Time | t _R | - | 1447 | - | | |
| Turn-Off Delay Time | t _{D(OFF)} | - | 2736 | - | | |
| Turn-Off Fall Time | t _F | - | 3812 | - | | |

- Notes:
- Device mounted on FR-4 material with 1inch² (6.45cm²), 2oz. (0.071mm thick) Cu.
 - Repetitive rating, pulse width limited by junction temperature.
 - Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to production testing.

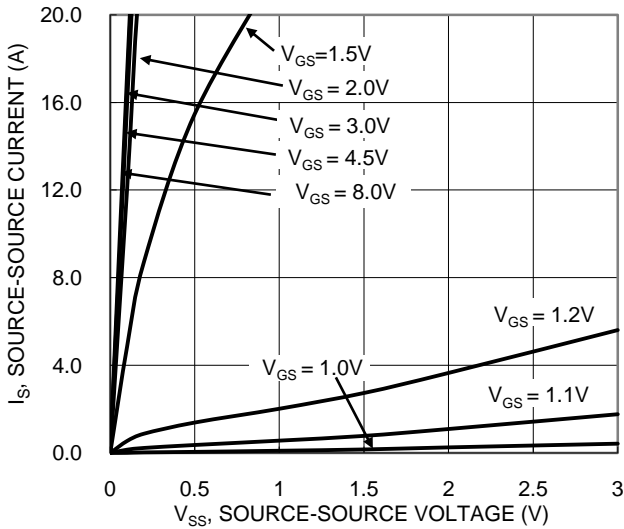


Figure 1. Typical Output Characteristic

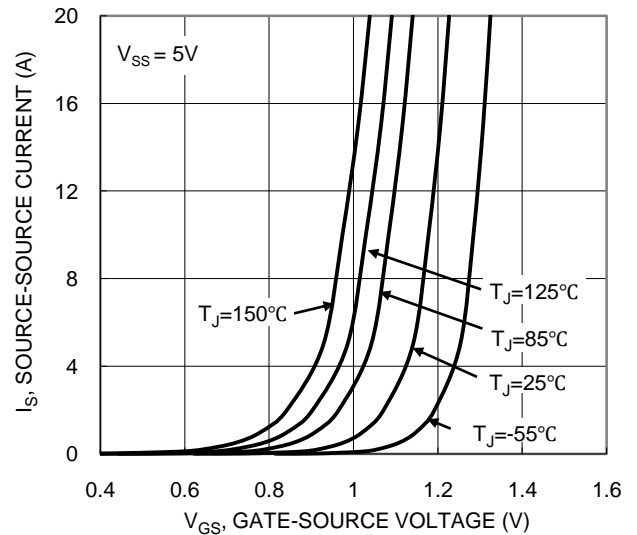


Figure 2. Typical Transfer Characteristic

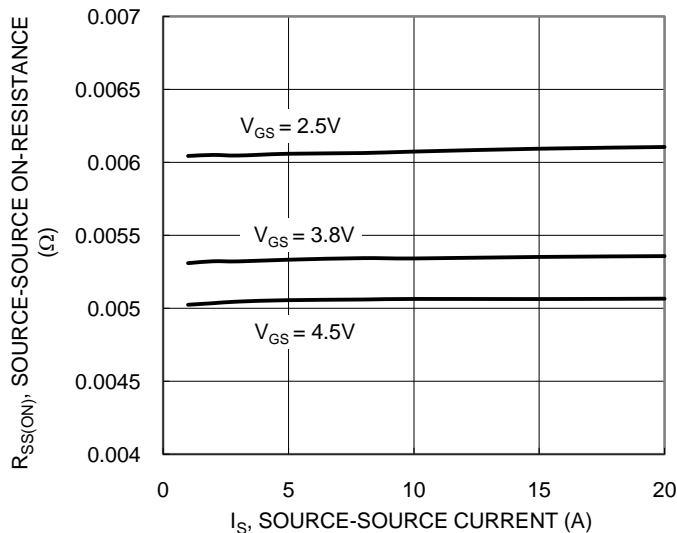


Figure 3. Typical On-Resistance vs. Source Current and Gate Voltage

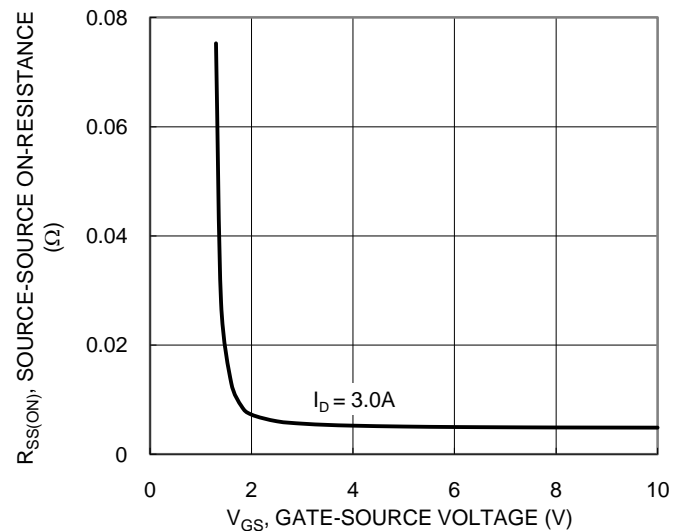


Figure 4. Typical Transfer Characteristic

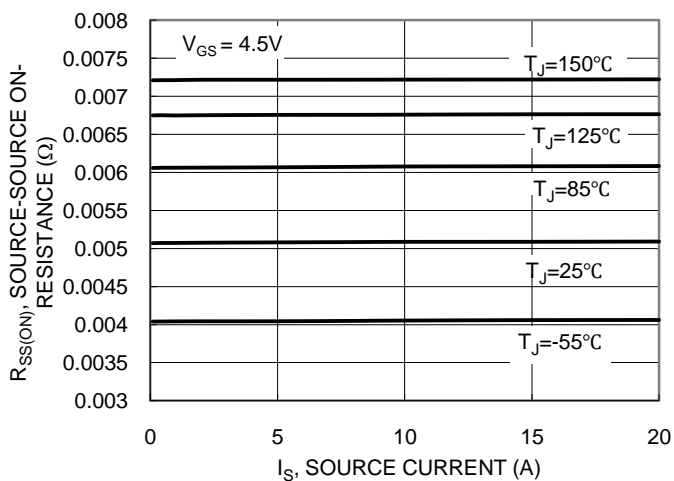


Figure 5. Typical On-Resistance vs. Source Current and Junction Temperature

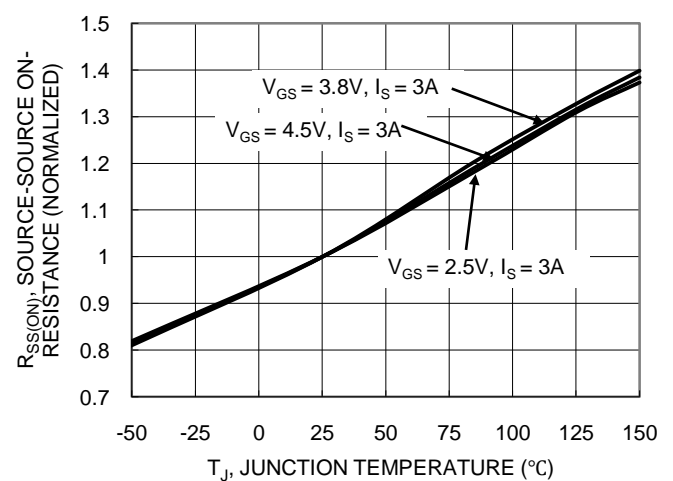


Figure 6. On-Resistance Variation with Junction Temperature

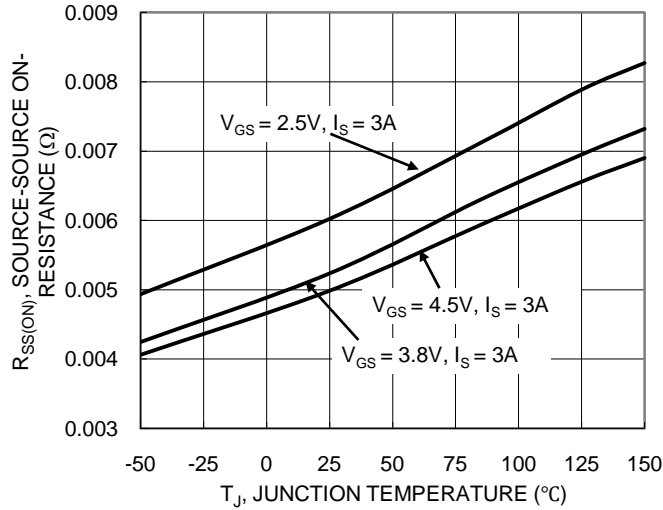


Figure 7. On-Resistance Variation with Junction Temperature

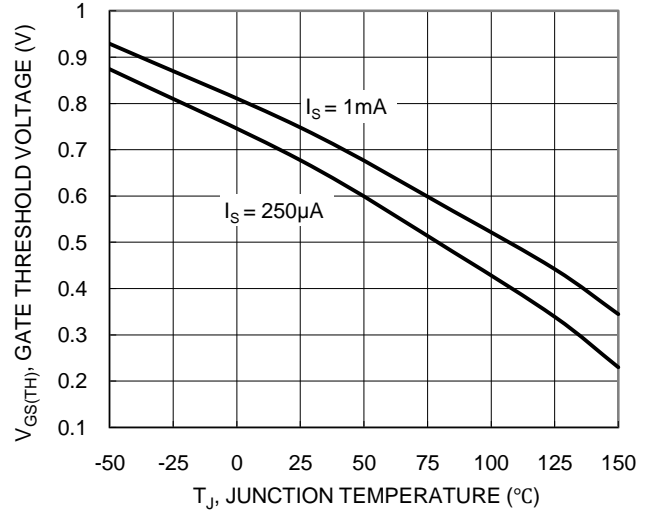


Figure 8. Gate Threshold Variation vs. Junction Temperature

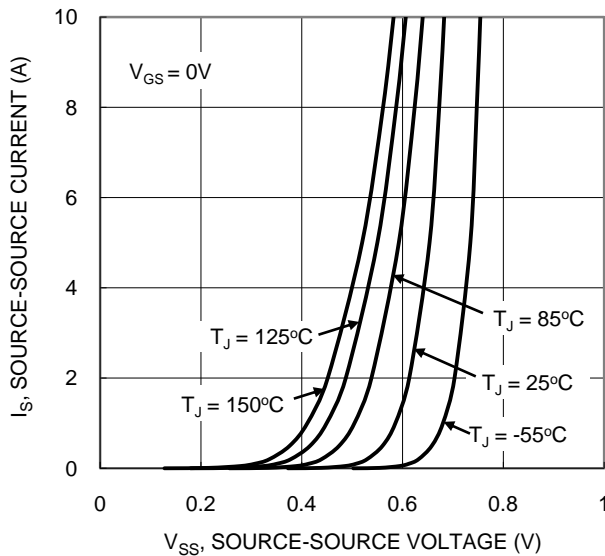


Figure 9. Diode Forward Voltage vs. Current

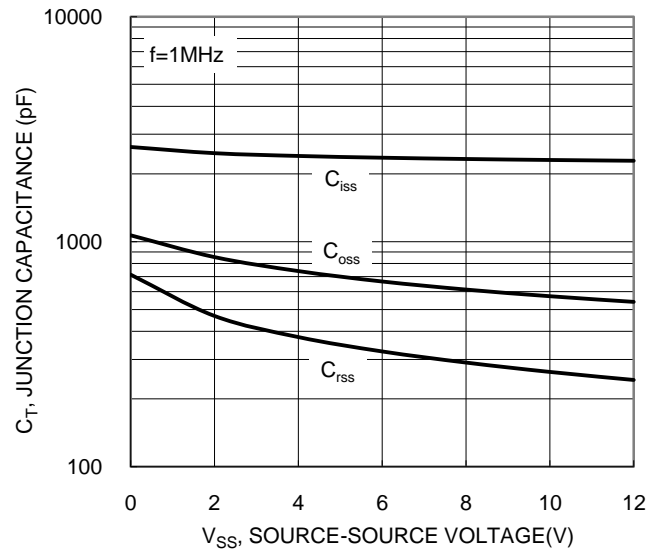


Figure 10. Typical Junction Capacitance

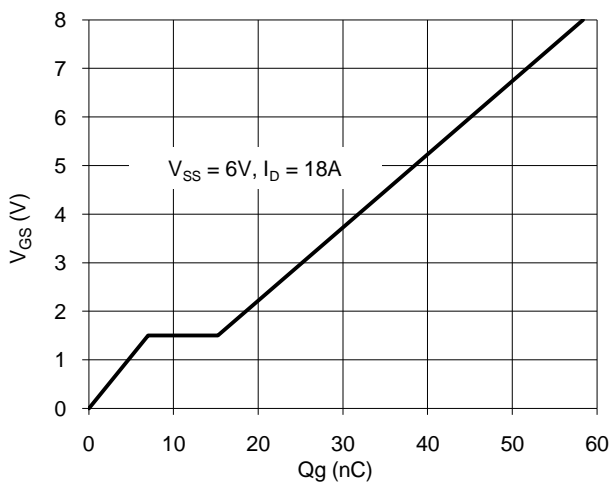


Figure 11. Gate Charge

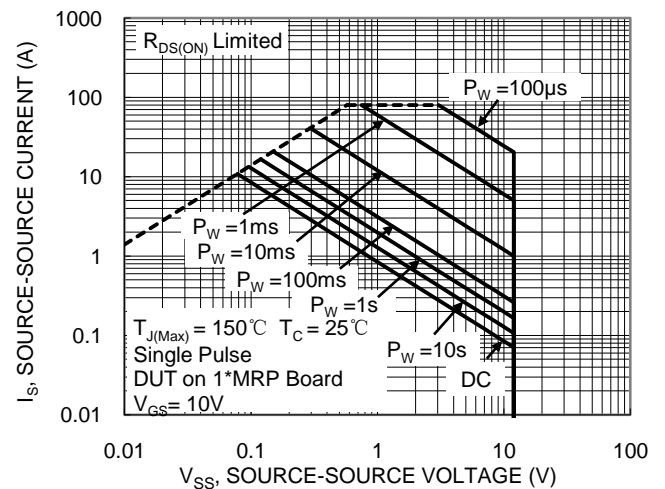


Figure 12. SOA, Safe Operation Area

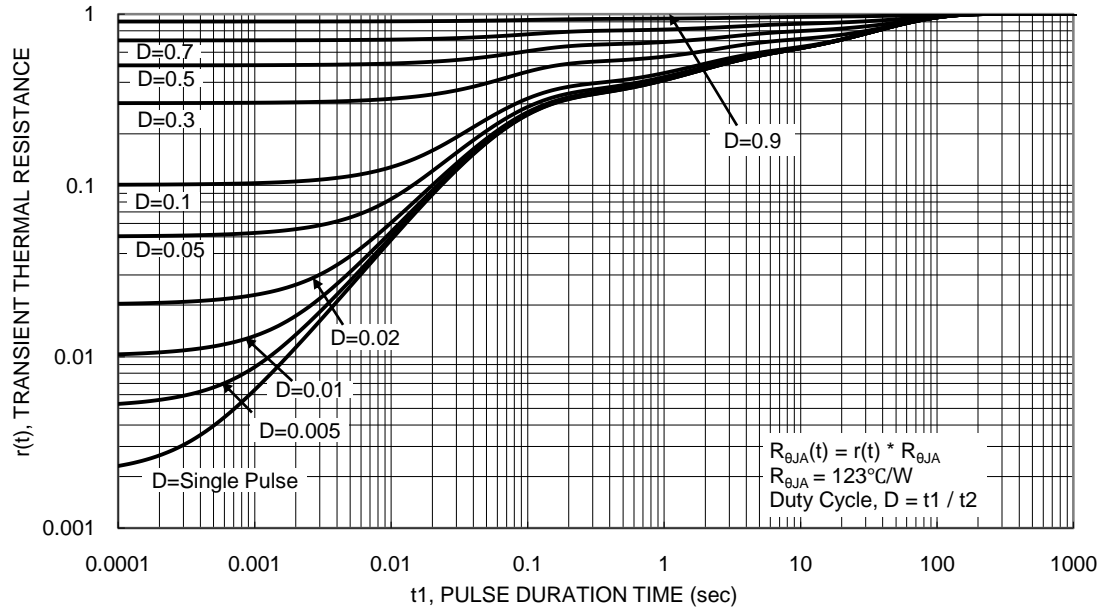
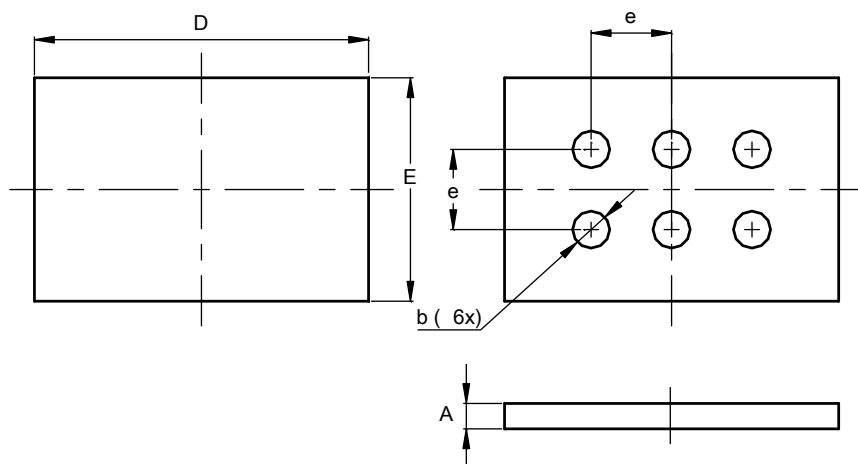


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

X3-DSN2718-6 (Type B)

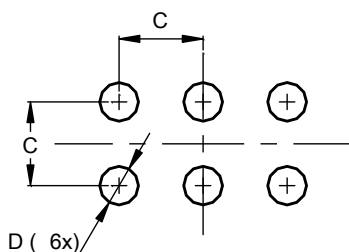


| X3-DSN2718-6 (Type B) | | | |
|--------------------------|------|------|------|
| Dim | Min | Max | Typ |
| A | 0.15 | 0.27 | 0.21 |
| b | 0.27 | 0.33 | 0.30 |
| D | 2.64 | 2.76 | 2.70 |
| E | 1.75 | 1.87 | 1.81 |
| e | 0.62 | 0.68 | 0.65 |
| All Dimensions in mm | | | |

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

X3-DSN2718-6 (Type B)



| Dimensions | Value (in mm) |
|------------|------------------|
| C | 0.65 |
| D | 0.30 |

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