TAIWAN SEMICONDUCTOR

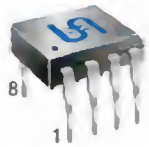
RoHS COMPLIANCE

TS358/TS2904

Single Supply Dual Operational Amplifiers

SOP-8

DIP-8



Pin assignment:

1. Output A	8. Vcc
2. Input A (-)	7. Output B
3. Input A (+)	6. Input B (-)
4. Gnd	5. Input B (+)

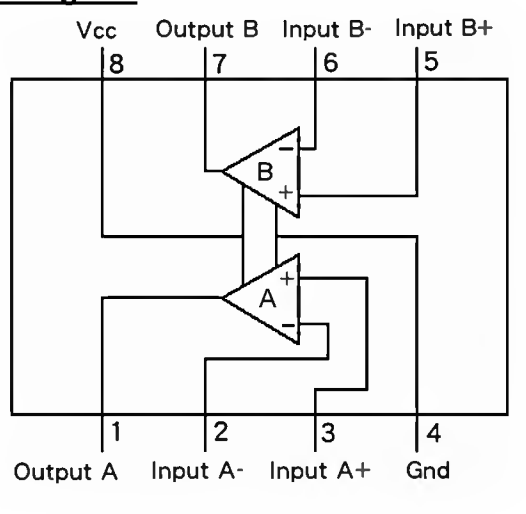
General Description

Utilizing the circuit designs perfected for recently introduced Quad Operational Amplifiers, these dual operational amplifiers have several distinct advantages over standard operational amplifier types in single supply applications. They can operate at supply voltages as low as 3.0 Volts or as high as 32 Volts with quiescent currents about one fifth of those associated with the LM741 (on a pet amplifier basis). The common mode input range includes the negative supply, thereby eliminating the necessity for external biasing components in many applications. The TS358/TS2904 is equivalent to one half of TS324/TS2902, and output voltage range also includes the negative supply voltage.

Features

- Short circuit protected outputs
- True differential input stage
- Single supply operation: 3V to 32V
- Low input bias currents
- Internally compensated
- Common mode range extends to negative supply
- Single and split supply operation
- Similar performance to the popular MC1558

Block Diagram



Ordering Information

Part No.	Package	Packing
TS358CD C3	DIP-8	50pcs / Tube
TS358CS RL	SOP-8	2.5Kpcs / 13” Reel
TS2904CD C3	DIP-8	50pcs / Tube
TS2904CS RL	SOP-8	2.5Kpcs / 13” Reel

Absolute Maximum Rating

Parameter		Symbol	Limit	Unit
Supply Voltage	TS358	V_{CC}	+32 or ±16	V
	TS2904		+26 or ±13	
Differential Input Voltage (Split Power Supplies)	TS358	V_{IDR}	32	V
	TS2904		26	
Input Common Mode Voltage Range (note 1)		V_{ICR}	-0.3 to 32	V
			-0.3 to 26	
Input Forward Current (note 2)		I_{IF}	50	mA
Output Short Circuit Duration		tsc	Continuous	
Operating Junction Temperature Range	TS358	T_J	0 ~ +70	°C
	TS2904		-40 ~ +85	
Storage Temperature Range		T_{STG}	-65 ~ +150	°C

Note 1: For supply. Voltages less than 32V/26V for the TS358/TS2904 the absolute maximum input voltage is equal to the supply voltage.

Note 2: This input current will only exist when the voltage is negative at any of the input leads. Normal output states will reestablish when the input voltage returns to a voltage greater than -0.3V.

Electrical Characteristics

(V_{CC} = 5V, Ta=25°C; unless otherwise specified.)

Characteristics	Symbol	TS358			TS2904			Unit
		Min	Typ	Max	Min	Typ	Max	
Input Offset Voltage V_{CC}= 5.0V to 30V, V_{IC}= 0V to Vcc -1.7 V, Vo= 1.4V, R_S= 0Ω T_{LOW} ≤ Ta ≤T_{HIGH}	Vio	-- --	2.0 --	7.0 9.0	-- --	2.0 --	5.0 10	mV
Average Temperature Coefficient of Input Offset Voltage	ΔIio/ΔT	--	7.0	--	--	7.0	--	uV/°C
Input Offset Current T_{LOW} ≤ Ta ≤T_{HIGH}	Iio	-- --	5.0 --	50 150	-- --	5.0 --	50 150	nA
Average Temperature Coefficient of input Offset Current	ΔIio/ΔT	--	10	--	--	10	--	pA/°C
Input Bias Current T_{LOW} ≤ Ta ≤T_{HIGH}	I_{IB}	-- --	-45 -50	-250 -500	-- --	-45 -50	-250 -500	nA
Input Common-Mode Voltage Range) V_{CC} = 30 V (Note1) V_{CC} = 30 V, T_{LOW} ≤ Ta ≤T_{HIGH}	V_{ICR}	0 0	-- --	28.3 28	0 0	-- --	24.3 24	V
Differential Input Voltage Range	V_{IDR}	--	--	V_{CC}	--	--	V_{CC}	V
Large Signal Open-Loop Voltage Gain R_L = 2.0K, V_{CC} =15V, For Large V_O Swing, T_{LOW} ≤ Ta ≤T_{HIGH}	A_{VOL}	25 15	100 --	-- --	25 15	100 --	-- --	V/mV
Channel Separation 1.0 KHz to 20KHz	--	--	-120	--	--	-120	--	dB
Common Mode Rejection Ratio R_S ≤ 10 kΩ	CMRR	65	70	--	50	70	--	dB
Power Supply Rejection Ratio	PSRR	65	100	--	50	100	--	dB
Output Voltage Range, RL = 2KΩ	V_{OR}	0	--	3.3	0	--	3.3	V
Output Voltage -- High Limit V_{CC} = 30 V, R_L = 2 kΩ V_{CC} = 30 V, R_L = 10 kΩ	V_{OH}	26 27	-- 28	-- --	22 23	-- 24	-- --	V
Output Voltage -- Low Limit V_{CC} = 5.0 V, R_L = 10 kΩ	V_{OL}	--	5.0	20	--	5.0	100	mV
Output Source Current V_{ID}=+1.0V,V_{CC}=15V	I_{O+}	20	40	--	20	40	--	mA
Output Sink Current V_{ID} = -1.0 V, V_{CC} = 15 V V_{ID} = -1.0 V, V_O = 200 mV	I_{O-}	10 12	20 50	-- --	10 12	20 50	-- --	mA uA
Output Short Circuit to Ground (Note 2)	I_{OS}	--	40	60	--	40	60	mA
Power Supply Current , V_{CC} = 30 VV_O = 0 V, R_L = ∞ V_{CC} = 5.0 V, V_O = 0 V, R_L = ∞	I_{CC}	-- --	1.5 0.7	3.0 1.2	-- --	1.5 0.7	3.0 1.2	mA

Notes :

1. The input common mode voltage or either input signal voltage should not be allowed to go negative by more than 0.3 V. The upper end of the common mode voltage range is Vcc 17V, but either or both inputs can go to +32V.
2. Short circuits from the output to Vcc can cause excessive heating and eventual destruction. Destructive dissipation can recruit from simultaneous shorts on all amplifiers.
3. TS358: Tlow=0ºC, Thigh=+70ºC
   TS2904: Tlow=-40ºC, Thigh=+85ºC

Electrical Characteristics Curve

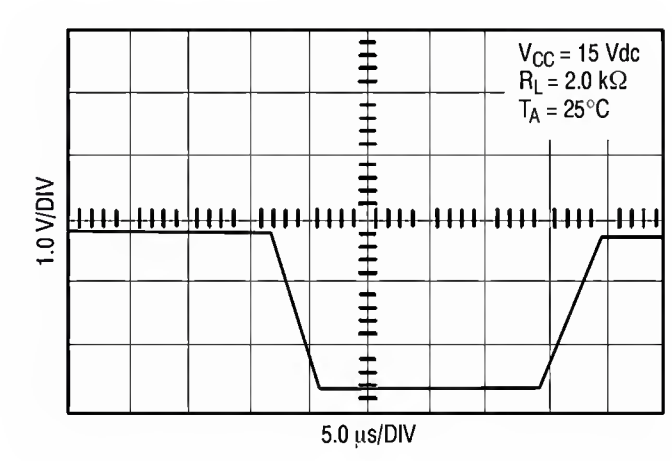


Figure 1. Large Signal Voltage Follower Response

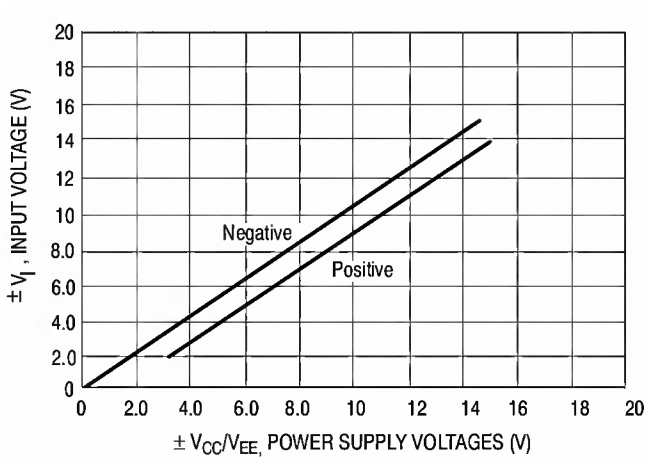


Figure 2. Input Voltage Range

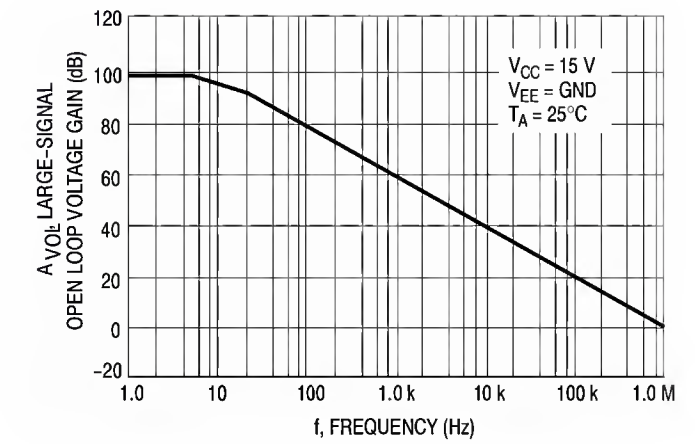


Figure 3. Open Loop Frequency

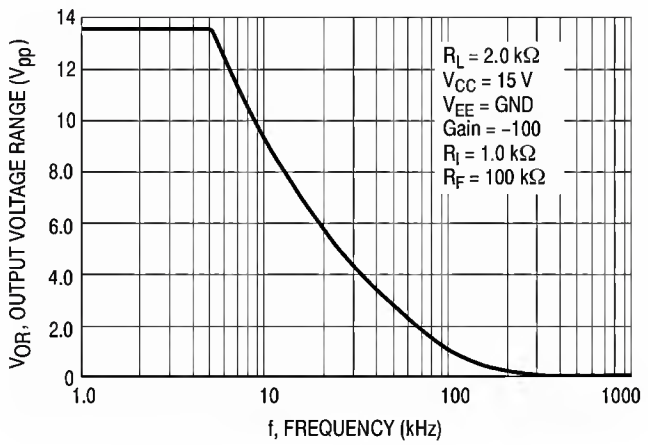


Figure 4. Large Signal Frequency Response

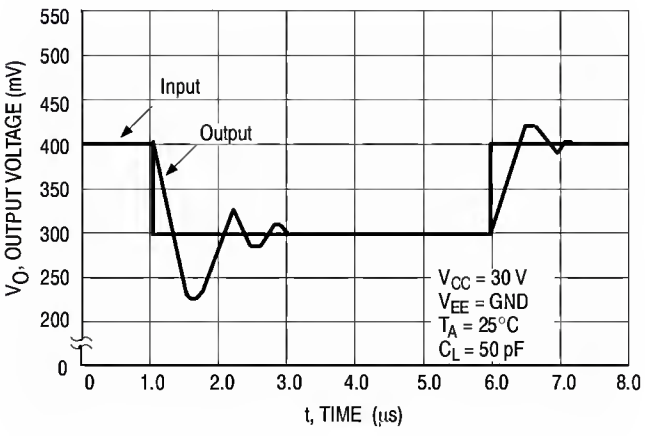


Figure 5. Small-Signal Voltage Follower Pulse Response (Noninverting)

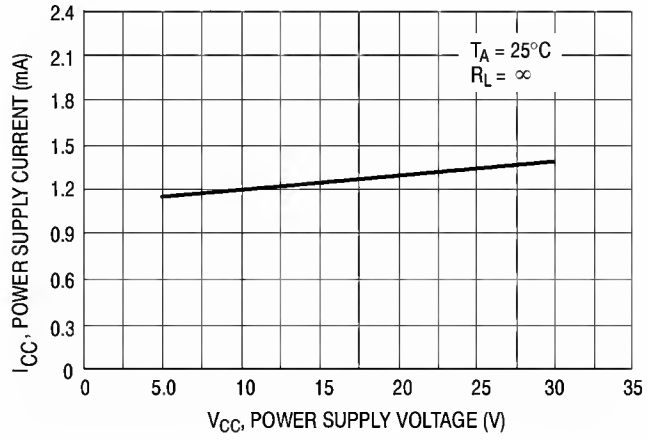


Figure 6. Power Supply Current vs. Supply Voltage

Application Description

The TS358/TS2904 made using two internally compensated, two-stage operational amplifiers. The first stage performs not only the first stage gain function but also performs the level shifting and transconductance reduction functions. By reducing the transconductance, a smaller compensation capacitor (only 5.0pF) can be employed, thus saving chip area. Another feature of this input stage is that the input common mode range can include the negative supply or ground, in single supply operation, without saturating either the input devices or the differential to single-ended converter. The second stage consists of a standard current source load amplifier stage.

Each amplifier is biased from an internal-voltage regulator, and which has a low temperature coefficient thus giving each amplifier good temperature characteristics as well as excellent power supply rejection.

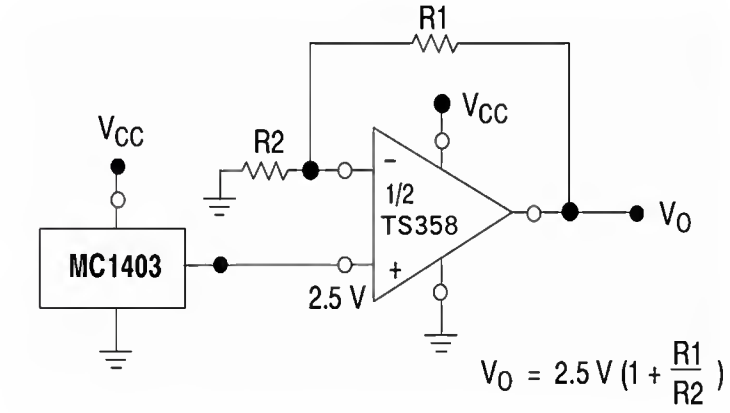


Figure 7. Voltage Reference

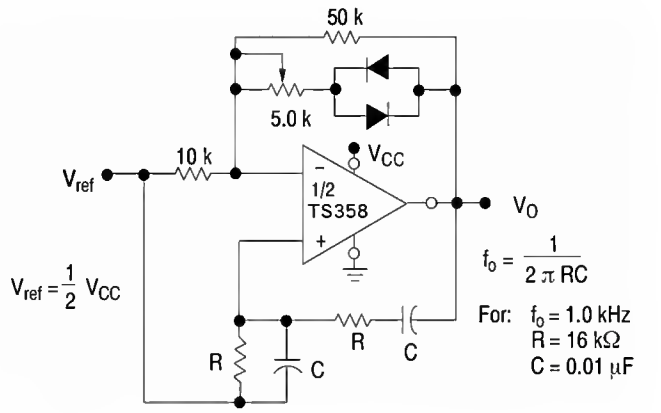


Figure 8. Wien Bridge Oscillator

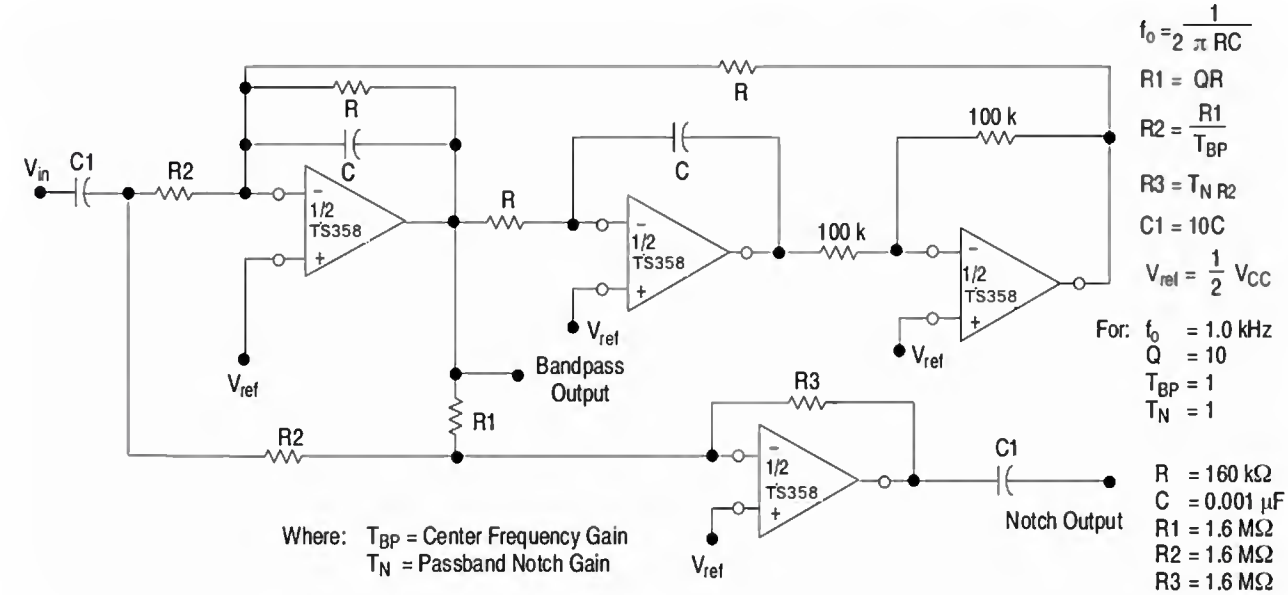


Figure 9. Bi-Quad Filter

Application Description (Continue)

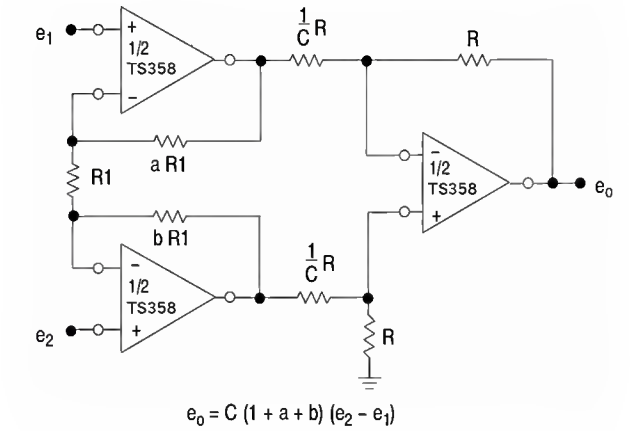


Figure 10. High Impedance Differential Amplifier

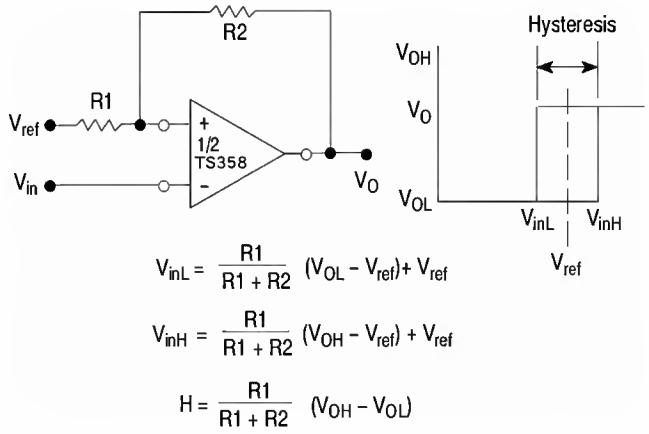


Figure 11. Comparator with Hysteresis

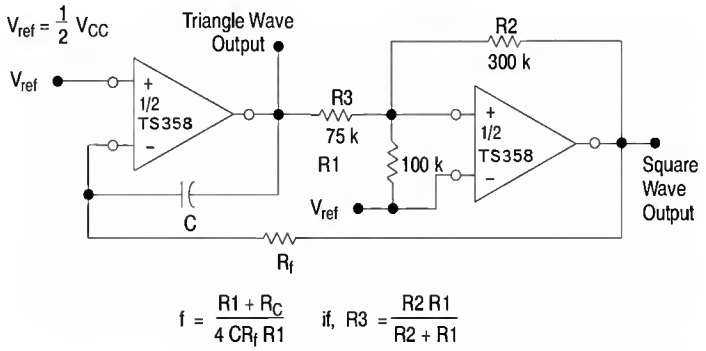
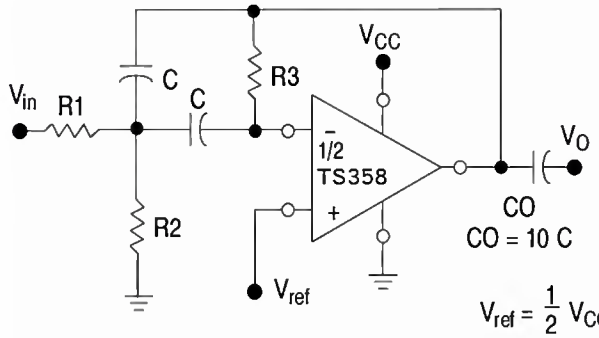


Figure 12. Function Generator



Given: f_o = center frequency

$A(f_o)$ = gain at center frequency

Choose value f_o, C

Then: $R3 = \frac{Q}{\pi f_o C}$

$R1 = \frac{R3}{2 A(f_o)}$

$R2 = \frac{R1\ R3}{4Q^2\ R1 - R3}$

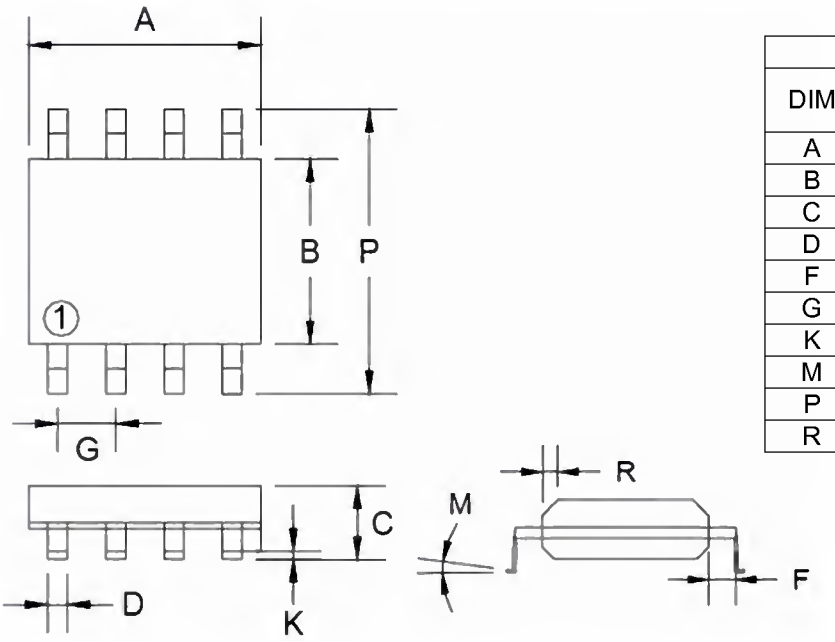
For less than 10% error from operational amplifier. $\frac{Q_o f_o}{BW} < 0.1$

Where f_o and BW are expressed in Hz.

If source impedance varies, filter may be preceded with voltage follower buffer to stabilize filter parameters.

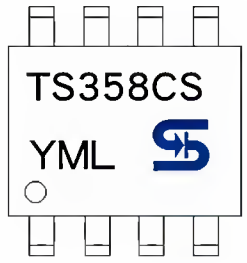
Figure 13. Multiple Feedback Bandpass Filter

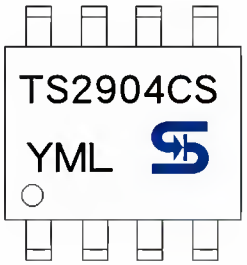
SOP-8 Mechanical Drawing



SOP-8 DIMENSION				
DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX.
A	4.80	5.00	0.189	0.196
B	3.80	4.00	0.150	0.157
C	1.35	1.75	0.054	0.068
D	0.35	0.49	0.014	0.019
F	0.40	1.25	0.016	0.049
G	1.27BSC		0.05BSC	
K	0.10	0.25	0.004	0.009
M	0°	7°	0°	7°
P	5.80	6.20	0.229	0.244
R	0.25	0.50	0.010	0.019

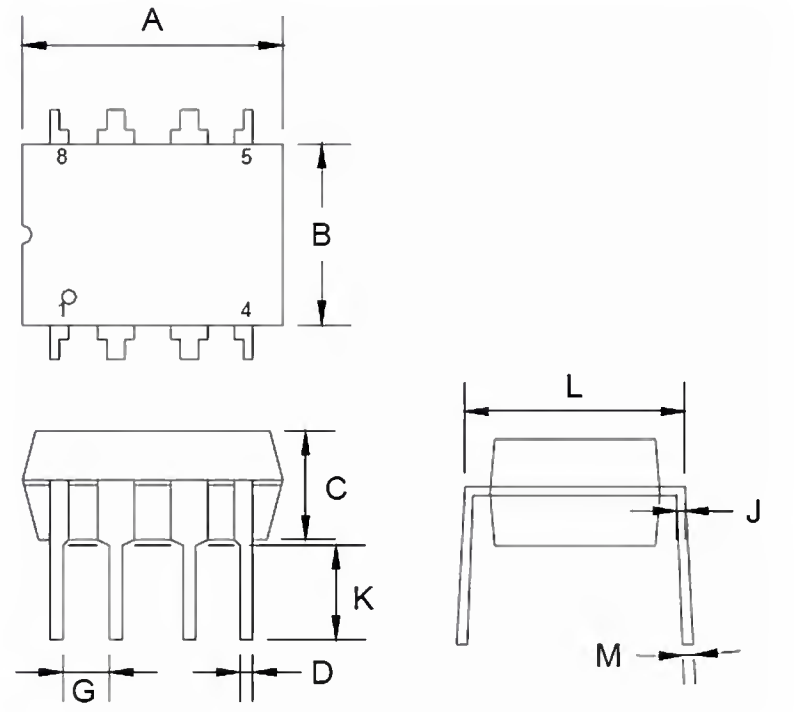
Marking Diagram





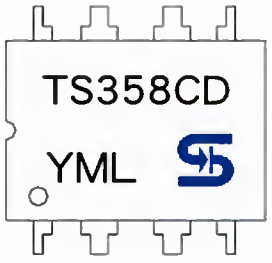
Y = Year Code
M = Month Code
(**A**=Jan, **B**=Feb, **C**=Mar, **D**=Apl, **E**=May, **F**=Jun, **G**=Jul, **H**=Aug, **I**=Sep, **J**=Oct, **K**=Nov, **L**=Dec)
L = Lot Code

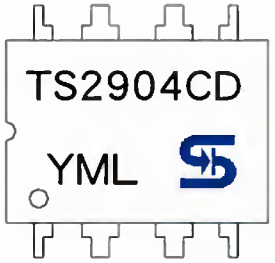
DIP-8 Mechanical Drawing



DIP-8 DIMENSION				
DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	9.07	9.32	0.357	0.367
B	6.22	6.48	0.245	0.255
C	3.18	4.45	0.125	0.135
D	0.35	0.55	0.019	0.020
G	2.54 (typ)		0.10 (typ)	
J	0.29	0.31	0.011	0.012
K	3.25	3.35	0.128	0.132
L	7.75	8.00	0.305	0.315
M	-	10°	-	10°

Marking Diagram





Y = Year Code
M = Month Code
(**A**=Jan, **B**=Feb, **C**=Mar, **D**=Apl, **E**=May, **F**=Jun, **G**=Jul, **H**=Aug, **I**=Sep, **J**=Oct, **K**=Nov, **L**=Dec)
L = Lot Code

Notice

Specifications of the products displayed herein are subject to change without notice. TSC or anyone on its behalf, assumes no responsibility or liability for any errors or inaccuracies.

Information contained herein is intended to provide a product description only. No license, express or implied, to any intellectual property rights is granted by this document. Except as provided in TSC's terms and conditions of sale for such products, TSC assumes no liability whatsoever, and disclaims any express or implied warranty, relating to sale and/or use of TSC products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright, or other intellectual property right.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify TSC for any damages resulting from such improper use or sale.