

## Product Summary

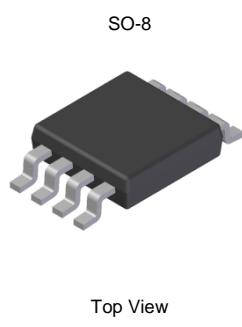
BV <sub>DSS</sub>	R <sub>DSS(ON)</sub> Max	I <sub>D</sub> T <sub>A</sub> = +25°C
-20V	33mΩ @ V <sub>GS</sub> = -4.5V	-6.5A
	52mΩ @ V <sub>GS</sub> = -2.5V	-5.0A

## Description

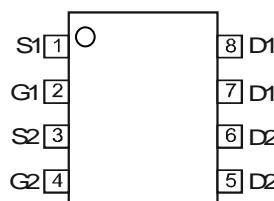
This new generation MOSFET is designed to minimize the on-state resistance (R<sub>DSS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

## Applications

- Backlighting
- Power Management Functions
- DC-DC Converters



Top View



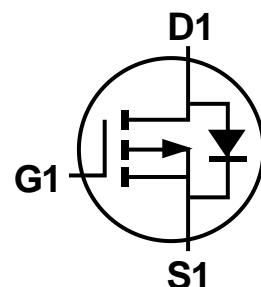
Top View  
Internal Schematic

## Features

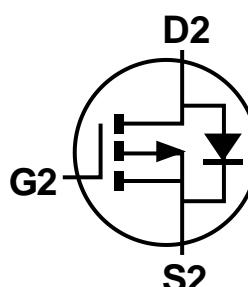
- Dual P-Channel MOSFET
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

## Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections Indicator: See Diagram
- Terminals: Finish - Matte Tin Annealed over Copper Lead Frame. Solderable per MIL-STD-202, Method 208 
- Weight: 0.072 grams (Approximate)



S1  
P-Channel MOSFET



S2  
P-Channel MOSFET

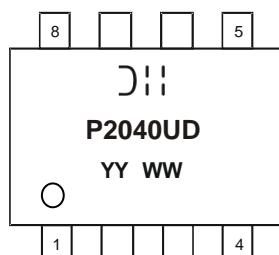
## Ordering Information (Note 4)

Part Number	Case	Packaging
DMP2040USD-13	SO-8	2500/Tape & Reel

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



D11 = Manufacturer's Marking  
P2040UD = Product Type Marking Code  
YYWW = Date Code Marking  
YY or YY = Year (ex: 17 = 2017)  
WW = Week (01 to 53)

**Maximum Ratings** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			$V_{DSS}$	-20	V
Gate-Source Voltage			$V_{GSS}$	$\pm 12$	V
Continuous Drain Current (Note 6) $V_{GS} = -4.5\text{V}$	Steady State	$T_A = +25^\circ\text{C}$ $T_A = +70^\circ\text{C}$	$I_D$	-6.5 -5.0	A
Continuous Drain Current (Note 7) $V_{GS} = -4.5\text{V}$	Steady State	$T_C = +25^\circ\text{C}$ $T_C = +70^\circ\text{C}$	$I_D$	-12 -10	A
Pulsed Drain Current (10 $\mu\text{s}$ Pulse, Duty Cycle = 1%)			$I_{DM}$	-30	A
Continuous Source-Drain Diode Current (Note 6)			$I_S$	-2.2	A
Avalanche Current (Note 8) $L = 0.1\text{mH}$			$I_{AS}$	-16	A
Avalanche Energy (Note 8) $L = 0.1\text{mH}$			$E_{AS}$	13.5	mJ

**Thermal Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Total Power Dissipation (Note 5)		$T_A = +25^\circ\text{C}$	$P_D$	1.1	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State		$R_{\theta JA}$	108	$^\circ\text{C}/\text{W}$
Total Power Dissipation (Note 6)		$T_A = +25^\circ\text{C}$	$P_D$	1.6	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State		$R_{\theta JA}$	76	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction to Case (Note 7)	Steady State		$R_{\theta JC}$	20	$^\circ\text{C}/\text{W}$
Operating and Storage Temperature Range			$T_J, T_{STG}$	-55 to +150	$^\circ\text{C}$

**Electrical Characteristics** ( $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 9)</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	-20	—	—	V	$V_{GS} = 0\text{V}, I_D = -250\mu\text{A}$
Zero Gate Voltage Drain Current	$I_{DSS}$	—	—	-1	$\mu\text{A}$	$V_{DS} = -16\text{V}, V_{GS} = 0\text{V}$
Gate-Source Leakage	$I_{GS}$	—	—	$\pm 100$	nA	$V_{GS} = \pm 12\text{V}, V_{DS} = 0\text{V}$
<b>ON CHARACTERISTICS (Note 9)</b>						
Gate Threshold Voltage	$V_{GS(\text{TH})}$	-0.6	—	-1.5	V	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(\text{ON})}$	—	26	33	$\text{m}\Omega$	$V_{GS} = -4.5\text{V}, I_D = -8.9\text{A}$
		—	37.5	52		$V_{GS} = -2.5\text{V}, I_D = -6.9\text{A}$
Diode Forward Voltage	$V_{SD}$	—	-7.0	-1.2	V	$V_{GS} = 0\text{V}, I_S = -2.9\text{A}$
<b>DYNAMIC CHARACTERISTICS (Note 10)</b>						
Input Capacitance	$C_{iss}$	—	834	—	$\text{pF}$	$V_{DS} = -10\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$
Output Capacitance	$C_{oss}$	—	133	—		
Reverse Transfer Capacitance	$C_{rss}$	—	105	—		
Gate Resistance	$R_g$	—	4.9	—	$\Omega$	$V_{DS} = 0\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$
Total Gate Charge ( $V_{GS} = -4.5\text{V}$ )	$Q_g$	—	8.6	—		
Total Gate Charge ( $V_{GS} = -8\text{V}$ )	$Q_g$	—	19	—		
Gate-Source Charge	$Q_{gs}$	—	1.5	—	$\text{nC}$	$V_{DS} = -6\text{V}, I_D = -8.9\text{A}$
Gate-Drain Charge	$Q_{gd}$	—	2.5	—		
Turn-On Delay Time	$t_{D(\text{ON})}$	—	5.8	—		
Turn-On Rise Time	$t_R$	—	7.7	—	$\text{ns}$	$V_{DD} = -6\text{V}, R_L = 6\Omega$ $V_{GS} = -4.5\text{V}, R_g = 6\Omega, I_D = -1\text{A}$
Turn-Off Delay Time	$t_{D(\text{OFF})}$	—	28.1	—		
Turn-Off Fall Time	$t_F$	—	14.6	—		
Body Diode Reverse Recovery Time	$t_{RR}$	—	9.8	—	$\text{ns}$	$I_F = -8.9\text{A}, di/dt = -100\text{A}/\mu\text{s}$
Body Diode Reverse Recovery Charge	$Q_{RR}$	—	2.7	—	$\text{nC}$	$I_F = -8.9\text{A}, di/dt = -100\text{A}/\mu\text{s}$

- Notes:
5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
  6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
  7. Thermal resistance from junction to soldering point (on the exposed drain pad).
  8.  $I_{AS}$  and  $E_{AS}$  ratings are based on low frequency and duty cycles to keep  $T_J = +25^\circ\text{C}$ .
  9. Short duration pulse test used to minimize self-heating effect.
  10. Guaranteed by design. Not subject to product testing.

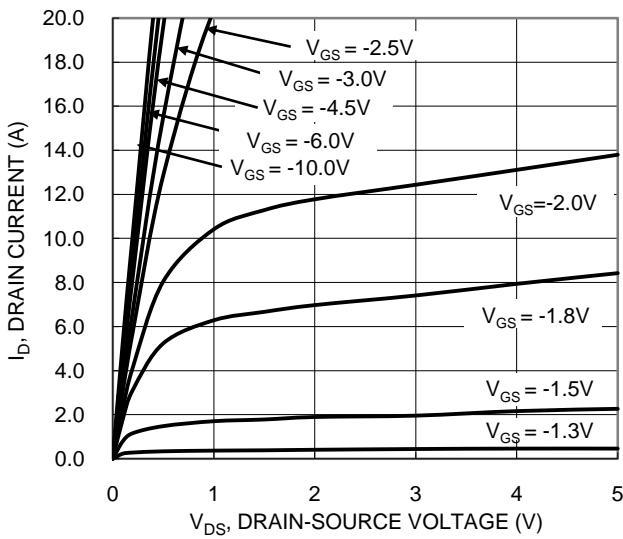


Figure 1. Typical Output Characteristic

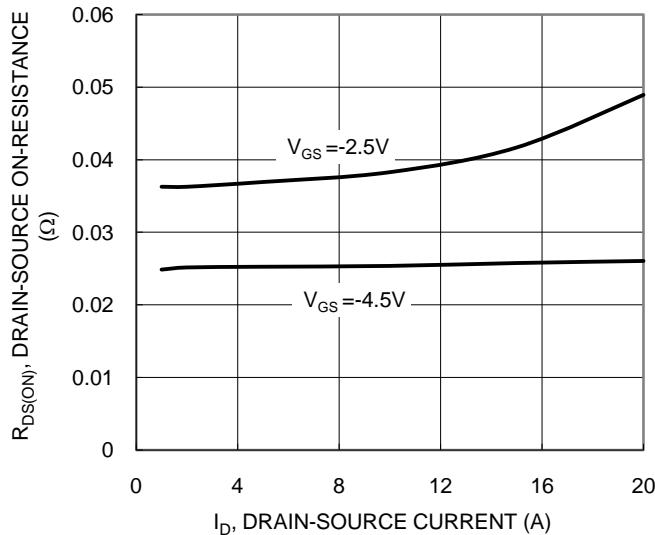


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

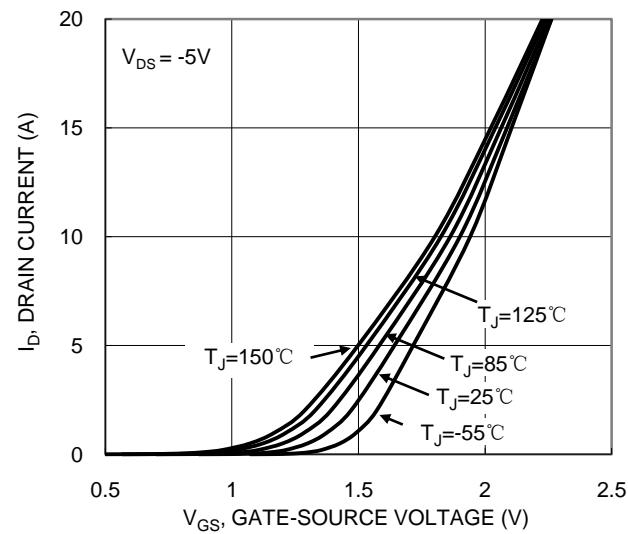


Figure 2. Typical Transfer Characteristic

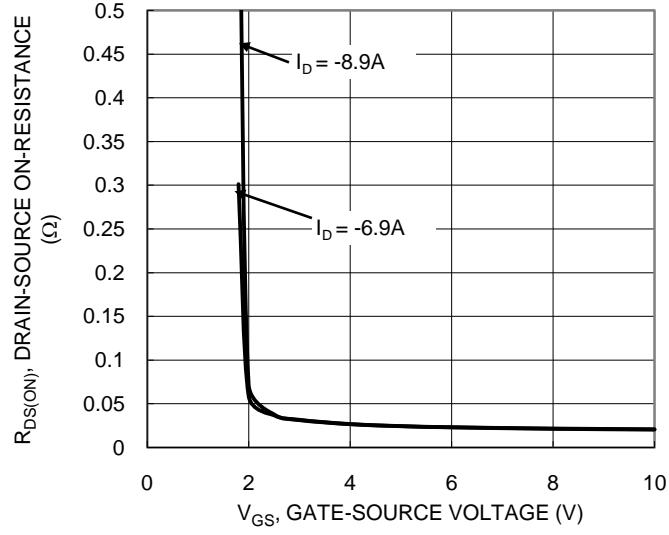


Figure 4. Typical Transfer Characteristic

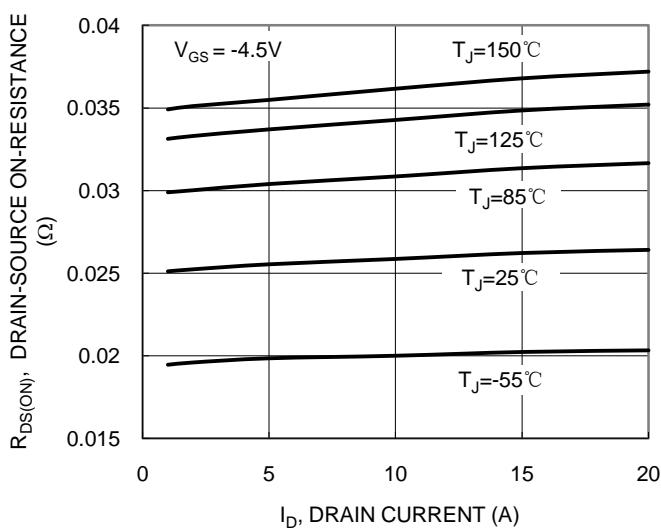


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

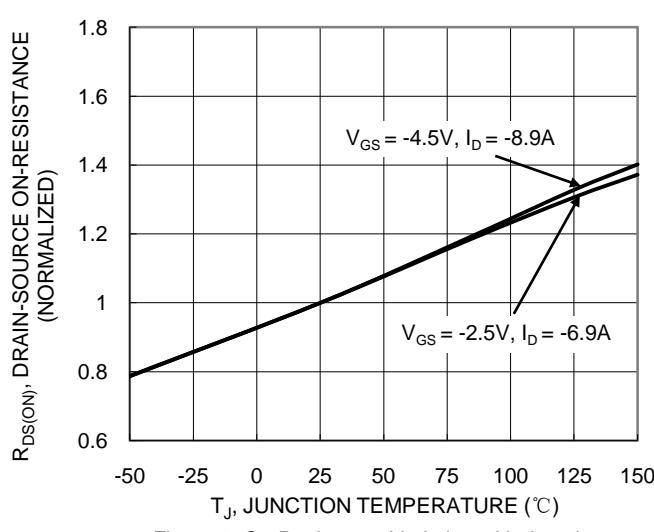


Figure 6. On-Resistance Variation with Junction Temperature

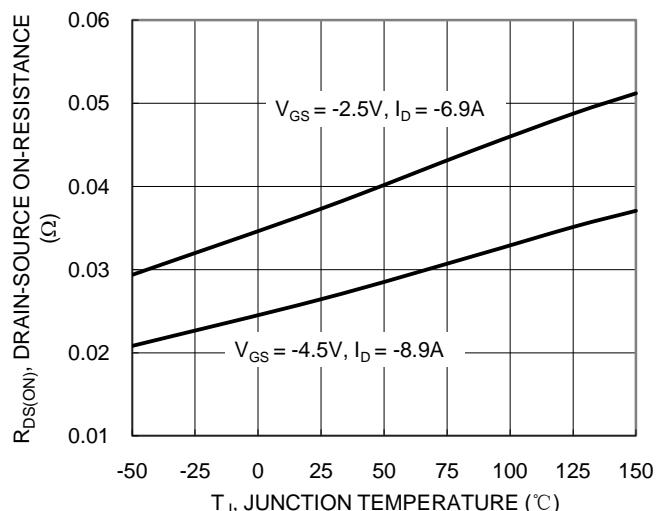


Figure 7. On-Resistance Variation with Junction Temperature

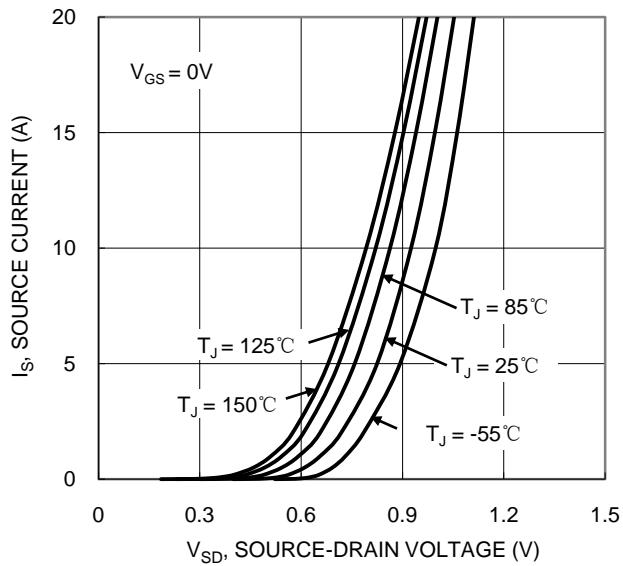


Figure 9. Diode Forward Voltage vs. Current

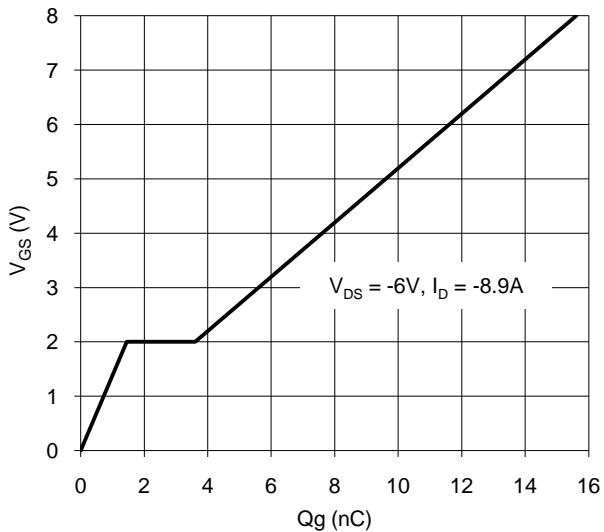


Figure 11. Gate Charge

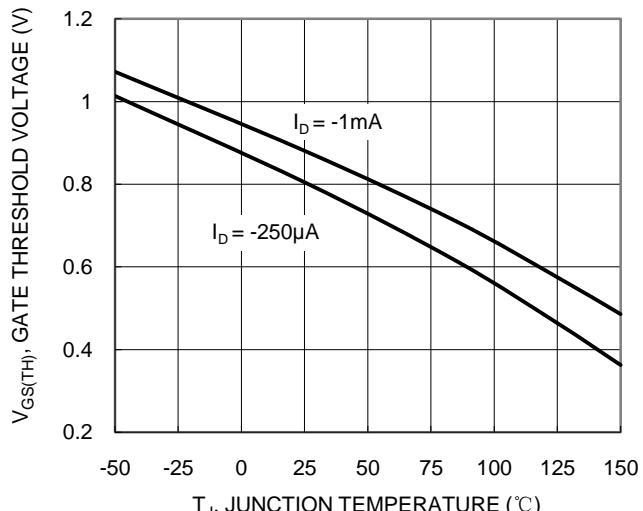


Figure 8. Gate Threshold Variation vs. Junction Temperature

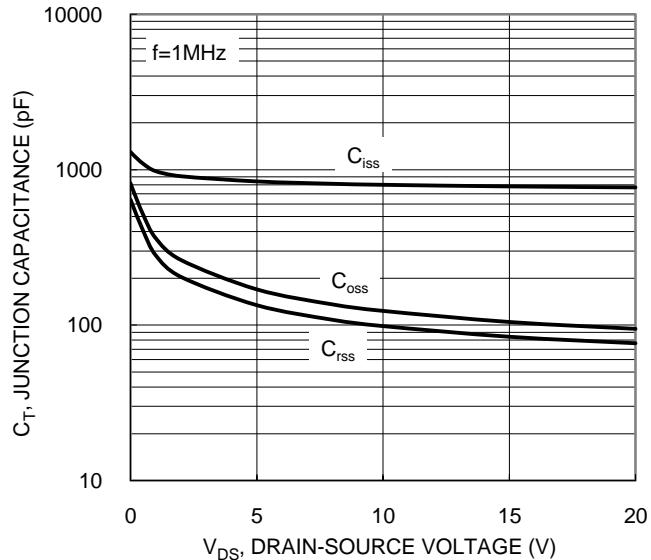


Figure 10. Typical Junction Capacitance

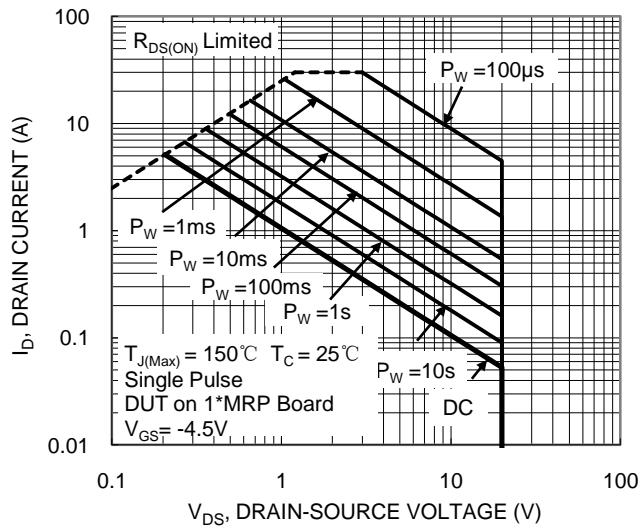


Figure 12. SOA, Safe Operation Area

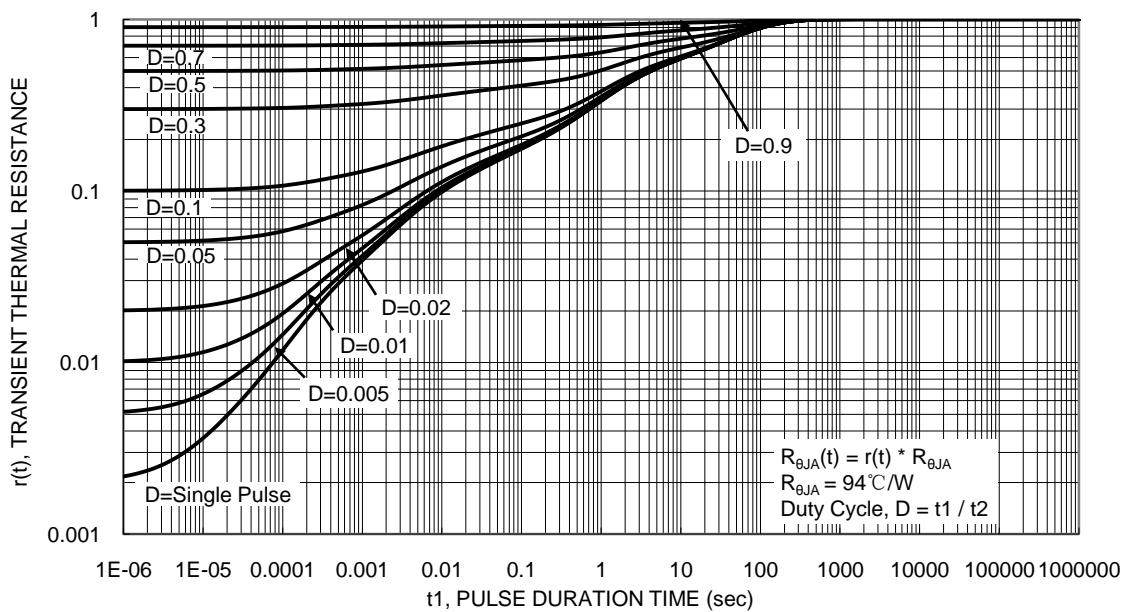
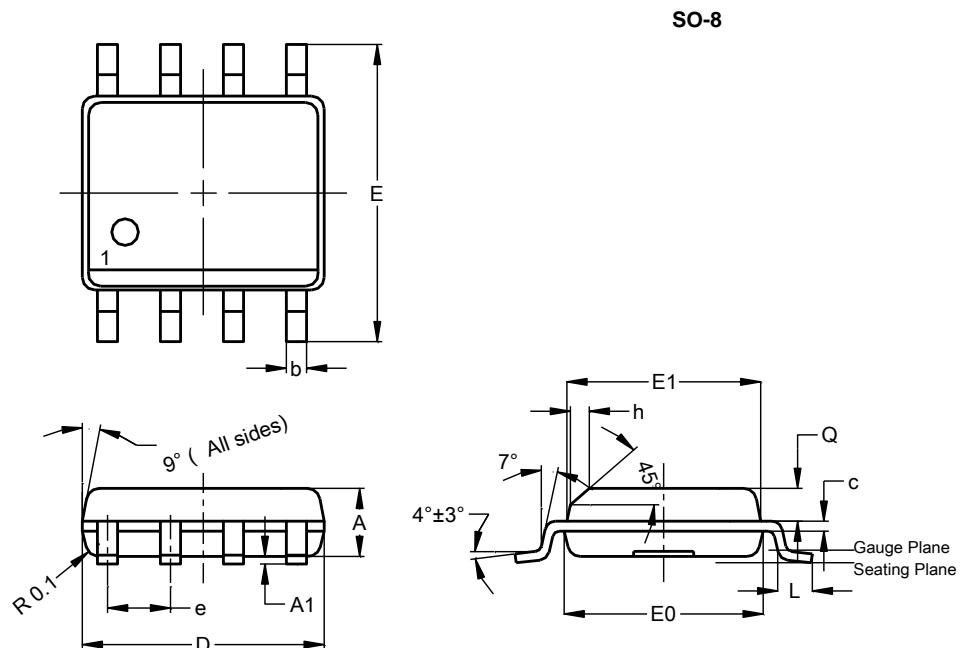


Figure 13. Transient Thermal Resistance

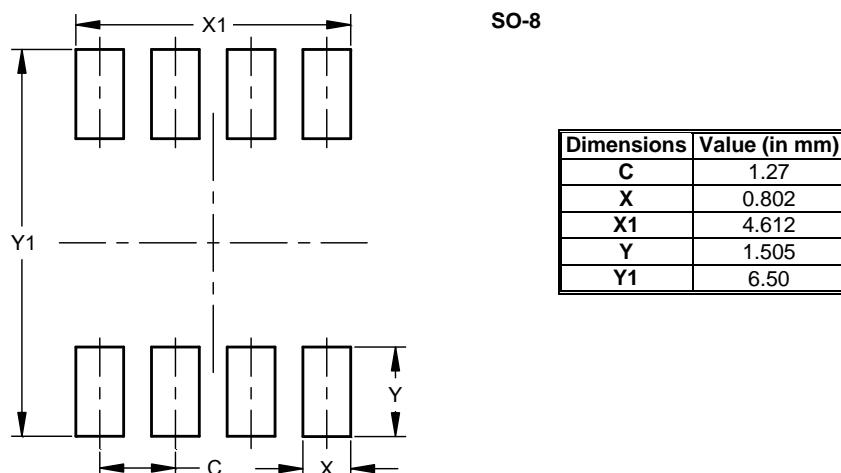
## Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.



## Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.



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