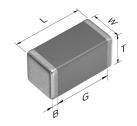


Multilayer Ceramic Chip Capacitors

C1005C0G1H470G050BA

Product Status	Production (NRND)		
TDK Item Description	C1005C0G1H470GT****		
Applications	Commercial Grade		
Feature	General (Up to 75V)		
Series	C1005 [EIA 0402]		
Brand	TDK		
Environmental Compliance	ROHS REACH Halogen Free Lead Free		



	Size	
Length(L)	1.00mm ±0.05mm	
Width(W)	0.50mm ±0.05mm	
Thickness(T)	0.50mm ±0.05mm	
Terminal Width(B)	0.10mm Min.	
Terminal Spacing(G)	0.30mm Min.	
Recommended Land Pattern (PA)	0.30mm to 0.50mm	
Recommended Land Pattern (PB)	0.35mm to 0.45mm	
Recommended Land Pattern (PC)	0.40mm to 0.60mm	

Electrical Characteristics		
Capacitance	47pF ±2%	
Rated Voltage	50VDC	
Temperature Characteristic	C0G(0±30ppm/°C)	
Q (Min.)	1000	
Insulation Resistance (Min.)	10000ΜΩ	

Other		
Operating Temp. Range	-55 to 125°C	
Soldering Method	Reflow	
AEC-Q200	NO	
Packing	Punched (Paper)Taping [180mm Reel]	
Package Quantity	10000pcs	

[!] Images are for reference only and show exemplary products.

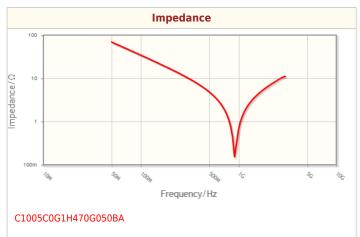
[!] This PDF document was created based on the data listed on the TDK Corporation website. ! All specifications are subject to change without notice.

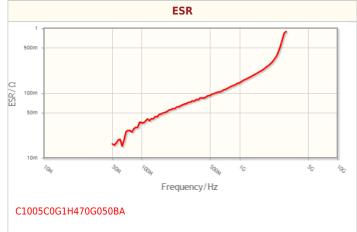


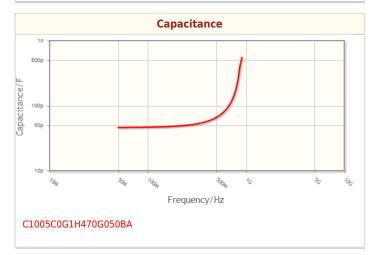
Multilayer Ceramic Chip Capacitors

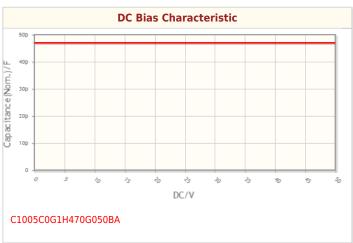
C1005C0G1H470G050BA

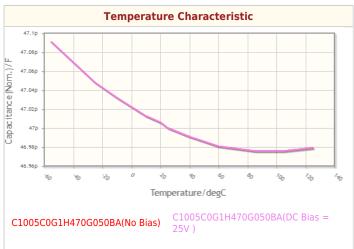
Characteristic Graphs(This is reference data, and does not guarantee the products characteristics.)











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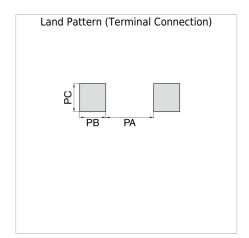
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Multilayer Ceramic Chip Capacitors

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Associated Images



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