

SVF521R3K1CMK4 :Part Detail

- Product Summary Page
- General Information
- Package Information
- Environmental and Compliance Information
- Manufacturing Information
- Ordering Information
- Operating Characteristics
- Reliability Data Lookup

General Information

Part Number	SVF521R3K1CMK4
Description	Vybrid R 32-bit MPU, ARM Cortex-A5 core, ARM Cortex-M4 core, MAPBGA 364
Product Line	GDLD322
PTI	MVNF
Material Type	Tested Packaged Device
Life Cycle Description (code)	PRODUCT RAPID GROWTH
Status	Active
Application/Qualification Tier	AUTO; >=10 YEARS APPLICATION LIFE

Package Information

Package Type and Termination Count	MAPBGA 364
Package Description and Mechanical Drawing	MAP 364 17*17*1.5 P0.8
Device Weight(g)	1.38960
Package Length (nominal)(mm)	17.000
Package Width (nominal)(mm)	17.000
Package Thickness (nominal)(mm)	1.500
Tape & Reel	No

Environmental and Compliance Information

Pb-Free	
RoHS Compliant	
Halogen Free	Yes
Material Composition Declaration (MCD)	Download MCD Report Download MCD Report
RoHS Certificate of Analysis (CoA)	Download RoHS CoA Report
2nd Level Interconnect	e2
Moisture Sensitivity Level (MSL)	3
Floor Life	168 HOURS

Peak Package Body Temperature (PPT)(°C)	260
Maximum Time at Peak Temperature (s)	40
Number of Reflow Cycles	3
REACH SVHC	NXP REACH Statement
UL94 (plastics flammability test)	V0;Burning stops within 10 seconds on a vertical specimen;no drips allowed

Manufacturing Information

Micron Size(μm)	.04
-----------------	-----

Ordering Information

Minimum Package Quantity (MPQ)	90
MPQ Container	TRAY
Exempt from Minimum Delivery Value	Yes
Preferred Order Quantity (POQ)	450
POQ Container	BOX
Export Control Classification Number (US)	5A002A
Harmonized Tariff (	
CCATS Document	G151157~CCATS
ENC Status	Restricted
Other Trade Compliance Documents	-
Budgetary Price excluding tax(US Currency	-
Order	Similar Samples

Operating Characteristics

Additional Features	Media Local Bus SPDIF Rx/Tx
CAN	2
Core Type	ARM Cortex-M4 ARM Cortex-A5
Core: Number of cores (Spec)	2
DSPI	4
ESAI	1
Ethernet	2
External Bus Interface	Flexbus
External Memory Supported	DDR NAND FC Quad SPI

I2C	4
Internal RAM (KB)	1500
Operating Frequency (Max) (MHz)	400
Sample Exception Availability	N
SDIO	2
UART	6
USB features	Integrated PHY USB
Video/Display features	MIU VADC TFT LCD Segment LCD

[Reliability Data Lookup](#)

ABOUT NXP

[Investors](#)
[Partners](#)
[Careers](#)

RESOURCES

[Mobile Apps](#)
[Press, News, Blogs](#)
[Contact Us](#)

FOLLOW US



News 3 May 2017



NXP Semiconductors Reports First
Quarter 2017 Results

[Read More](#)

[Privacy](#) | [Terms of Use](#) | [Terms of Sale](#) | [Feedback](#)

©2006-2017 NXP Semiconductors. All rights reserved.