



Parameter	Rating	Units
Blocking Voltage	250	V <sub>P</sub>
Load Current	250	mA <sub>rms</sub> / mA <sub>DC</sub>
On-Resistance (max)	7	Ω

## Features

- 3750V<sub>rms</sub> Input/Output Isolation
- Low Drive Power Requirements (TTL/CMOS Compatible)
- No Moving Parts
- High Reliability
- Arc-Free With No Snubbing Circuits
- FCC Compatible
- VDE Compatible
- No EMI/RFI Generation
- Small 6-Pin Package
- Machine Insertable, Wave Solderable
- Surface Mount Tape & Reel Version Available

## Applications

- Telecommunications
  - Telecom Switching
  - Tip/Ring Circuits
  - Modem Switching (Laptop, Notebook, Pocket Size)
  - Hook Switch
  - Dial Pulsing
- Instrumentation
  - Multiplexers
  - Data Acquisition
  - Electronic Switching
  - I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Medical Equipment—Patient/Equipment Isolation
- Security Systems
- Aerospace
- Industrial Controls

## Description

PLB150 is a 250V, 250mA, 7Ω, single-pole, normally closed (1-Form-B) solid state relay that uses optically coupled technology to provide an enhanced 3750V<sub>rms</sub> isolation barrier between the input and the output of the relay. The efficient MOSFET switches use IXYS Integrated Circuits Division's patented OptoMOS architecture while the optically coupled output is controlled by a highly efficient GaAlAs infrared LED.

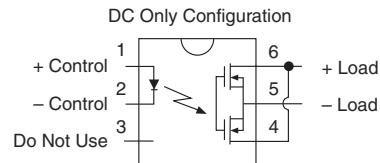
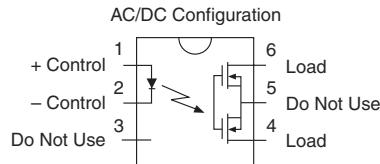
## Approvals

- UL Recognized Component: File E76270
- CSA Certified Component: Certificate 1175739
- EN/IEC 60950-1 Certified Component: TUV Certificate B 09 07 49410 004

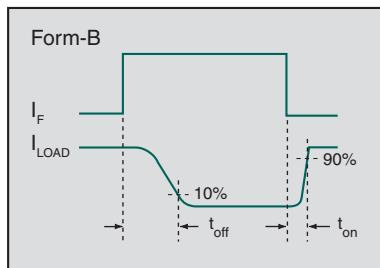
## Ordering Information

Part Number	Description
PLB150	6-Pin DIP (50/Tube)
PLB150S	6-Pin Surface Mount (50/Tube)
PLB150STR	6-Pin Surface Mount (1,000/Reel)

## Pin Configuration



## Switching Characteristics of Normally Closed Devices



## Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Blocking Voltage	250	V <sub>P</sub>
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	A
Input Power Dissipation <sup>1</sup>	150	mW
Total Power Dissipation <sup>2</sup>	800	mW
Isolation Voltage, Input to Output (60 sec.)	3750	V <sub>rms</sub>
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

<sup>1</sup> Derate linearly 1.33 mW / °C

<sup>2</sup> Derate linearly 6.67 mW / °C

*Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.*

## Electrical Characteristics @ 25°C (Unless Otherwise Noted)

Parameter	Conditions	Symbol	Min	Typ	Max	Units
<b>Output Characteristics</b>						
Load Current (Continuous)			-	-	250	mA <sub>rms</sub> / mA <sub>DC</sub>
AC/DC Configuration	-	I <sub>L</sub>	-	-	350	mA <sub>DC</sub>
DC Configuration						
Peak Load Current	10ms	I <sub>LPK</sub>	-	-	±500	mA <sub>P</sub>
On-Resistance						
AC/DC Configuration	I <sub>L</sub> =250mA	R <sub>ON</sub>	-	-	7	Ω
DC Configuration	I <sub>L</sub> =350mA		-	-	3	
Off-State Leakage Current	V <sub>L</sub> =250V <sub>P</sub>	I <sub>LEAK</sub>	-	-	1	μA
Switching Speeds						
Turn-On		t <sub>on</sub>	-	-	1	ms
Turn-Off	I <sub>F</sub> =5 mA, V <sub>L</sub> =10V	t <sub>off</sub>	-	-	2.5	
Output Capacitance	V <sub>L</sub> =50V, f=1MHz	C <sub>OUT</sub>	-	110	-	pF
<b>Input Characteristics</b>						
Input Control Current to Activate	I <sub>L</sub> =250mA	I <sub>F</sub>	-	-	5	mA
Input Control Current to Deactivate	-	I <sub>F</sub>	0.4	0.7	-	mA
Input Voltage Drop	I <sub>F</sub> =5mA	V <sub>F</sub>	0.9	1.2	1.4	V
Reverse Input Current	V <sub>R</sub> =5V	I <sub>R</sub>	-	-	10	μA
<b>Common Characteristics</b>						
Input to Output Capacitance	-	C <sub>I/O</sub>	-	3	-	pF

## Manufacturing Information

### Moisture Sensitivity

 All plastic encapsulated semiconductor packages are susceptible to moisture ingress. IXYS Integrated Circuits Division classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL) rating** as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Rating
PLB150 / PLB150S	MSL 1

### ESD Sensitivity



This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

### Reflow Profile

This product has a maximum body temperature and time rating as shown below. All other guidelines of **J-STD-020** must be observed.

Device	Maximum Temperature x Time
PLB150 / PLB150S	250°C for 30 seconds

### Board Wash

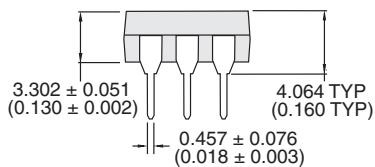
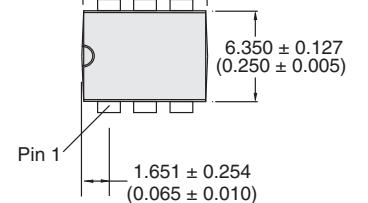
IXYS Integrated Circuits Division recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since IXYS Integrated Circuits Division employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake could be necessary if a wash is used after solder reflow processes. Chlorine- or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.



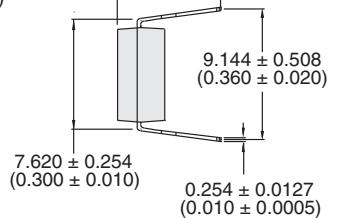
### Mechanical Dimensions

#### PLB150

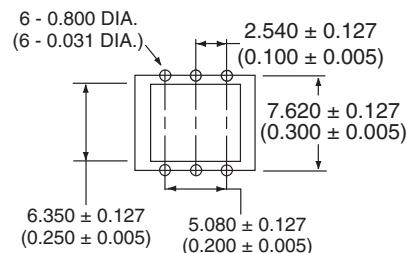
8.382 ± 0.381  
 (0.330 ± 0.015)



7.239 TYP  
 (0.285 TYP)



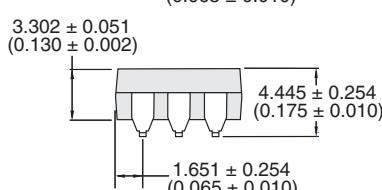
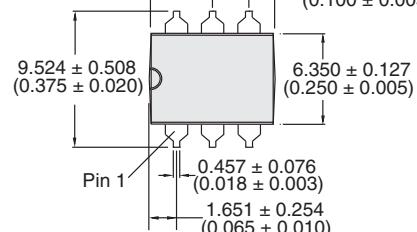
#### PCB Hole Pattern



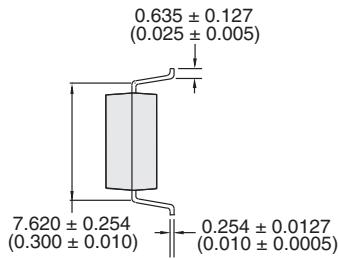
Dimensions  
 mm  
 (inches)

#### PLB150S

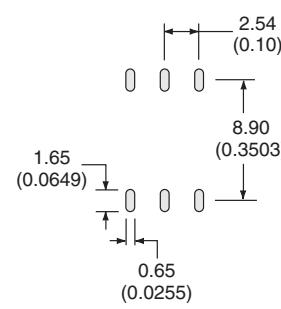
8.382 ± 0.381  
 (0.330 ± 0.015)



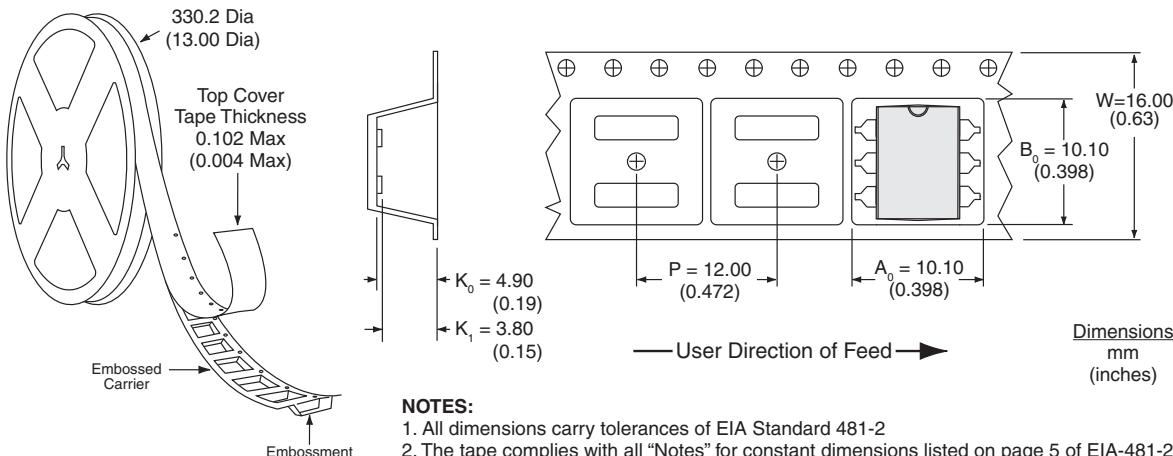
0.635 ± 0.127  
 (0.025 ± 0.005)



#### PCB Land Pattern



Dimensions  
 mm  
 (inches)

**PLB150STR Tape & Reel**

**NOTES:**

1. All dimensions carry tolerances of EIA Standard 481-2
2. The tape complies with all "Notes" for constant dimensions listed on page 5 of EIA-481-2

**For additional information please visit our website at: [www.ixysic.com](http://www.ixysic.com)**

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