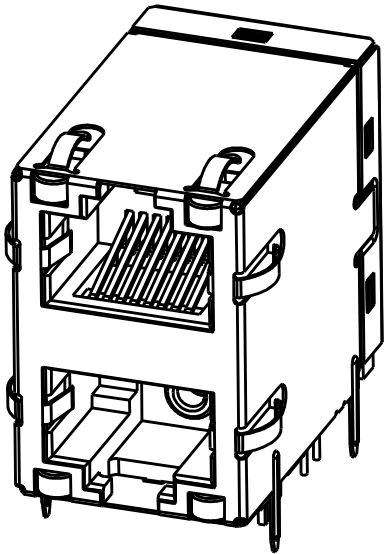
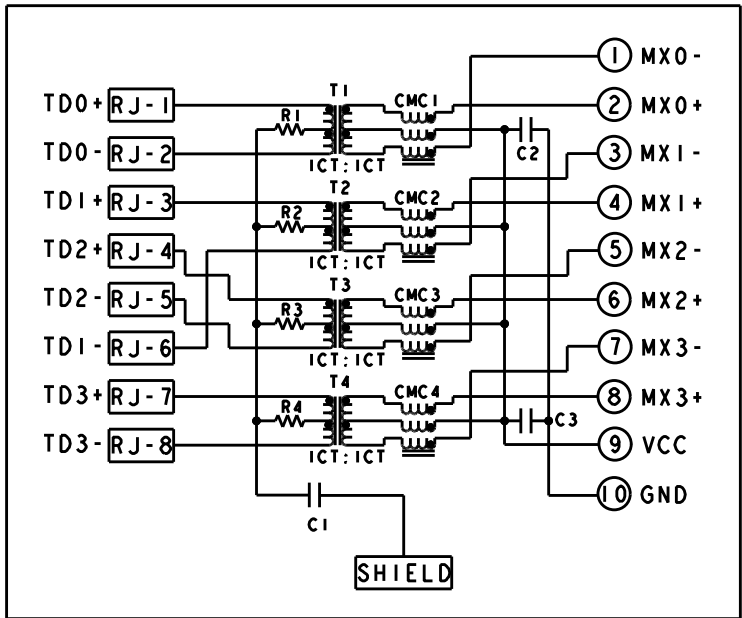




S8G56G GIGABIT CIRCUIT
TOP AND BOTTOM PORTS



C1 = 1000pF, 2kV CAPACITOR
C2 - C3 = 10nF, 50V CAPACITORS
R1-R4 = 75 Ohms, RESISTORS

1. MATERIALS:
PLASTIC HOUSING: BLACK, THERMOPLASTIC FLAMMABILITY RATING UL 94V-0
SHIELD: BRASS, PREPLATED WITH 0.76um MIN SEMI-BRIGHT NICKEL,
POST DIPPED WITH 2.54um MIN SAC SOLDER OR PURE TIN ON SOLDER TAILS.
CONTACTS: PHOSPHOR BRONZE, 1.27um MIN OVERALL NICKEL
UNDERPLATE WITH SELECT 1.27um MIN GOLD OR WITH SELECT 0.05um
MIN GOLD OVER 0.76um MIN PALLADIUM-NICKEL AT MATING INTERFACE
AND 2.54um MIN MATTE TIN ON SOLDER TAILS.
LED: DIFFUSED EPOXY LENS, CARBON STEEL LEAD FRAME TAILS OF LED
ARE PREPLATED WITH 2.03um MIN SILVER OVER 1.02um MIN NICKEL
UNDERPLATE OVER 1.02um MIN COPPER UNDERPLATE. POST-PLATED WITH
2.54um MIN MATTE TIN AND/OR SAC SOLDER DIP OR PURE TIN SOLDER DIP

2. MAGNETICS
APPLICATION: 10/100/1000 BASE-T
IMPEDANCE: 100 OHMS
TURNS RATIO (CHIP:CABLE): 1:1 ALL FOUR PAIRS
OPEN CIRCUIT INDUCTANCE (OCL): 350uH MIN @100kHz, 0.1VRMS,
8mADC BIAS FROM 0°C TO 70°C, ALL FOUR PAIRS
ALL FOUR PAIRS BI-DIRECTIONAL
PERFORMANCE @ 25°C:
INSERTION LOSS (IL): 1.1dB MAX FROM 0.5MHz TO 100MHz
RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 40MHz
12-20LOG(f/80)dB MIN FROM 40.1MHz TO 100MHz
CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz
33-20LOG(f/50)dB MIN FROM 40.1MHz TO 100MHz
COMMON MODE REJECTION RATIO (CMRR): 30dB MIN FROM 0.5MHz TO 100MHz
ISOLATION VOLTAGE: 2250VDC (MAX) FOR 60 SECONDS WITH A RISE TIME OF 500V/SEC AND
WITH ALL PORTS CONNECTED

3. PART NUMBER, DATE CODE AND COUNTRY OF ORIGIN ARE LOCATED
IN APPROXIMATE AREA SHOWN. DATE CODE: "YY" IS YEAR, "WW" IS
WORK WEEK, "D" IS DAY OF WEEK, WITH SUNDAY=1

4. TRP LOGO AND AGENCY APPROVAL LOGO ARE
LOCATED IN APPROXIMATE AREA SHOWN.

5. OPERATING TEMP: FROM 0°C TO +70°C.

6. RJ45 CAVITY CONFORMS TO FCC RULES AND REGULATION PART 68 SUBPART F.

7. INDICATED MAGNETIC CONNECTIONS ARE SYMMETRICAL AND
SUPPORT AUTO-MDI/MDIX.

8. DATUM AND BASIC DIMENSION ESTABLISHED BY CUSTOMER.

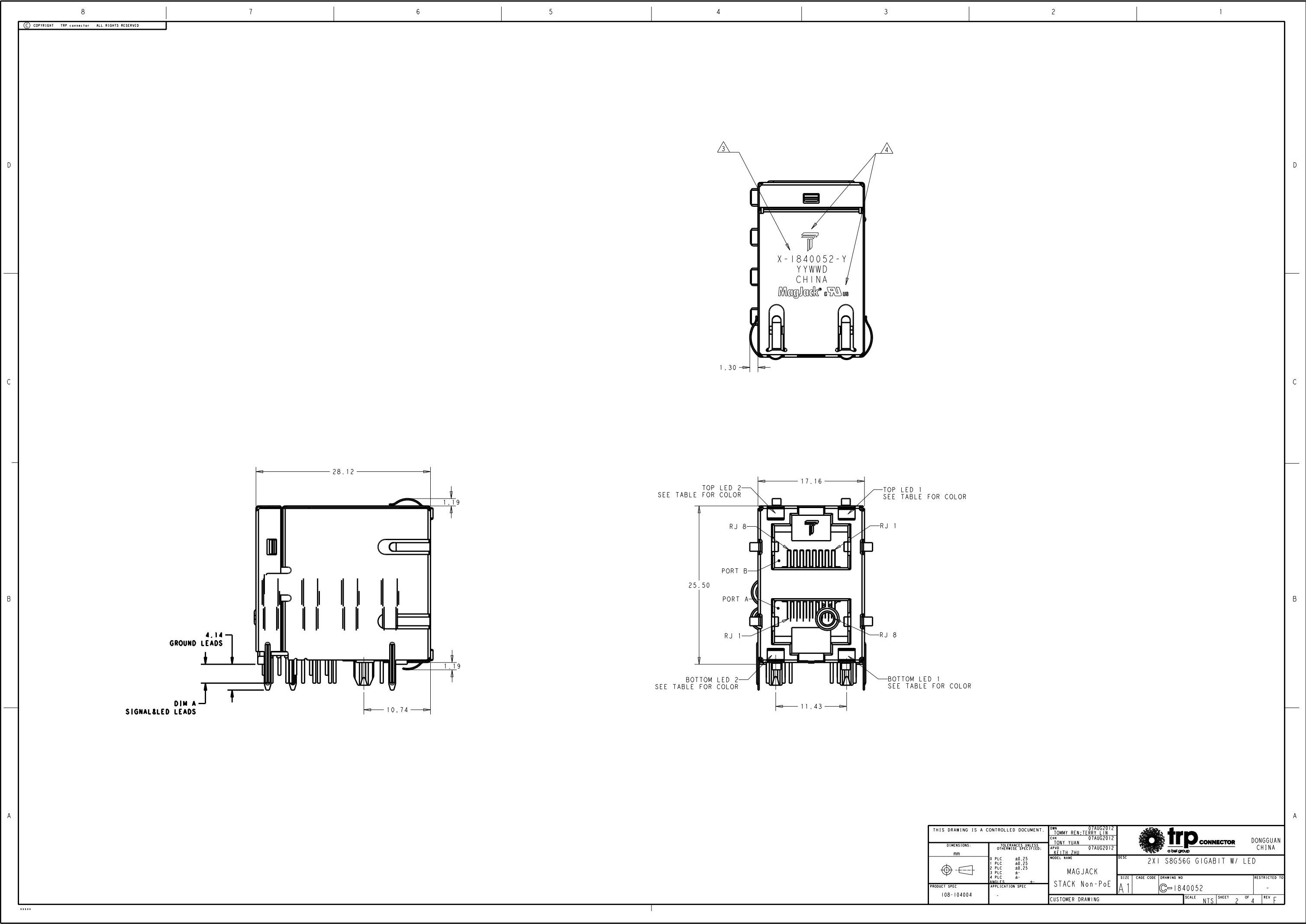
9. BASIC DIMENSION ESTABLISHED BY CUSTOMER, BUT MAY NOT BE
GREATER THAN 5.08mm.

10. LEDS ARE DRIVEN WITH CONSTANT CURRENT AT APPROX 20mA
LED COLOR: DOMINANT WAVELENGTH (λD): GREEN 568 nm TYP. @ IF=20mA
FORWARD VOLTAGE (VF): GREEN 2.2V TYP. @ IF=20mA
DOMINANT WAVELENGTH (λD): YELLOW 588 nm TYP. @ IF=20mA
FORWARD VOLTAGE (VF): YELLOW 2.1V TYP. @ IF=20mA
DOMINANT WAVELENGTH (λD): ORANGE 605 nm TYP. @ IF=20mA
FORWARD VOLTAGE (VF): ORANGE 2.1V TYP. @ IF=20mA
DOMINANT WAVELENGTH (λD): ULTRA LOW WAVELENGTH GREEN (ULWG) 525 nm TYP. @ IF=20mA
FORWARD VOLTAGE (VF): ULTRA LOW WAVELENGTH GREEN (ULWG) 2.8V TYP. @ IF=20mA

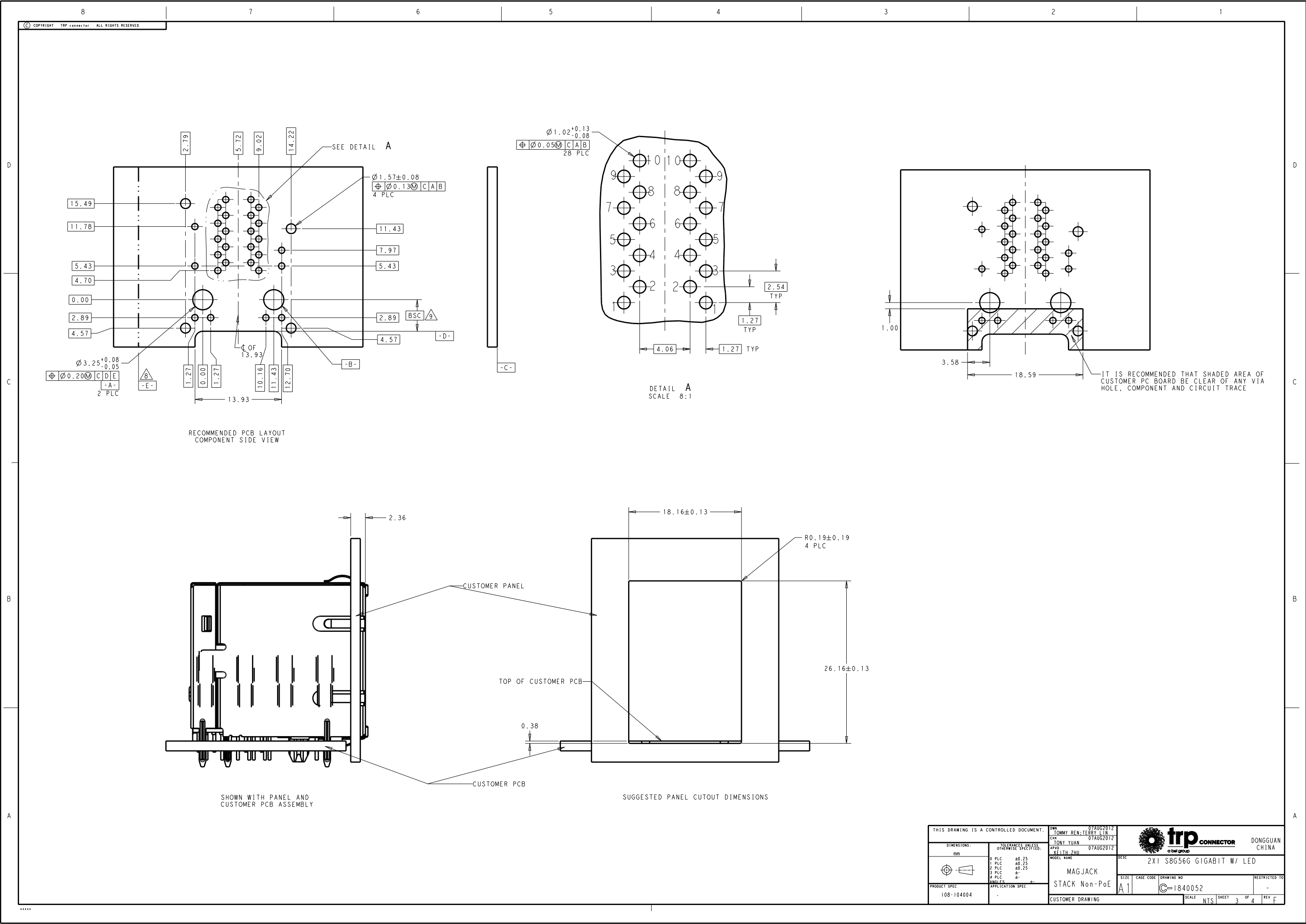
11. THE PARTS ARE RECOMMENDED FOR WAVE SOLDERING PROCESS.
PEAK TEMPERATURE 260°C MAX, 10 SECONDS MAX.

4.06	ULWG/YELLOW	ULWG/YELLOW	ULWG/YELLOW	ULWG/YELLOW	3-1840052-9
3.56	GREEN/YELLOW	GREEN	GREEN	GREEN/YELLOW	1-1840052-9
3.56	GREEN	GREEN	GREEN	GREEN	1-1840052-1
3.04	GREEN	GREEN/ORANGE	GREEN	GREEN/ORANGE	1840052-6
3.04	GREEN/YELLOW	GREEN/YELLOW	GREEN/YELLOW	GREEN/YELLOW	1840052-4
DIM A	BOTTOM LED 2	BOTTOM LED 1	TOP LED 2	TOP LED 1	PART NUMBER

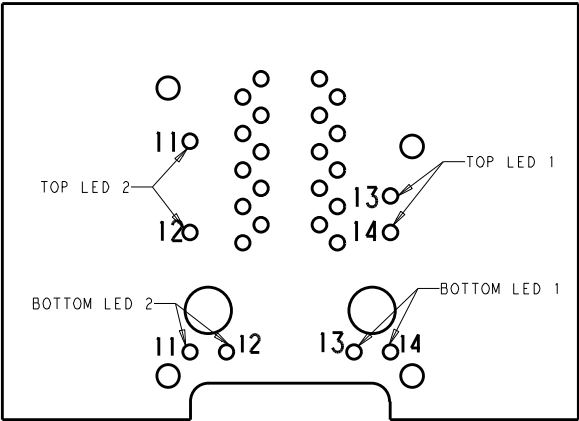
THIS DRAWING IS A CONTROLLED DOCUMENT.					
DIMENSIONS: mm		TOLERANCES UNLESS OTHERWISE SPECIFIED:		DRAWN: TOMMY REN; TERRY LIN CHK: TONY YUAN APVD: KELTH ZHU DATE: 07AUG2012	
0 PLC ±0.25 1 PLC ±0.25 2 PLC ±0.25 3 PLC ± 4 PLC ±		MODEL NAME: MAGJACK STACK Non-PoE		DESC: 2X1 S8G56G GIGABIT W/ LED	
PRODUCT SPEC: 108-104004		APPLICATION SPEC:		SIZE: A1 CAGE CODE: 1840052 DRAWING NO: 1840052	
CUSTOMER DRAWING		SCALE: NTS		SHEET 1 OF 4 REV F	



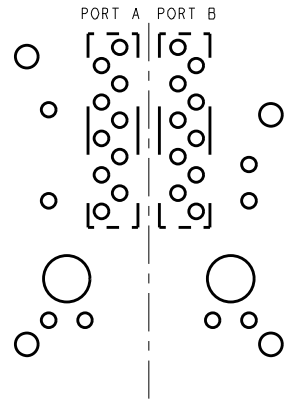
THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN 07AUG2012 CKD TOMMY REN: TERRY LIN 07AUG2012 CKD TONY YUAN 07AUG2012 APVD KEITH ZHU 07AUG2012	 trp CONNECTOR a bel group		DONGGUAN CHINA
DIMENSIONS: mm		TOLERANCES UNLESS OTHERWISE SPECIFIED:			
		0 PLOC	±0.25	DESC 2X1 S8G56G GIGABIT W/ LED	
		1 PLOC	±0.25		
		2 PLOC	±0.25		
		3 PLOC	±0.25		
		4 PLOC	±0.25		
PRODUCT SPEC 108-104004		APPLICATION SPEC		RESTRICTED TO	
				-	
		CUSTOMER DRAWING		SCALE NTS	
				SHEET 2 OF 4	
				REV F	



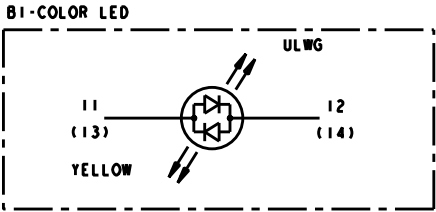
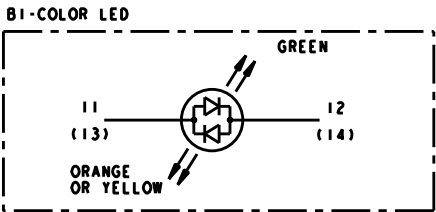
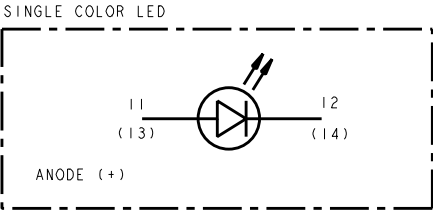
THIS DRAWING IS A CONTROLLED DOCUMENT.		OWN 07AUG2012 TOMMY REN;TERRY LIN		 trp CONNECTOR DONGGUAN CHINA	DESC 2X1 S8G56G GIGABIT W/ LED
DIMENSIONS: mm		CHK 07AUG2012 TONY YUAN			
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD 07AUG2012 KEITH ZHU		MODEL NAME MAGJACK	
0 PLC ± 0.25 1 PLC ± 0.25 2 PLC ± 0.25 3 PLC $\pm$ 4 PLC $\pm$ ANGLES		APPLICATION SPEC			
		PRODUCT SPEC 108-104004		SIZE A1	
		CUSTOMER DRAWING		CAGE CODE C=1840052	
				SCALE NTS	
				SHEET 3 OF 4	
				REV F	



LED HOLE DESIGNATIONS
VIEWED FROM COMPONENT SIDE



PORT ASSIGNMENT
COMPONENT SIDE VIEW



THIS DRAWING IS A CONTROLLED DOCUMENT.				DWN 07AUG2012 TOMMY REN;TERRY LIN		 trp CONNECTOR a bel group		DONGGUAN CHINA	
				CHK 07AUG2012 TONY YUAN					
DIMENSIONS: mm		TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD 07AUG2012 KEITH ZHU					
		0 PLC ±0.25 1 PLC ±0.25 2 PLC ±0.25 3 PLC ± 4 PLC ± ANGLES ±°		MODEL NAME MAGJACK		DESC 2X1 S8G56G GIGABIT W/ LED			
PRODUCT SPEC 108-104004		APPLICATION SPEC -		STACK Non-PoE		SIZE CAGE CODE DRAWING NO A1 C=1840052		RESTRICTED TO -	
CUSTOMER DRAWING						SCALE NTS		SHEET 4 of 4 REV F	