UML1N

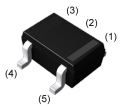
PNP complex transistor with switching diode

Datasheet

Parameter	Value
V_{CEO}	-50V
I_C	-150mA

●Outline

SOT-353
SC-88A



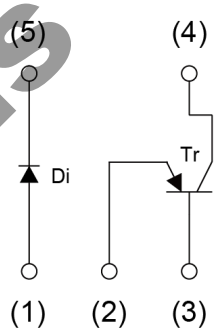
UMT5

●Features

1)The 2SA1774 and a diode are housed independently in a SOT-353 package.

●Inner circuit

(1) Di Anode
(2) Tr Emitter
(3) Tr Base
(4) Tr Collector
(5) Di Cathode



●Application

Low-frequency

●Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
UML1N	SOT-353 (UMT5)	2021	TR	180	8	3000	L1

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●Absolute maximum ratings (T_a = 25°C)

Pin No.1-5 Diode

Parameter	Symbol	Value	Unit
Reverse voltage	V_R	80	V
Repetitive peak reverse voltage	V_{RM}	80	V
Average rectified current	I_F	100	mA
Peak forward current	I_{FM}	300	mA
Surge current	I_{surge}	4	A
Rated in slash put frequency	f	100	MHz

Pin No.2-3-4 Transistor

Parameter	Symbol	Value	Unit
Collector-base voltage	V_{CBO}	-60	V
Collector-emitter voltage	V_{CEO}	-50	V
Emitter-base voltage	V_{EBO}	-6	V
Collector current	I_C	-150	mA

Each element

Parameter	Symbol	Value	Unit
Power dissipation	P_D *1,*2	150	mW/Total
Junction temperature	T_j	150	℃
Range of storage temperature	T_{stg}	-55 ～ +150	℃

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●Electrical characteristics (T_a = 25°C)

Pin No.1-5 Diode

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Forward voltage	V_F	I_F = 100mA	-	-	1.2	V
Reverse current	I_R	V_R = 70V	-	-	100	nA
Capacitance between terminals	C_T	V_R = 6V , f = 1MHz	-	-	3.5	pF
Reverse recovery time	t_{rr}	V_R = 6V , I_F = 5mA R_L = 50Ω (Figure 1)	-	-	4	ns

Pin No.2-3-4 Transistor

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Collector-base breakdown voltage	BV_{CBO}	I_C = -50μA	-60	-	-	V
Collector-emitter breakdown voltage	BV_{CEO}	I_C = -1mA	-50	-	-	V
Emitter-base breakdown voltage	BV_{EBO}	I_E = -50μA	-6	-	-	V
Collector cut-off current	I_{CBO}	V_{CB} = -60V	-	-	-100	nA
Emitter cut-off current	I_{EBO}	V_{EB} = -5V	-	-	-100	nA
Collector-emitter saturation voltage	$V_{CE(sat)}$	I_C = -50mA, I_B = -5mA	-	-	-500	mV
DC current gain	h_{FE}	V_{CE} = -6V, I_C = -1mA	120	-	560	-
Transition frequency	f_T*3	V_{CE} = -12V, I_E = 2mA, f = 100MHz	-	140	-	MHz
Output capacitance	C_{ob}	V_{CB} = -12V, I_E = 0A, f = 1MHz	-	4.0	5.0	pF

*1 Each termunal mounted on a reference land.

*2 120mW per element must not be exceeded.

*3 Characteristics of built-in transistor.

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●Electrical characteristic curves(T_a=25°C) <For Diode>

Fig.1 Reverse Current vs. Reverse Voltage

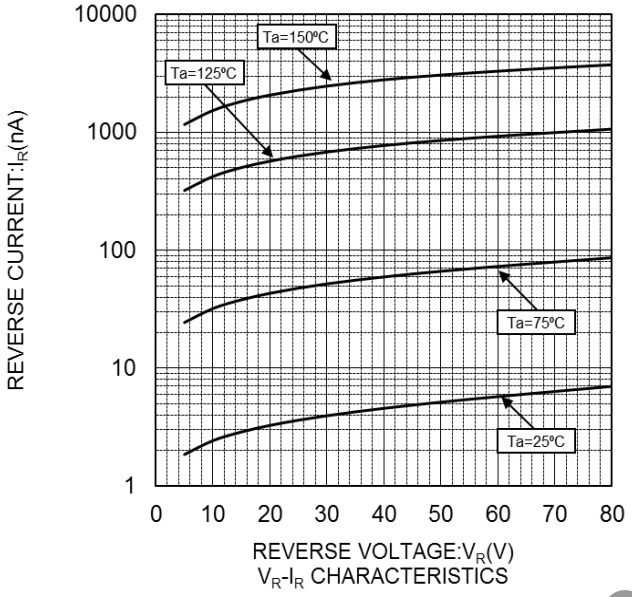


Fig.2 Forward Current vs. Forward Voltage

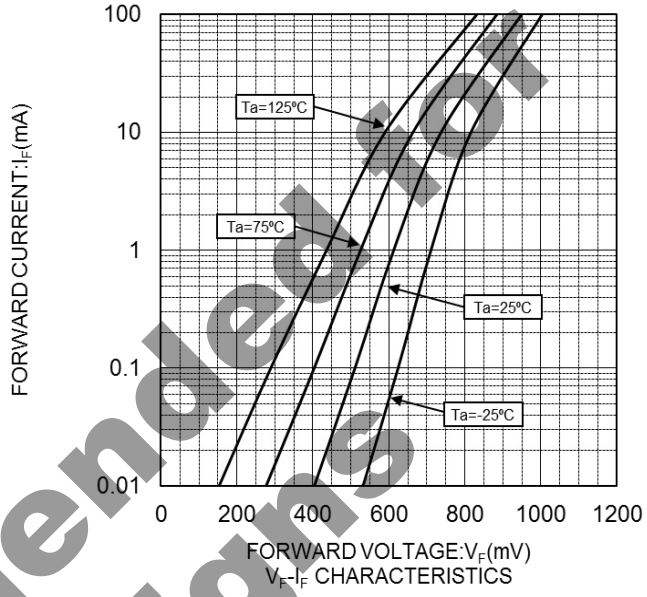
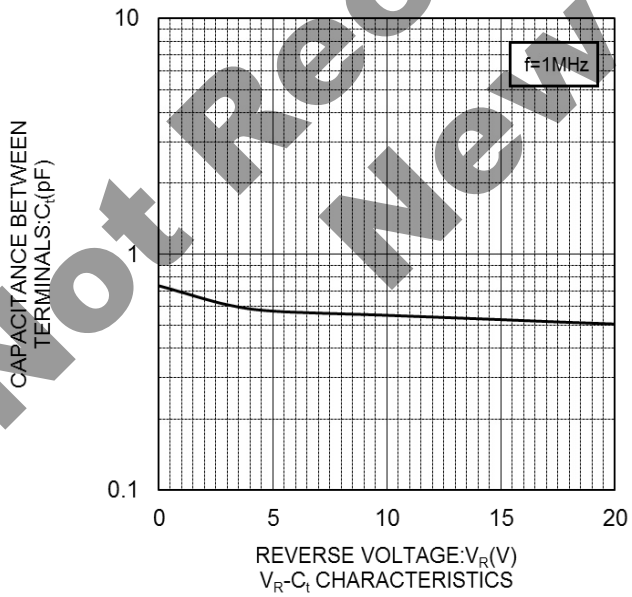


Fig.3 Capacitance Between Terminals vs. Reverse Voltage



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Fig.4 Ground Emitter Propagation Characteristics

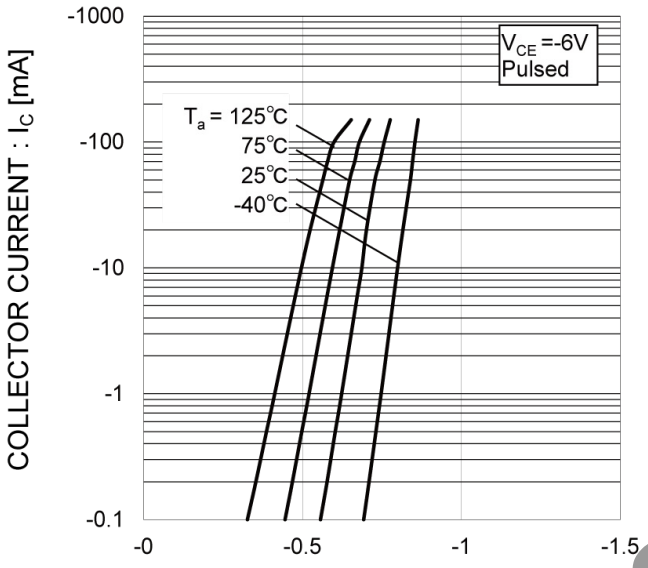


Fig.5 Typical Output Characteristics

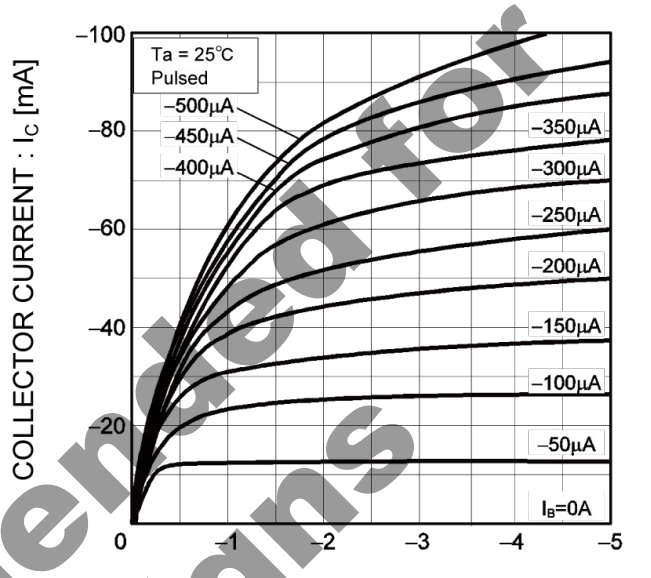


Fig.6 DC Current Gain vs. Collector Current (I)

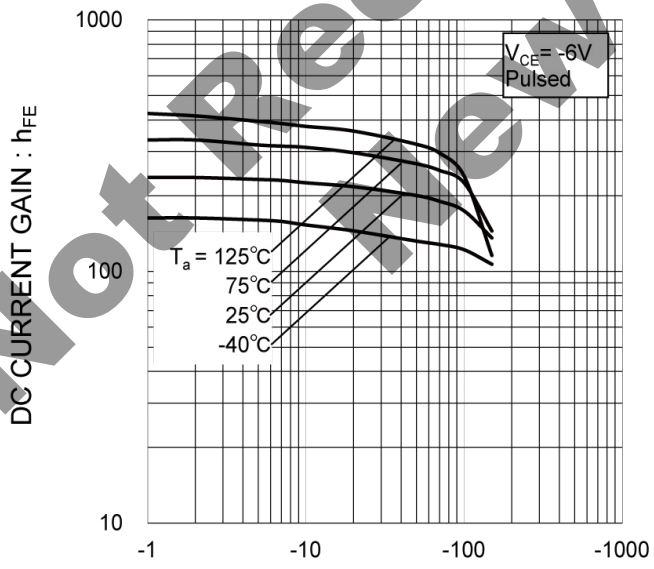
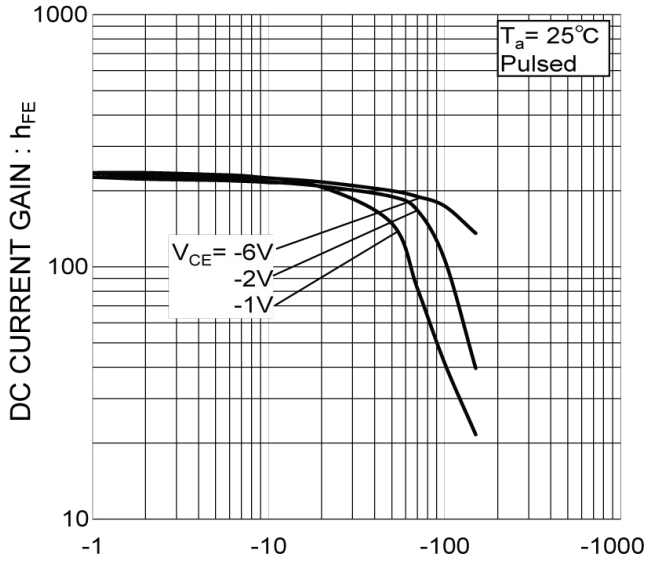


Fig.7 DC Current Gain vs. Collector Current (II)



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●Electrical characteristic curves(T_a=25°C) <For Transistor>

Fig.8 Collector-Emitter Saturation Voltage vs. Collector Current (I)

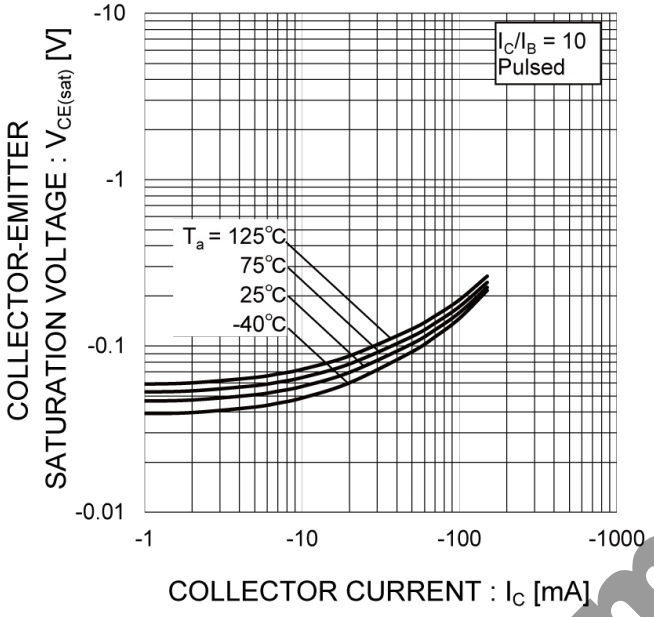


Fig.9 Collector-Emitter Saturation Voltage vs. Collector Current (II)

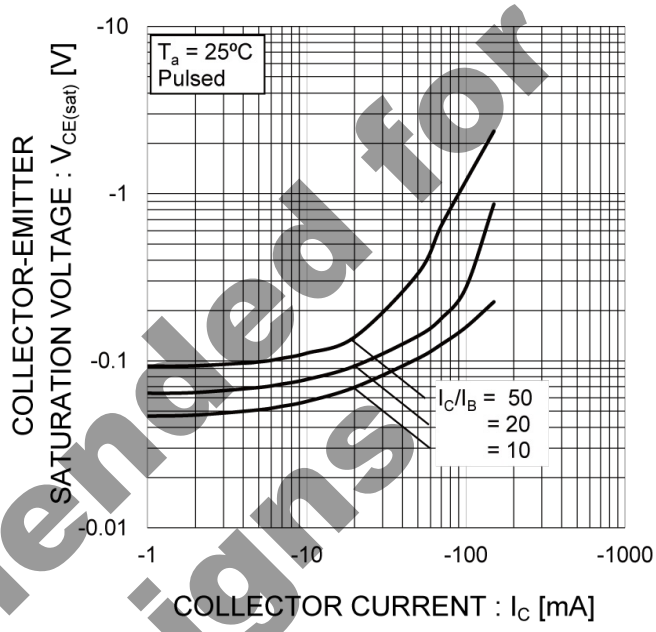


Fig.10 Base-Emitter Saturation Voltage vs. Collector Current

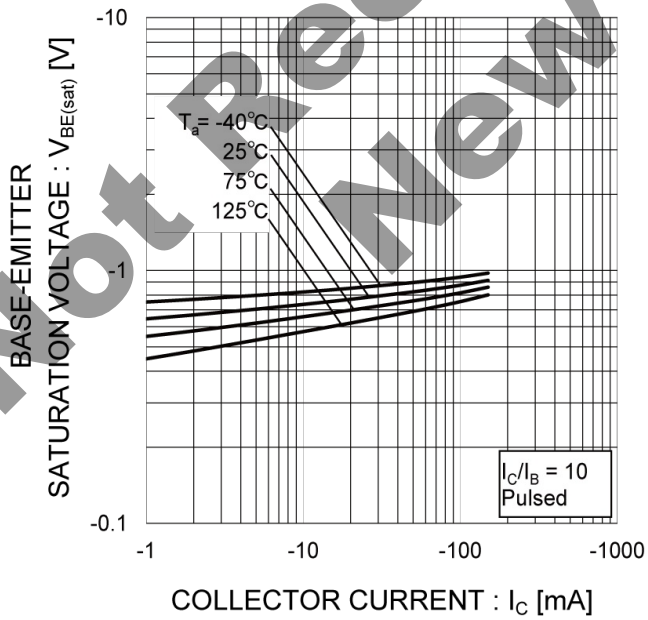
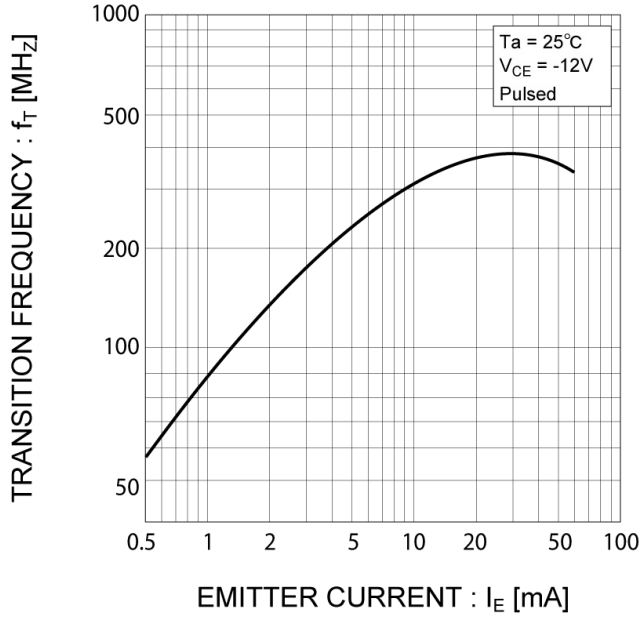


Fig.11 Gain Bandwidth Product vs. Emitter Current



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Fig.12 Emitter Input Capacitance vs. Emitter-Base Voltage
Collector Output Capacitance vs. Collector-Base Voltage

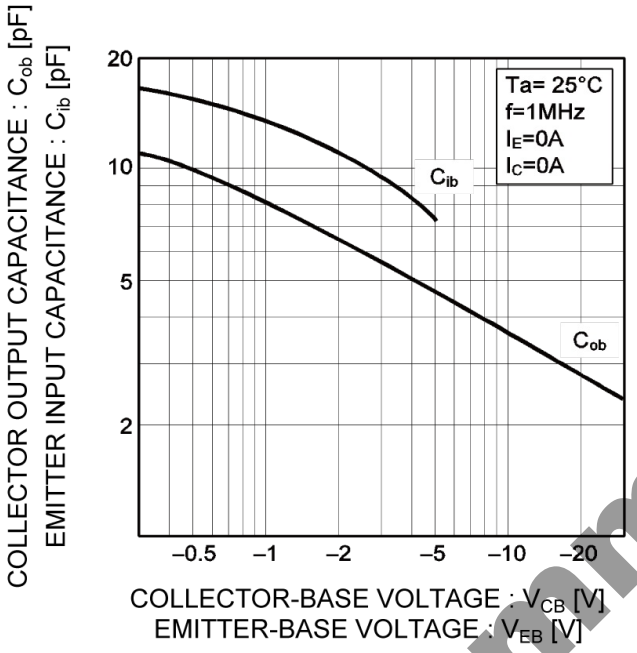
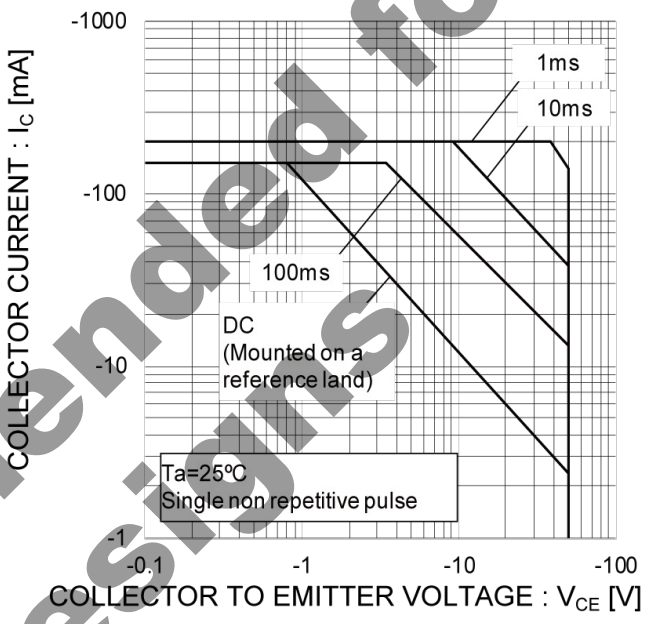
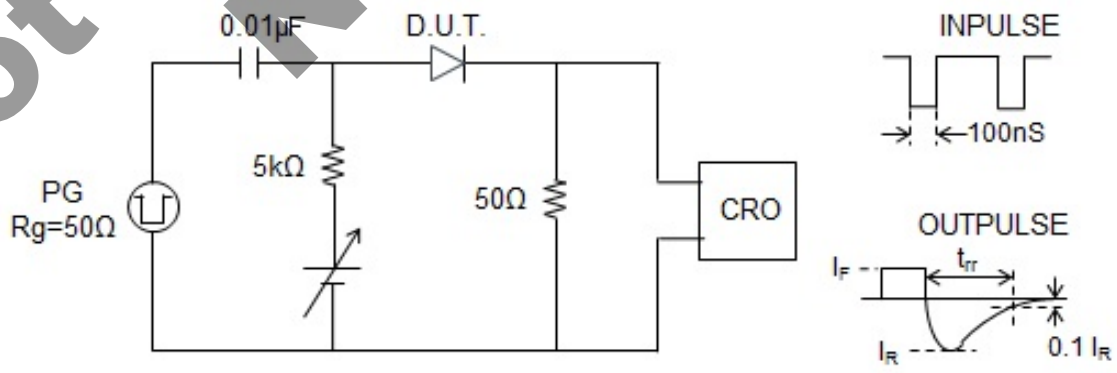


Fig.13 Safe Operating Area



(figure 1) Reverse recovery time test circuit



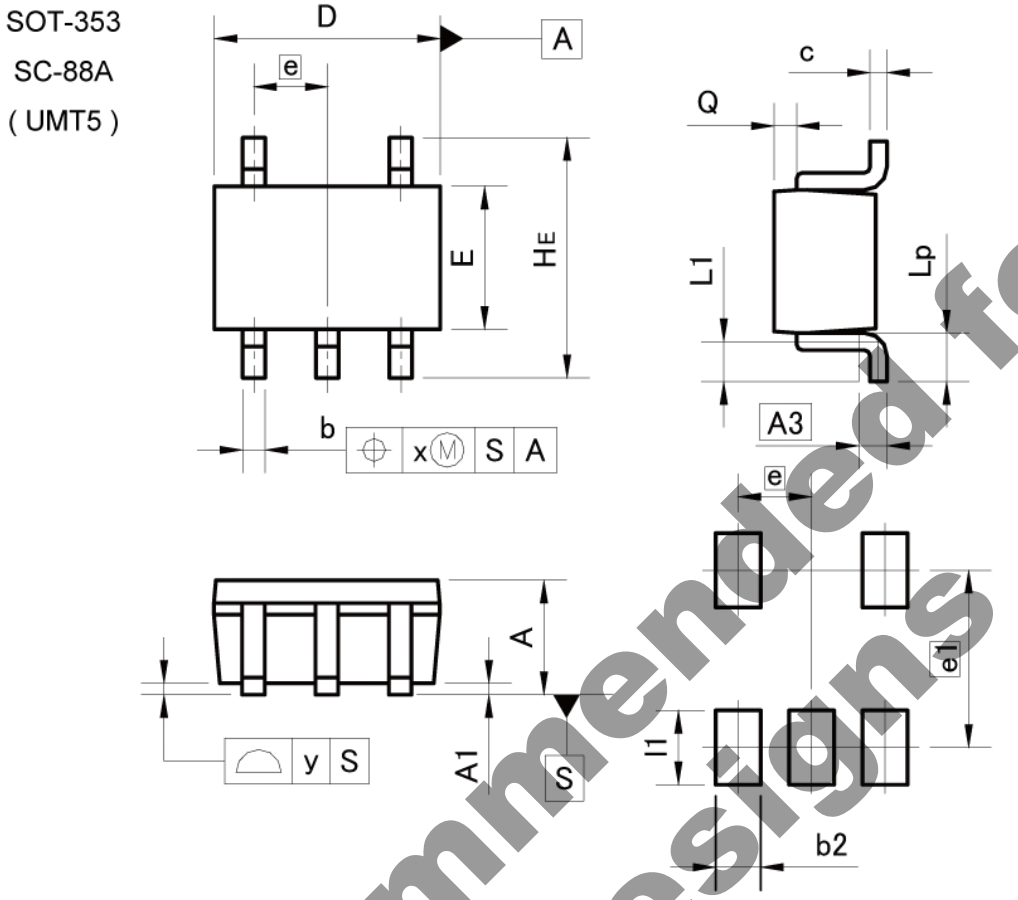
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●Dimensions

SOT-353
SC-88A
(UMT5)



Pattern of terminal position areas
[Not a pattern of soldering pads]

DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.80	1.00	0.031	0.039
A1	0.00	0.10	0.000	0.004
A3	0.25		0.010	
b	0.15	0.30	0.006	0.012
c	0.10	0.20	0.004	0.008
D	1.90	2.10	0.075	0.083
E	1.15	1.35	0.045	0.053
e	0.65		0.026	
HE	2.00	2.20	0.079	0.087
L1	0.10	0.40	0.004	0.016
Lp	0.25	0.55	0.010	0.022
Q	0.10	0.30	0.004	0.012
x	–	0.10	–	0.004
y	–	0.10	–	0.004

DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
b2	–	0.40	–	0.016
e1	1.55		0.061	
l1	–	0.65	–	0.026

Dimension in mm/inches

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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASSⅡb	CLASSⅢ
CLASSⅣ		CLASSⅢ	

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   [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure

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   [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
   [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl_2, H_2S, NH_3, SO_2, and NO_2
   [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
   [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
   [f] Sealing or coating our Products with resin or other coating materials
   [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
   [h] Use of the Products in places subject to dew condensation

4. The Products are not subject to radiation-proof design.

5. Please verify and confirm characteristics of the final or mounted products in using the Products.

6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.

7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.

8. Confirm that operation temperature is within the specified range described in the product specification.

9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.

2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
   [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
   [b] the temperature or humidity exceeds those recommended by ROHM
   [c] the Products are exposed to direct sunshine or condensation
   [d] the Products are exposed to high Electrostatic

2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.

3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.

4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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