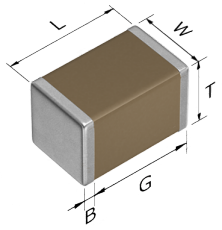
Multilayer Ceramic Chip Capacitors

CGA4J3X7S2A474K125AE

Product Status	Production
TDK Item Description	CGA4J3X7S2A474KT***S
Applications	Automotive Grade
Feature	AEC-Q200AEC-Q200 MidMid Voltage (100 to 630V) SoftSoft Termination
Series	CGA4(2012) [EIA 0805]
Brand	TDK
Environmental Compliance	RoHS, REACH, Halogen Free, Lead Free



Size	
Length(L)	2.00mm +0.45,-0.20mm
Width(W)	1.25mm +0.25,-0.20mm
Thickness(T)	1.25mm +0.25,-0.20mm
Terminal Width(B)	0.20mm Min.
Terminal Spacing(G)	0.50mm Min.
Recommended Land Pattern (PA)	1.00mm to 1.30mm(Flow Soldering) 0.90mm to 1.20mm(Reflow Soldering)
Recommended Land Pattern (PB)	1.00mm to 1.20mm(Flow Soldering) 0.70mm to 0.90mm(Reflow Soldering)
Recommended Land Pattern (PC)	0.80mm to 1.10mm(Flow Soldering) 0.90mm to 1.20mm(Reflow Soldering)

Electrical Characteristics	
Capacitance	470nF ±10%
Rated Voltage	100VDC
Temperature Characteristic	X7S(±22%)
Dissipation Factor (Max.)	5%
Insulation Resistance (Min.)	1063MΩ

Other	
Operating Temp. Range	-55 to 125°C
Soldering Method	Wave (Flow) Reflow
AEC-Q200	YES
Packing	Embossed (Plastic)Taping [180mm Reel]
Package Quantity	2000pcs

! Images are for reference only and show exemplary products.
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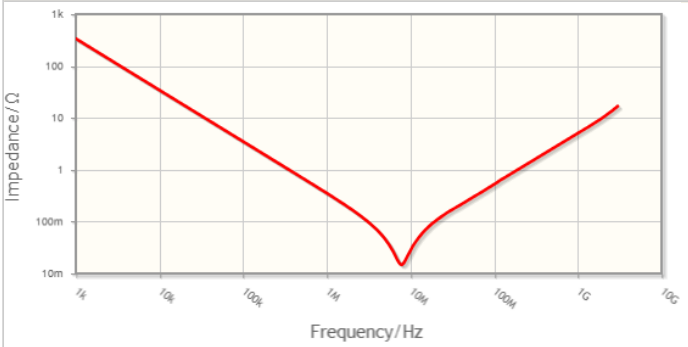
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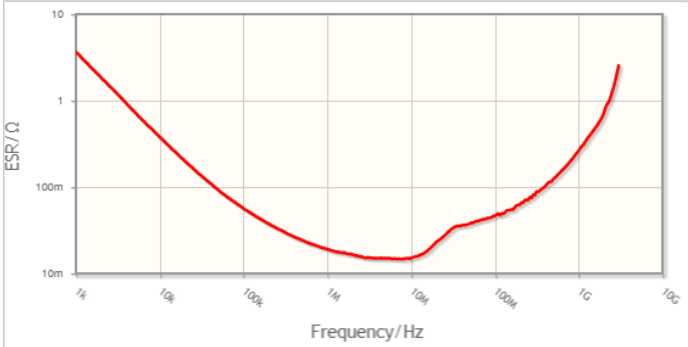
Characteristic Graphs(This is reference data, and does not guarantee the products characteristics.)

Impedance



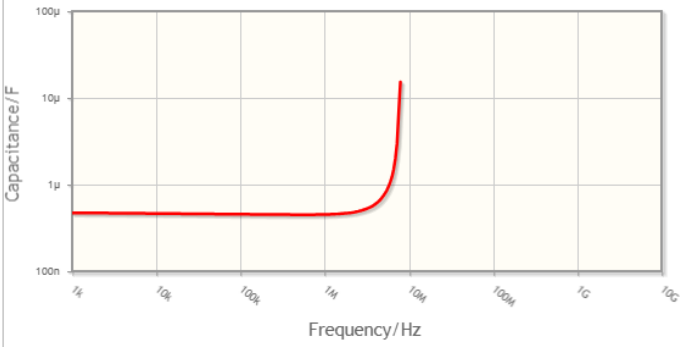
CGA4J3X7S2A474K125AE

ESR



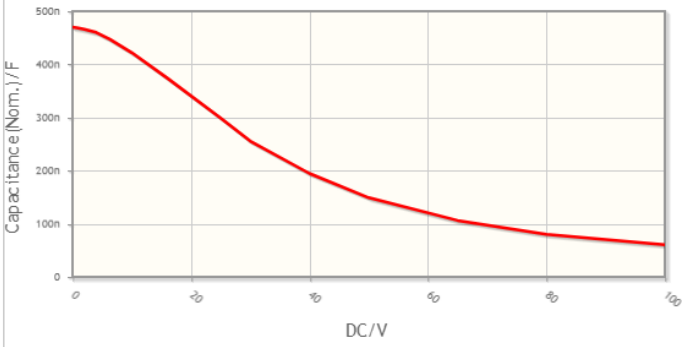
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Capacitance



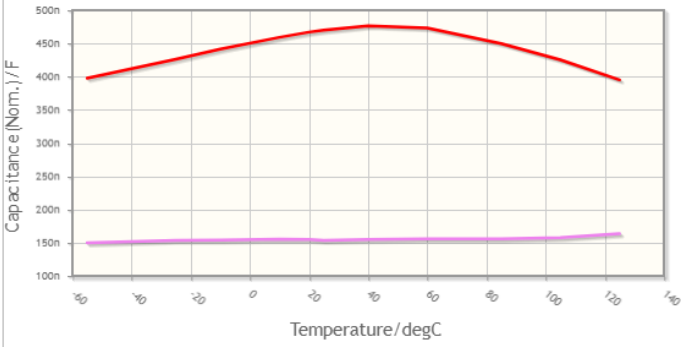
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DC Bias Characteristic



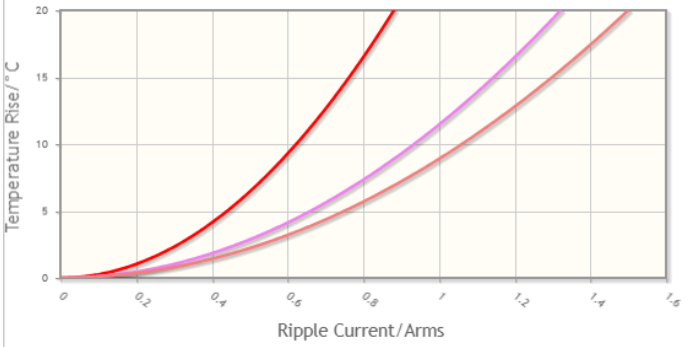
CGA4J3X7S2A474K125AE

Temperature Characteristic



CGA4J3X7S2A474K125AE(No Bias) CGA4J3X7S2A474K125AE(DC Bias = 50V)

Ripple Temperature Rising



CGA4J3X7S2A474K125AE(100kHz) CGA4J3X7S2A474K125AE(500kHz) CGA4J3X7S2A474K125AE(1MHz)

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Associated Images

Land Pattern (Terminal Connection)



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