

TBC12-11EGWA

30 mm (1.2 inch) 5x7 Dot Matrix Display

DESCRIPTIONS

- The High Efficiency Red source color devices are made with Gallium Arsenide Phosphide on Gallium Phosphide Orange Light Emitting Diode
- The Green source color devices are made with Gallium Phosphide Green Light Emitting Diode

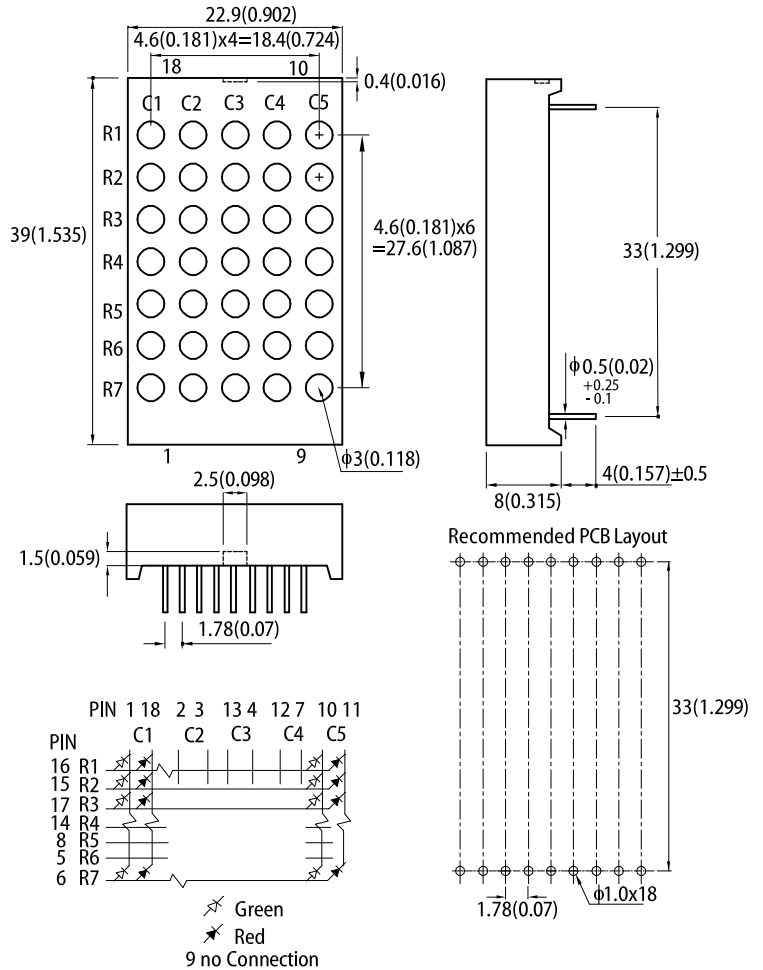
FEATURES

- 1.2 inch matrix height
- Dot size 3mm
- Low current operation
- High contrast and light output
- Easy mounting on P.C. boards or sockets
- Mechanically rugged
- Standard: gray face, white dot
- RoHS compliant

APPLICATIONS

- Home and smart appliances
- Display time and digital combination
- Industrial and instrumental applications
- Numeric status

PACKAGE DIMENSIONS



Notes:

1. All dimensions are in millimeters (inches). Tolerance is $\pm 0.25(0.01)$ unless otherwise noted.
2. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.

SELECTION GUIDE

Part Number	Emitting Color (Material)	Lens Type	Iv (ucd) @ 10mA ^[1]		Description
			Min.	Typ.	
TBC12-11EGWA	High Efficiency Red (GaAsP/GaP)	White Diffused	5600	14000	Column Cathode
			*2200	*4000	
	Green (GaP)		9000	16000	
			*3600	*6000	

Notes:

1. Luminous intensity / luminous Flux: $\pm 15\%$.
- * Luminous intensity value is traceable to CIE127-2007 standards.

ELECTRICAL / OPTICAL CHARACTERISTICS at T_A=25°C

Parameter	Symbol	Emitting Color	Value		Unit
			Typ.	Max.	
Wavelength at Peak Emission I _F = 10mA	λ_{peak}	High Efficiency Red Green	627 565	-	nm
Dominant Wavelength I _F = 10mA	λ_{dom} ^[1]	High Efficiency Red Green	617 568	-	nm
Spectral Bandwidth at 50% Φ REL MAX I _F = 10mA	$\Delta\lambda$	High Efficiency Red Green	45 30	-	nm
Capacitance	C	High Efficiency Red Green	15 15	-	pF
Forward Voltage I _F = 10mA	V _F ^[2]	High Efficiency Red Green	1.9 2.0	2.3 2.4	V
Reverse Current (V _R = 5V)	I _R	High Efficiency Red Green	-	10 10	μA

Notes:

1. The dominant wavelength (λ_d) above is the setup value of the sorting machine. (Tolerance λ_d : $\pm 1\text{nm}$.)
2. Forward voltage: $\pm 0.1\text{V}$.
3. Wavelength value is traceable to CIE127-2007 standards.
4. Excess driving current and / or operating temperature higher than recommended conditions may result in severe light degradation or premature failure.

ABSOLUTE MAXIMUM RATINGS at T_A=25°C

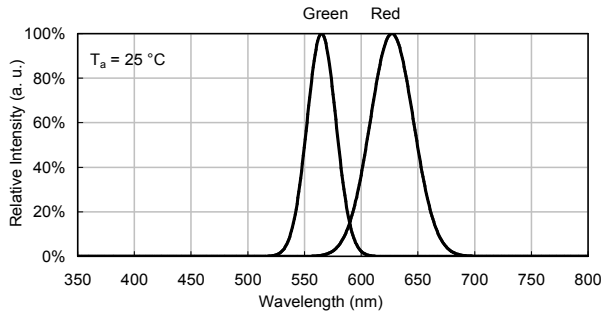
Parameter	Symbol	Value		Unit
		High Efficiency Red	Green	
Power Dissipation	P _D	75	62.5	mW
Reverse Voltage	V _R	5	5	V
Junction Temperature	T _j	125	110	°C
Operating Temperature	T _{op}	-40 to +85		°C
Storage Temperature	T _{stg}	-40 to +85		°C
DC Forward Current	I _F	30	25	mA
Peak Forward Current	I _{FM} ^[1]	160	140	mA
Electrostatic Discharge Threshold (HBM)	-	8000	8000	V
Lead Solder Temperature ^[2]		260°C For 3-5 Seconds		

Notes:

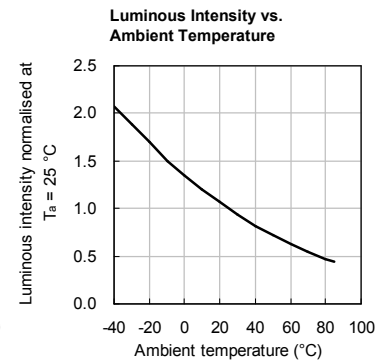
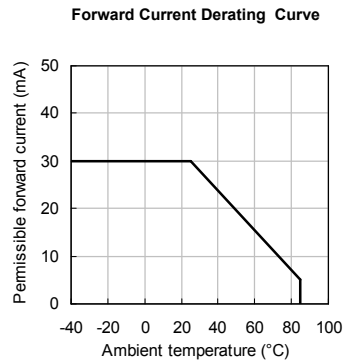
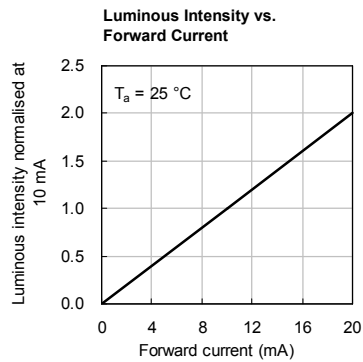
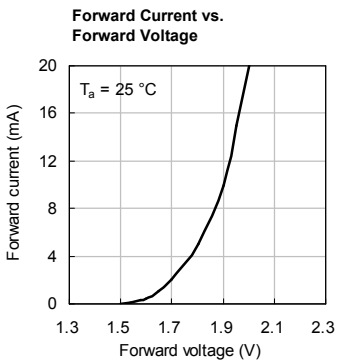
1. 1/10 Duty Cycle, 0.1ms Pulse Width.
2. 2mm below package base.
3. Relative humidity levels maintained between 40% and 60% in production area are recommended to avoid the build-up of static electricity – Ref JEDEC/JESD625-A and JEDEC/J-STD-033.

TECHNICAL DATA

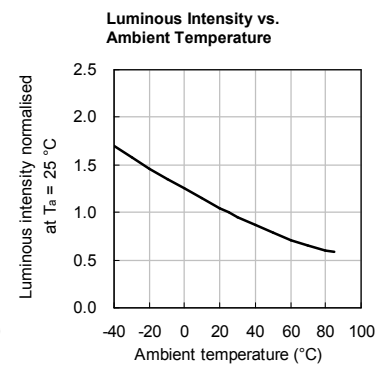
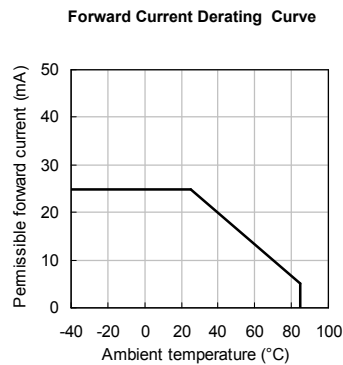
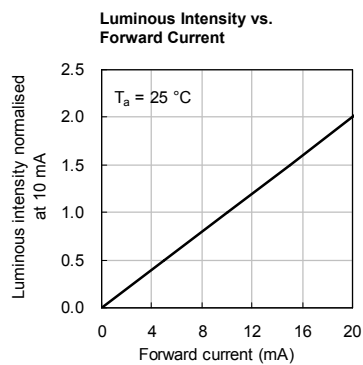
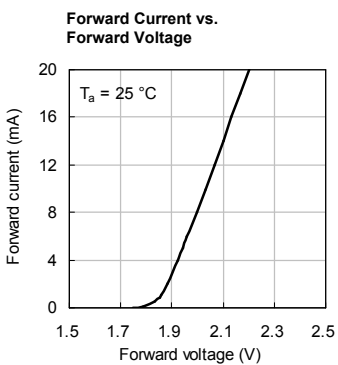
RELATIVE INTENSITY vs. WAVELENGTH



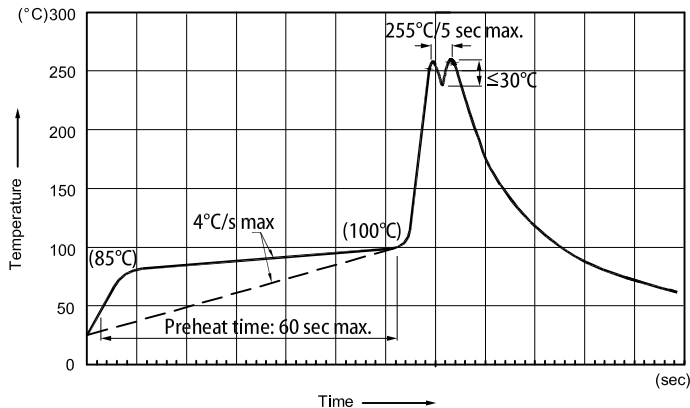
HIGH EFFICIENCY RED



GREEN

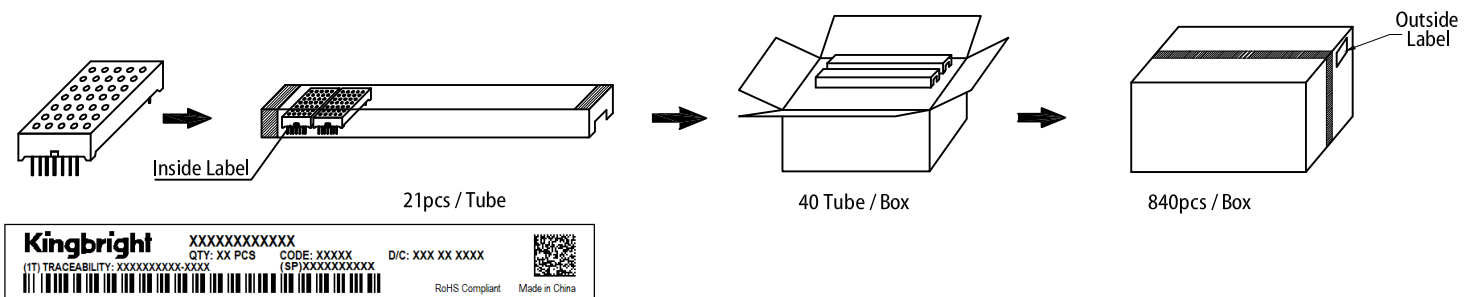


RECOMMENDED WAVE SOLDERING PROFILE



- Notes:
1. Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260°C.
 2. Peak wave soldering temperature between 245°C ~ 255°C for 3 sec (5 sec max).
 3. Do not apply stress to the epoxy resin while the temperature is above 85°C.
 4. Fixtures should not incur stress on the component when mounting and during soldering process.
 5. SAC 305 solder alloy is recommended.
 6. No more than one wave soldering pass.
 7. During wave soldering, the PCB top-surface temperature should be kept below 105°C.

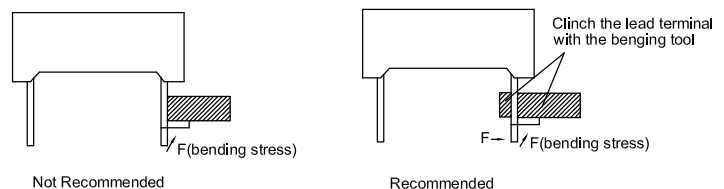
PACKING & LABEL SPECIFICATIONS



THROUGH HOLE DISPLAY MOUNTING METHOD

Lead Forming

Do not bend the component leads by hand without proper tools. The leads should be bent by clinching the upper part of the lead firmly such that the bending force is not exerted on the plastic body.



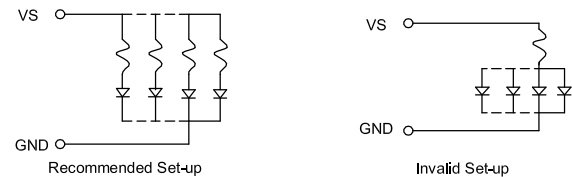
Installation

1. The installation process should not apply stress to the lead terminals.
2. When inserting for assembly, ensure the terminal pitch matches the substrate board's hole pitch to prevent spreading or pinching the lead terminals. (Fig.1)
3. The component shall be placed at least 5mm from edge of PCB to avoid damage caused excessive heat during wave soldering. (Fig.2)



CIRCUIT DESIGN NOTES

1. Protective current-limiting resistors may be necessary to operate the LEDs within the specified range.
2. LEDs mounted in parallel should each be placed in series with its own current-limiting resistor. (Fig.3)
3. The driving circuit should be designed to protect the LED against reverse voltages and transient voltage spikes when the circuit is powered up or shut down.
4. The safe operating current should be chosen after considering the maximum ambient temperature of the operating environment.
5. Prolonged reverse bias should be avoided, as it could cause metal migration, leading to an increase in leakage current or causing a short circuit.



(Fig.3)

PRECAUTIONARY NOTES

1. The information included in this document reflects representative usage scenarios and is intended for technical reference only.
2. The part number, type, and specifications mentioned in this document are subject to future change and improvement without notice. Before production usage customer should refer to the latest datasheet for the updated specifications.
3. When using the products referenced in this document, please make sure the product is being operated within the environmental and electrical limits specified in the datasheet. If customer usage exceeds the specified limits, Kingbright will not be responsible for any subsequent issues.
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6. When any special process such as potting is required for LED assembly, please consult with Kingbright representative before proceeding.
7. All design applications should refer to Kingbright application notes available at <https://www.KingbrightUSA.com/ApplicationNotes>