

NX3020NAKS

30 V, 180 mA dual N-channel Trench MOSFET

25 September 2026 Product data sheet

1. General description

Dual N-channel enhancement mode Field-Effect Transistor (FET) in a very small SOT363-3 (SC-88) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Very fast switching
- Trench MOSFET technology
- ESD protection
- Low threshold voltage

3. Applications

- Relay driver
- High-speed line driver
- Low-side load switch
- Switching circuits

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Per transistor							
V_{DS}	drain-source voltage	T_j = 25 °C		-	-	30	V
V_{GS}	gate-source voltage			-20	-	20	V
I_D	drain current	V_{GS} = 4.5 V; T_{amb} = 25 °C	[1]	-	-	180	mA
Static characteristics (per transistor)							
R_{DSon}	drain-source on-state resistance	V_{GS} = 10 V; I_D = 100 mA; T_j = 25 °C		-	2.7	4.5	Ω

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm^2.

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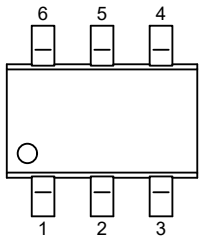
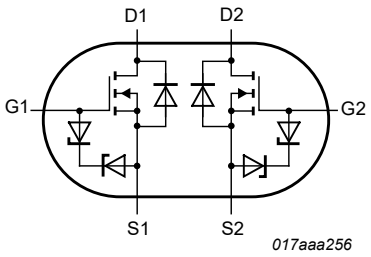
5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	S1	source TR1	 Transparent top view **TSSOP6 (SOT363-3)**	
2	G1	gate TR1		
3	D2	drain TR2		
4	S2	source TR2		
5	G2	gate TR2		
6	D1	drain TR1		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
NX3020NAKS	TSSOP6	Plastic surface-mounted package; 6 leads	SOT363-3

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
NX3020NAKS	Ua%

[1] % = placeholder for manufacturing site code

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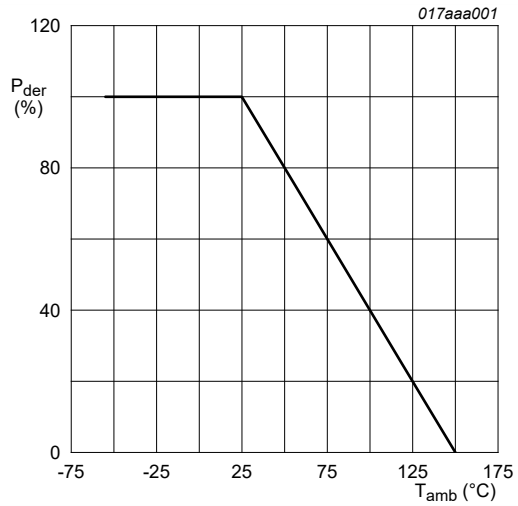
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

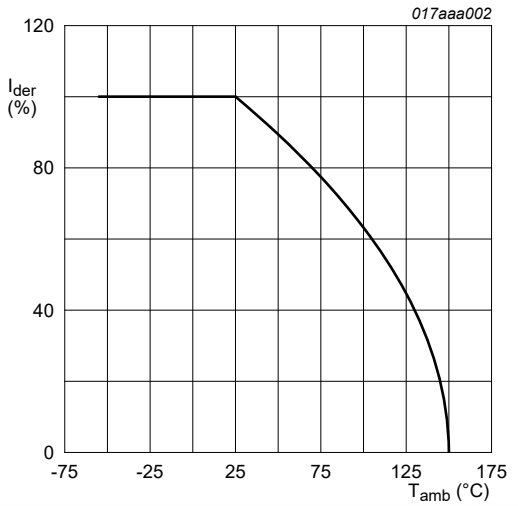
Symbol	Parameter	Conditions		Min	Max	Unit
Per transistor						
V_{DS}	drain-source voltage	T_j = 25 °C		-	30	V
V_{GS}	gate-source voltage			-20	20	V
I_D	drain current	V_{GS} = 4.5 V; T_{amb} = 25 °C	[1]	-	180	mA
		V_{GS} = 4.5 V; T_{amb} = 100 °C	[1]	-	110	mA
I_{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \leq$ 10 µs		-	720	mA
P_{tot}	total power dissipation	T_{amb} = 25 °C	[2]	-	260	mW
			[1]	-	280	mW
		T_{sp} = 25 °C		-	1100	mW
Source-drain diode						
I_S	source current	T_{amb} = 25 °C		-	180	mA
Per device						
T_j	junction temperature			-55	150	°C
T_{amb}	ambient temperature			-55	150	°C
T_{stg}	storage temperature			-65	150	°C
P_{tot}	total power dissipation	T_{amb} = 25 °C	[2]	-	375	mW

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm^2.
[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



$$P_{der} = \frac{P_{tot}}{P_{tot(25°C)}} \times 100\ \%$$

Fig. 1. Normalized total power dissipation as a function of ambient temperature



$$I_{der} = \frac{I_D}{I_{D(25°C)}} \times 100\ \%$$

Fig. 2. Normalized continuous drain current as a function of ambient temperature

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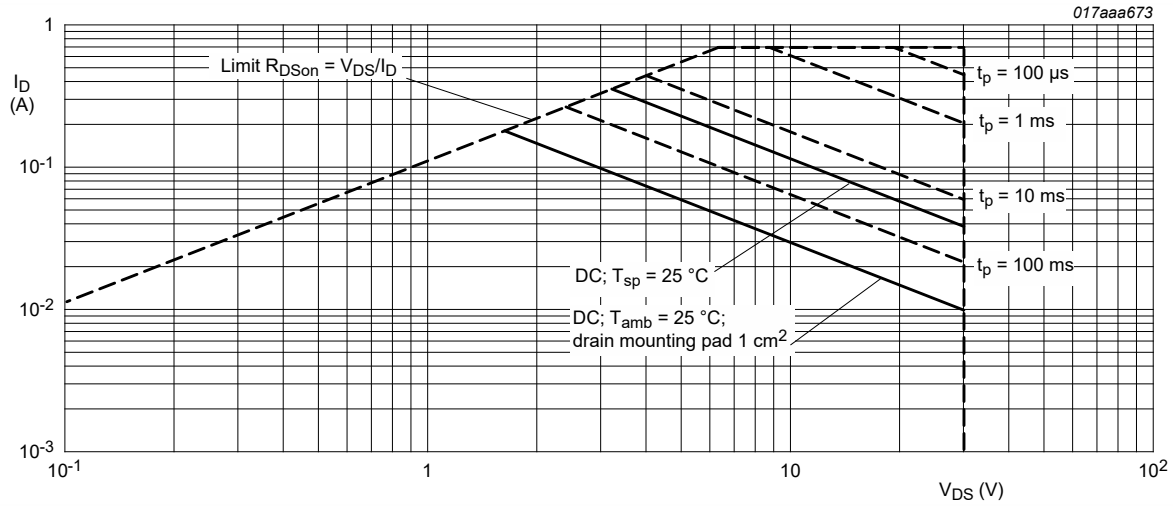

I_{DM} = single pulse

Fig. 3. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage

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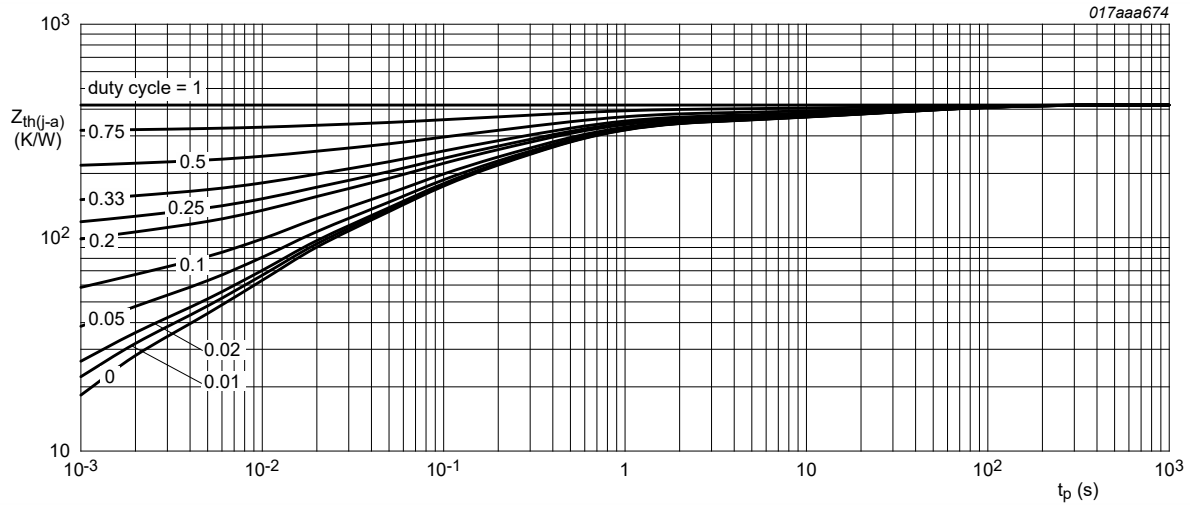
9. Thermal characteristics

Table 6. Thermal characteristics

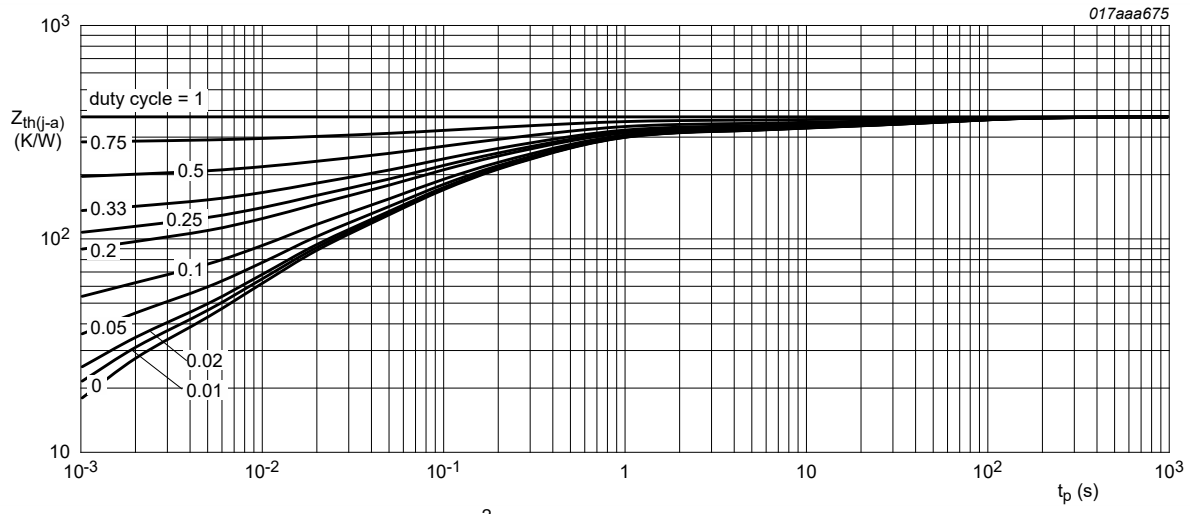
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Per transistor							
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	390	480	K/W
			[2]	-	380	430	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	110	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 1 cm^2.



FR4 PCB, standard footprint

Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for drain 1 cm^2

Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Static characteristics (per transistor)							
$V_{(BR)DSS}$	drain-source breakdown voltage	I_D = 250 µA; V_{GS} = 0 V; T_j = 25 °C		30	-	-	V
V_{GSth}	gate-source threshold voltage	I_D = 250 µA; V_{DS} = V_{GS}; T_j = 25 °C		0.8	1.2	1.5	V
I_{DSS}	drain leakage current	V_{DS} = 30 V; V_{GS} = 0 V; T_j = 25 °C		-	-	1	µA
		V_{DS} = 30 V; V_{GS} = 0 V; T_j = 150 °C		-	-	10	µA
I_{GSS}	gate leakage current	V_{GS} = 20 V; V_{DS} = 0 V; T_j = 25 °C		-	-	3.5	µA
		V_{GS} = -20 V; V_{DS} = 0 V; T_j = 25 °C		-	-	-3.5	µA
		V_{GS} = 10 V; V_{DS} = 0 V; T_j = 25 °C		-	-	1	µA
		V_{GS} = -10 V; V_{DS} = 0 V; T_j = 25 °C		-	-	-1	µA
		V_{GS} = 4.5 V; V_{DS} = 0 V; T_j = 25 °C		-	-	0.5	µA
		V_{GS} = -4.5 V; V_{DS} = 0 V; T_j = 25 °C		-	-	-0.5	µA
R_{DSon}	drain-source on-state resistance	V_{GS} = 10 V; I_D = 100 mA; T_j = 25 °C		-	2.7	4.5	Ω
		V_{GS} = 10 V; I_D = 100 mA; T_j = 150 °C		-	5.5	9.2	Ω
		V_{GS} = 4.5 V; I_D = 100 mA; T_j = 25 °C		-	3	5.2	Ω
		V_{GS} = 2.5 V; I_D = 10 mA; T_j = 25 °C		-	4	13	Ω
g_{fs}	forward transconductance	V_{DS} = 10 V; I_D = 150 mA; T_j = 25 °C		-	320	-	mS
Dynamic characteristics (per transistor)							
$Q_{G(tot)}$	total gate charge	V_{DS} = 15 V; I_D = 150 mA; V_{GS} = 4.5 V; T_j = 25 °C		-	0.34	0.44	nC
Q_{GS}	gate-source charge			-	0.11	-	nC
Q_{GD}	gate-drain charge			-	0.06	-	nC
C_{iss}	input capacitance	V_{DS} = 10 V; f = 1 MHz; V_{GS} = 0 V; T_j = 25 °C		-	13	20	pF
C_{oss}	output capacitance			-	2.6	-	pF
C_{rss}	reverse transfer capacitance			-	1.1	-	pF
$t_{d(on)}$	turn-on delay time	V_{DS} = 20 V; R_L = 250 Ω; V_{GS} = 10 V; $R_{G(ext)}$ = 6 Ω; T_j = 25 °C		-	5	10	ns
t_r	rise time			-	5	-	ns
$t_{d(off)}$	turn-off delay time			-	34	68	ns
t_f	fall time			-	17	-	ns
Source-drain diode (per transistor)							
V_{SD}	source-drain voltage	I_S = 115 mA; V_{GS} = 0 V; T_j = 25 °C		0.47	0.7	1.2	V

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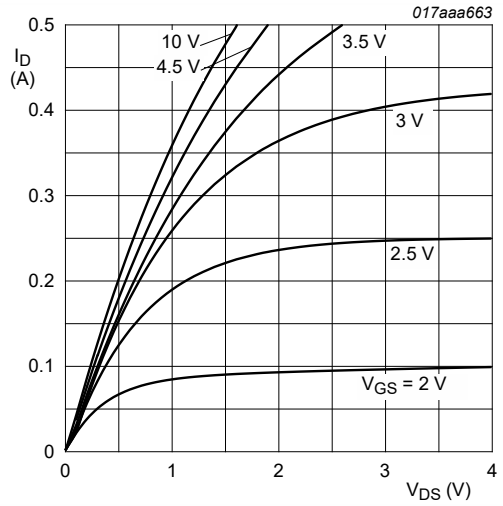
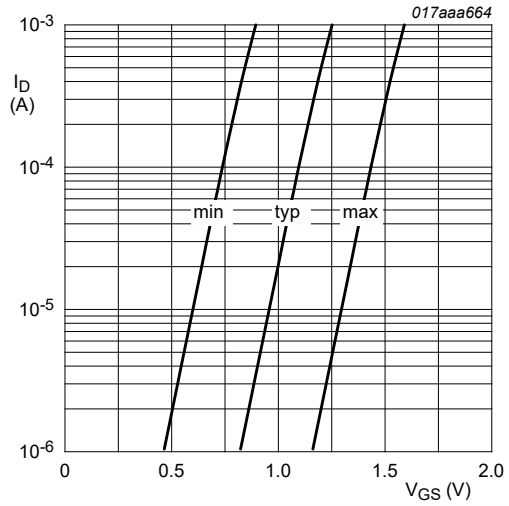


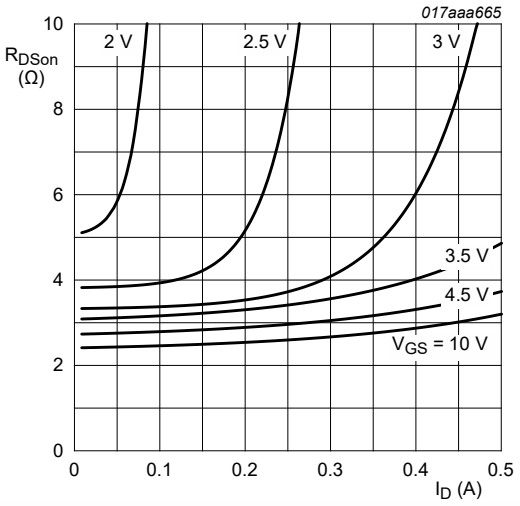
T_j = 25 °C

Fig. 6. Output characteristics: drain current as a function of drain-source voltage; typical values



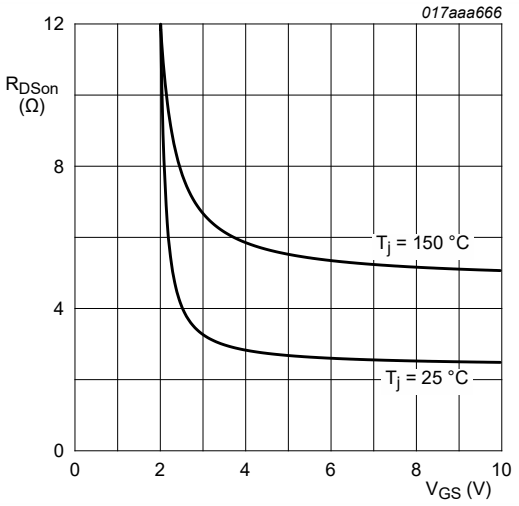
T_j = 25 °C; V_{DS} = 5 V

Fig. 7. Sub-threshold drain current as a function of gate-source voltage



T_j = 25 °C

Fig. 8. Drain-source on-state resistance as a function of drain current; typical values



I_D = 0.15 A

Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values

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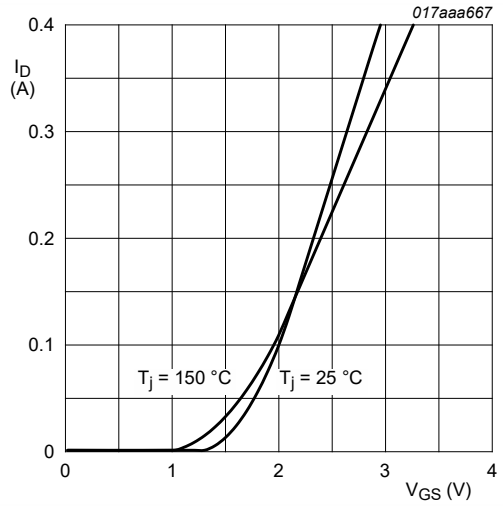
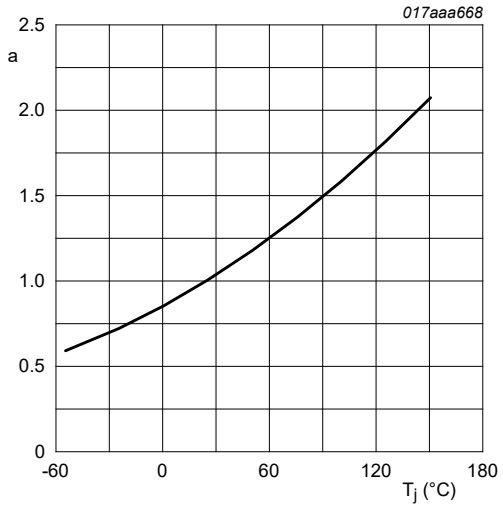


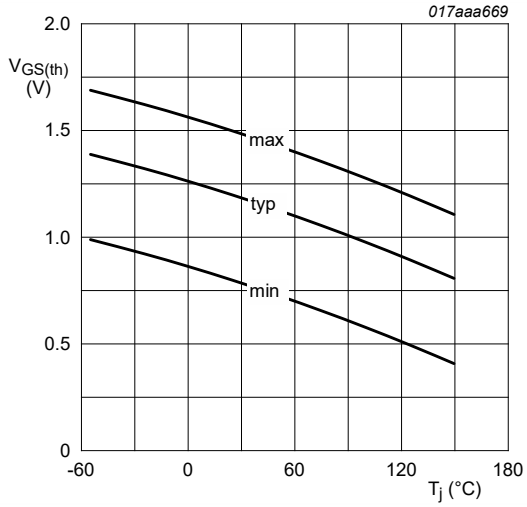
$V_{DS} > I_D \times R_{DSon}$

Fig. 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values



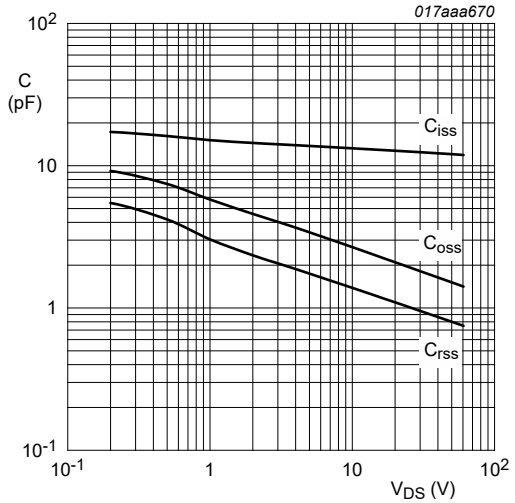
$$a = \frac{R_{DSon}}{R_{DSon(25°C)}}$$

Fig. 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values



I_D = 0.25 mA; $V_{DS} = V_{GS}$

Fig. 12. Gate-source threshold voltage as a function of junction temperature



f = 1 MHz; V_{GS} = 0 V

Fig. 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

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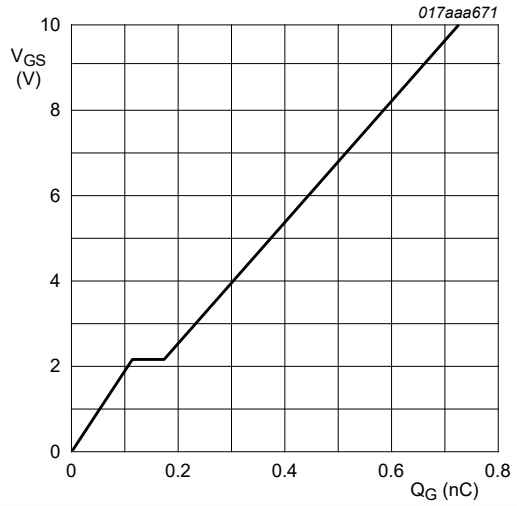


I_D = 0.15 A; V_{DS} = 15 V; T_{amb} = 25 °C

Fig. 14. Gate-source voltage as a function of gate charge; typical values

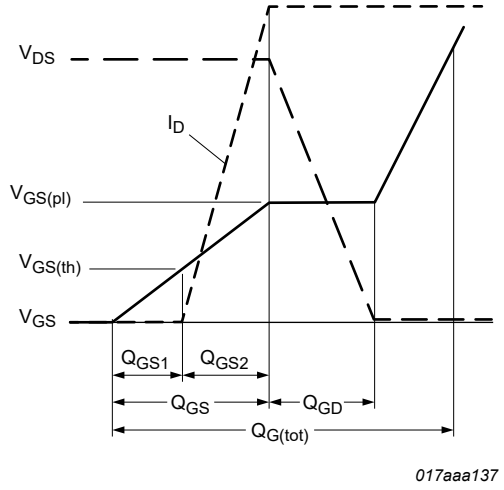
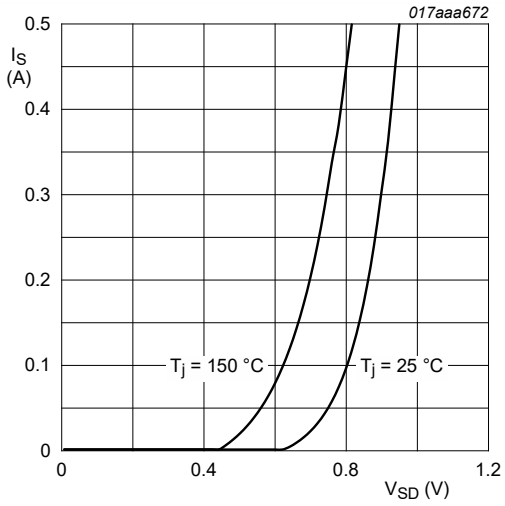


Fig. 15. Gate charge waveform definitions



V_{GS} = 0 V

Fig. 16. Source current as a function of source-drain voltage; typical values

11. Test information

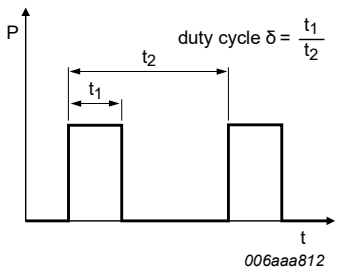


Fig. 17. Duty cycle definition

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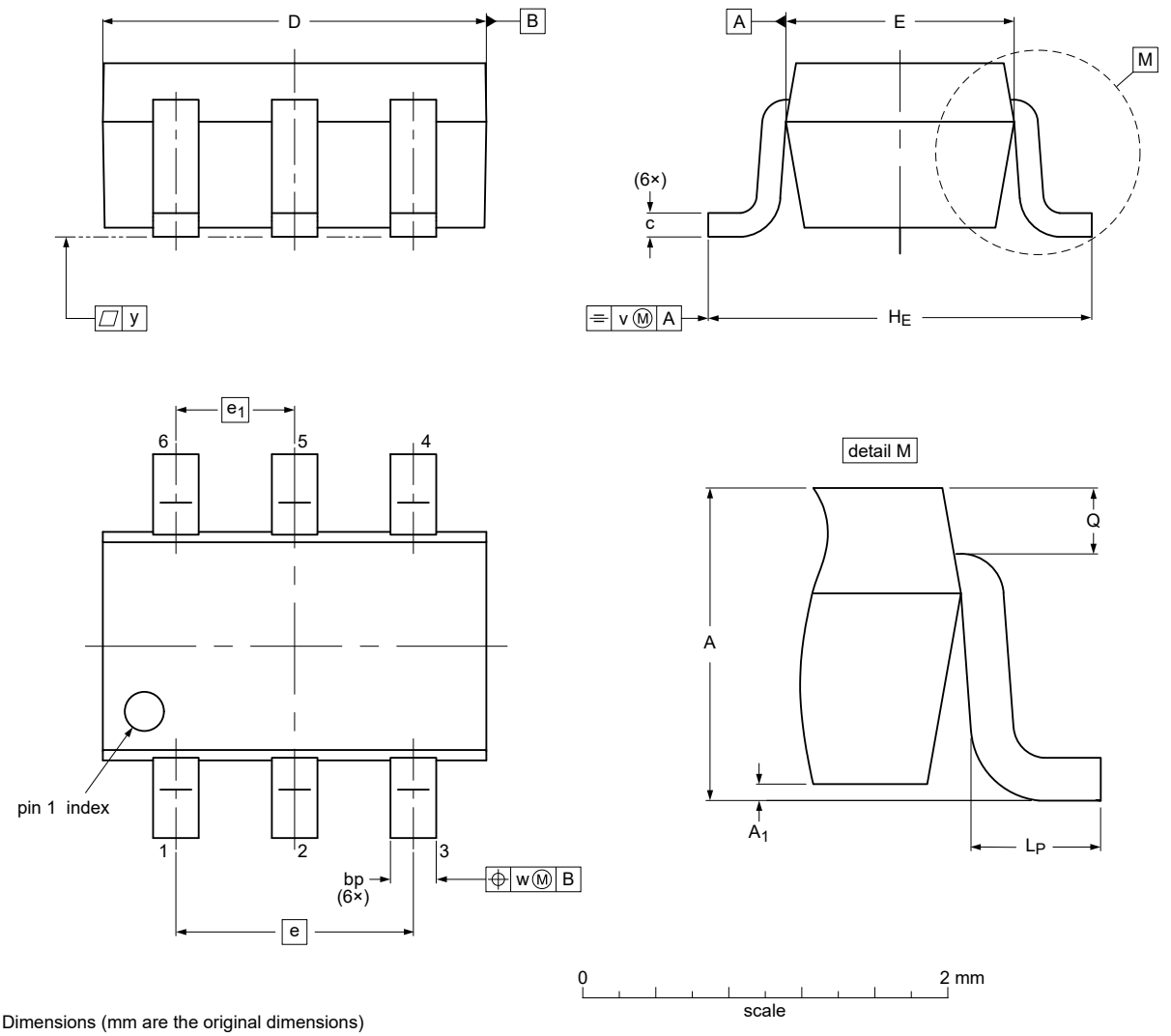
12. Package outline

Plastic surface-mounted package; 6 leads **SOT363-3**



Dimensions (mm are the original dimensions)

Unit[1]		A	A_1	bp	c	D	E	e	e_1	H_E	L_P	Q	v	w	y
mm	max	1.10	0.10	0.30	0.25	2.20	1.35	1.30 BSC	0.65 BSC	2.2	0.45	0.25	0.20	0.20	0.10
	nom			0.15	0.10	1.80	1.15			2.0	0.15	0.15			
	min	0.80													

sot363-3_po

Outline version	References				European projection	Issue date
	IEC	JEDEC	JEITA			
SOT363-3			SC-88			26-03-18

Fig. 18. Package outline TSSOP6 (SOT363-3)

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13. Soldering

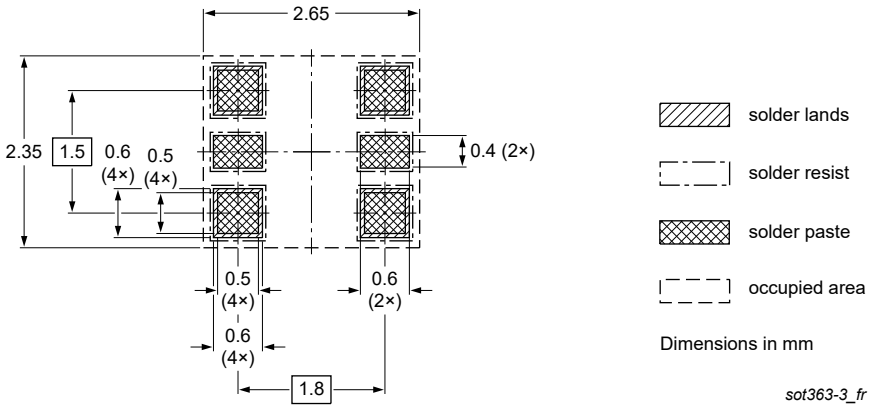

Fig. 19. Reflow soldering footprint for TSSOP6 (SOT363-3)

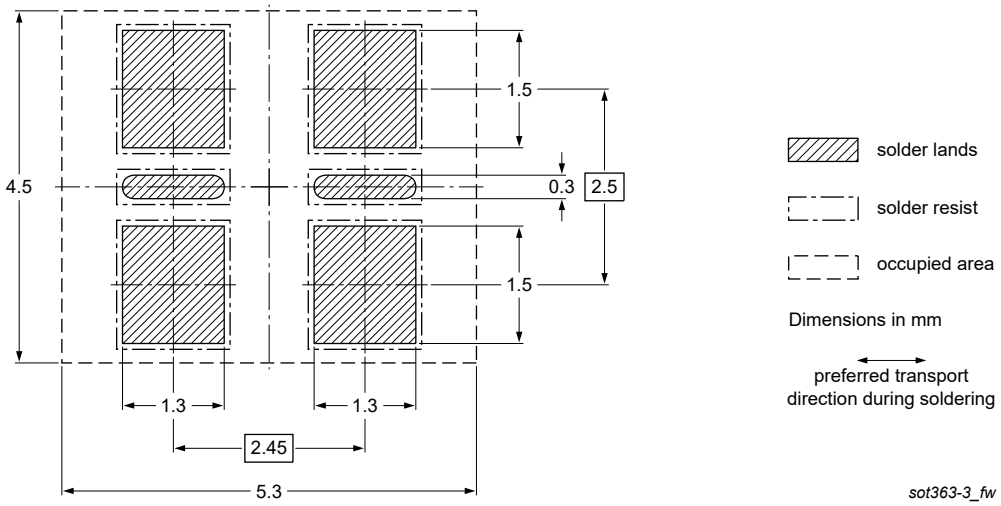

Fig. 20. Wave soldering footprint for TSSOP6 (SOT363-3)

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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
NX3020NAKS v.4	20260925	Product data sheet	-	NX3020NAKS v.3
Modifications:	• Package outline changed to SOT363-3			
NX3020NAKS v.3	20131111	Product data sheet	-	NX3020NAKS v.2
NX3020NAKS v.2	20131029	Product data sheet	-	NX3020NAKS v.1
NX3020NAKS v.1	20120706	Product data sheet	-	-

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term 'short data sheet' is explained in section "Definitions".
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For more information, please visit: http://www.nexperia.com
For sales office addresses, please send an email to: salesaddresses@nexperia.com
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