

NE85630 / 2SC4226 JEITA Part No.

Data Sheet

NPN Silicon RF Transistor

R09DS0022EJ0200

Rev.2.00

NPN Epitaxial Silicon RF Transistor for High-Frequency Low-Noise Amplification 3-pin super Minimold

Jun 29, 2011

DESCRIPTION

The NE85630 / 2SC4226 is a low supply voltage transistor designed for VHF, UHF low noise amplifier. It is suitable for a high density surface mount assembly since the transistor has been applied 3-pin super minimold package.

FEATURES

- Low noise : $NF = 1.2 \text{ dB TYP. @ } V_{CE} = 3 \text{ V, } I_C = 7 \text{ mA, } f = 1 \text{ GHz}$
- High gain : $|S_{21e}|^2 = 9 \text{ dB TYP. @ } V_{CE} = 3 \text{ V, } I_C = 7 \text{ mA, } f = 1 \text{ GHz}$
- 3-pin super minimold package

<R> ORDERING INFORMATION

Part Number	Order Number	Package	Quantity	Supplying Form
NE85630 2SC4226	NE85630-A 2SC4226-A	3-pin super	50 pcs (Non reel)	• 8 mm wide embossed taping • Pin 3 (Collector) face the perforation side of the tape
NE85630-T1 2SC4226-T1	NE85630-T1-A 2SC4226-T1-A	Minimold (Pb-Free)	3 kpcs/reel	

Remark To order evaluation samples, please contact your nearby sales office.
The unit sample quantity is 50 pcs.

ABSOLUTE MAXIMUM RATINGS (T_A = +25°C)

Parameter	Symbol	Ratings	Unit
Collector to Base Voltage	V_{CBO}	20	V
Collector to Emitter Voltage	V_{CEO}	12	V
Emitter to Base Voltage	V_{EBO}	3	V
Collector Current	I_C	100	mA
Total Power Dissipation	P_{tot} ^{Note}	150	mW
Junction Temperature	T_j	150	°C
Storage Temperature	T_{stg}	-65 to +150	°C

Note Free air

CAUTION

Observe precautions when handling because these devices are sensitive to electrostatic discharge.

The mark <R> shows major revised points.

The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

ELECTRICAL CHARACTERISTICS (T_A = +25°C)

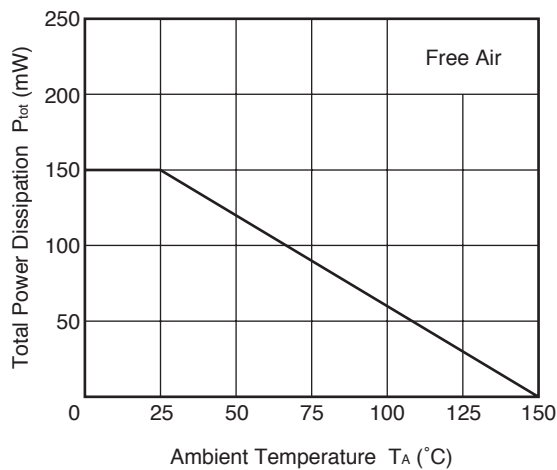
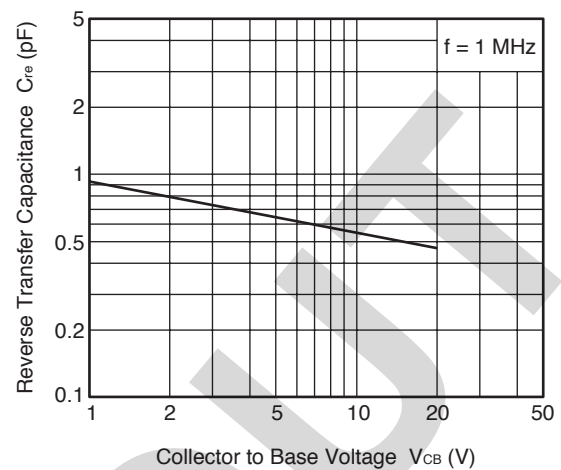
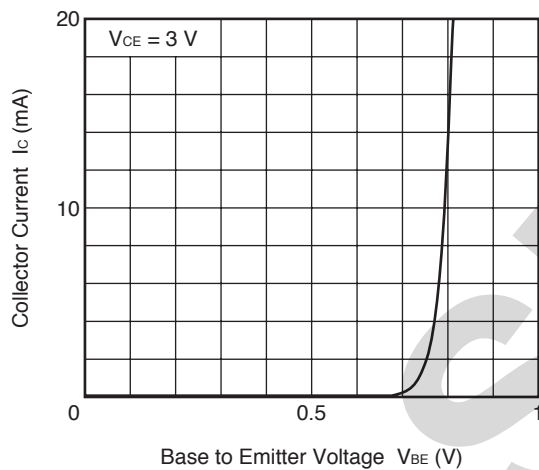
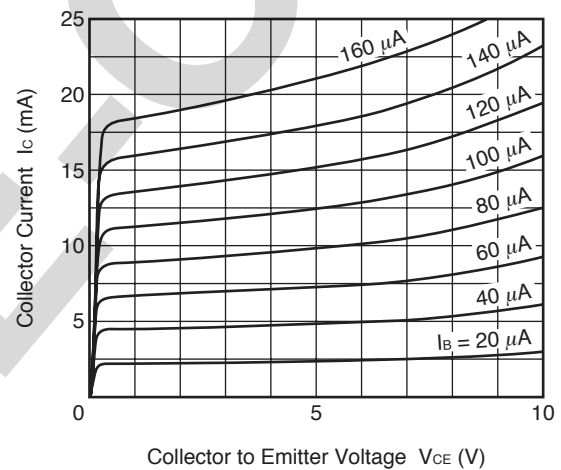
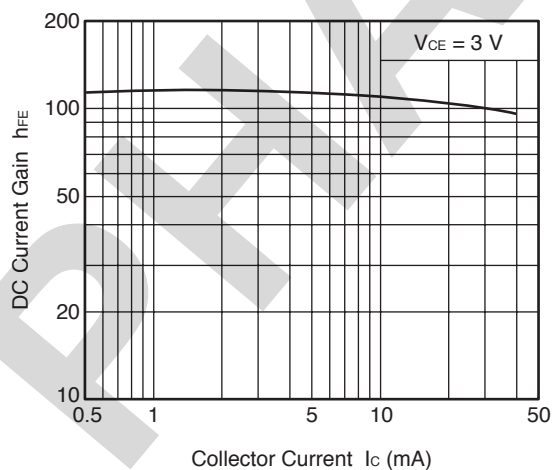
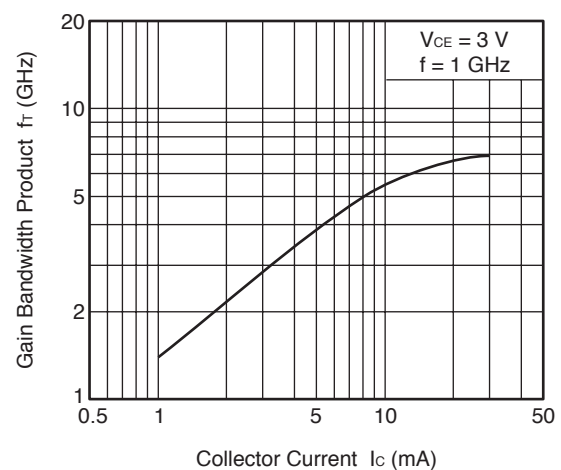
Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
DC Characteristics						
Collector Cut-off Current	I _{CBO}	V _{CB} = 10 V, I _E = 0	–	–	1.0	μA
Emitter Cut-off Current	I _{EBO}	V _{EB} = 1 V, I _C = 0	–	–	1.0	μA
DC Current Gain	h _{FE} ^{Note 1}	V _{CE} = 3 V, I _C = 7 mA	40	110	250	–
RF Characteristics						
Gain Bandwidth Product	f _T	V _{CE} = 3 V, I _C = 7 mA	3.0	4.5	–	GHz
Insertion Power Gain	S _{21e} ²	V _{CE} = 3 V, I _C = 7 mA, f = 1 GHz	7	9	–	dB
Noise Figure	NF	V _{CE} = 3 V, I _C = 7 mA, f = 1 GHz	–	1.2	2.5	dB
Reverse Transfer Capacitance	C _{re} ^{Note 2}	V _{CB} = 3 V, I _E = 0, f = 1 MHz	–	0.7	1.5	pF

Notes 1. Pulse measurement: PW ≤ 350 μs, Duty Cycle ≤ 2%

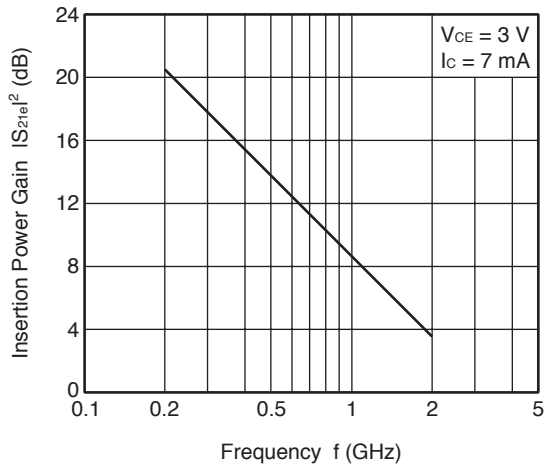
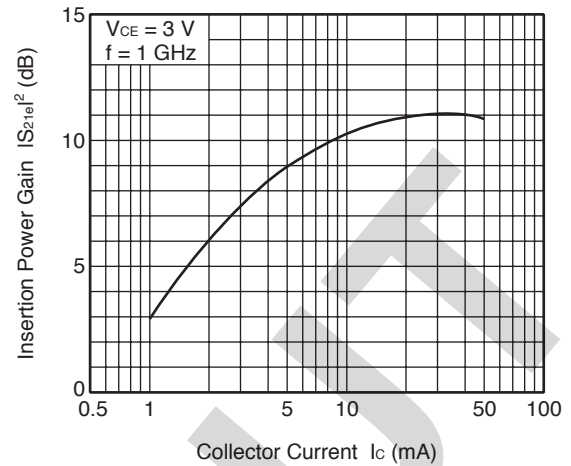
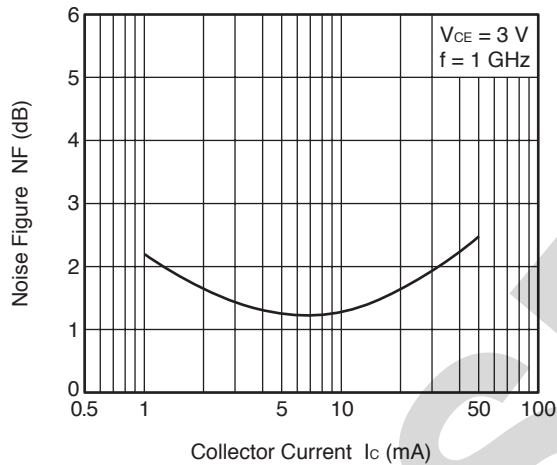
2. Collector to base capacitance when the emitter grounded

<R> h_{FE} CLASSIFICATION

Rank	R23/Y23	R24/Y24	R25/Y25
Marking	R23	R24	R25
h _{FE} Value	40 to 80	70 to 140	125 to 250

TYPICAL CHARACTERISTICS ($T_A = +25^\circ\text{C}$, unless otherwise specified)TOTAL POWER DISSIPATION
vs. AMBIENT TEMPERATUREREVERSE TRANSFER CAPACITANCE
vs. COLLECTOR TO BASE VOLTAGECOLLECTOR CURRENT vs.
BASE TO EMITTER VOLTAGECOLLECTOR CURRENT vs.
COLLECTOR TO EMITTER VOLTAGEDC CURRENT GAIN vs.
COLLECTOR CURRENTGAIN BANDWIDTH PRODUCT
vs. COLLECTOR CURRENT

Remark The graphs indicate nominal characteristics.

**INSERTION POWER GAIN
vs. FREQUENCY**

**INSERTION POWER GAIN
vs. COLLECTOR CURRENT**

**NOISE FIGURE vs.
COLLECTOR CURRENT**


Remark The graphs indicate nominal characteristics.

S-PARAMETERS

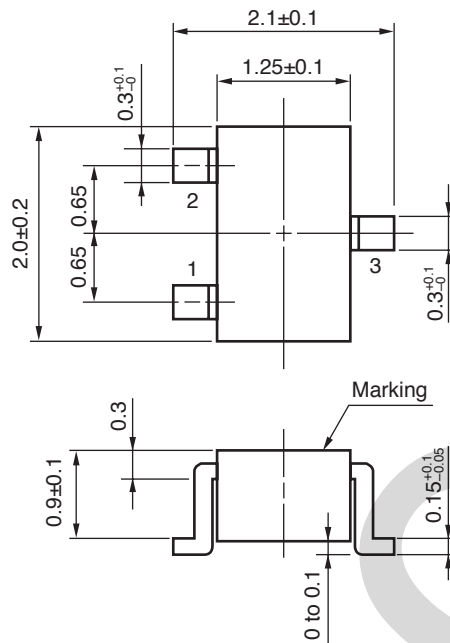
S-parameters and noise parameters are provided on our Web site in a format (S2P) that enables the direct import of the parameters to microwave circuit simulators without the need for keyboard inputs.

Click here to download S-parameters.

[RF and Microwave] → [Device Parameters]

URL <http://www2.renesas.com/microwave/en/download.html>

PHASE OUT

PACKAGE DIMENSIONS**3-PIN SUPER MINIMOLD (UNIT: mm)****PIN CONNECTIONS**

- 1. Emitter
 - 2. Base
 - 3. Collector
- (EIAJ : SC-70)

Revision History	NE85630 / 2SC4226 Data Sheet
-------------------------	-------------------------------------

Rev.	Date	Description	
		Page	Summary
–	Dec 2003	–	Previous No. :PU10450EJ01V0DS
2.00	Jun 29, 2011	p.1	Modification of ORDERING INFORMATION
		p.2	Modification of h_{FE} CLASSIFICATION

All trademarks and registered trademarks are the property of their respective owners.

NOTICE

1. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation of these circuits, software, and information in the design of your equipment. California Eastern Laboratories and Renesas Electronics assumes no responsibility for any losses incurred by you or third parties arising from the use of these circuits, software, or information.
2. California Eastern Laboratories has used reasonable care in preparing the information included in this document, but California Eastern Laboratories does not warrant that such information is error free. California Eastern Laboratories and Renesas Electronics assumes no liability whatsoever for any damages incurred by you resulting from errors in or omissions from the information included herein.
3. California Eastern Laboratories and Renesas Electronics do not assume any liability for infringement of patents, copyrights, or other intellectual property rights of third parties by or arising from the use of Renesas Electronics products or technical information described in this document. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of California Eastern Laboratories or Renesas Electronics or others.
4. You should not alter, modify, copy, or otherwise misappropriate any Renesas Electronics product, whether in whole or in part. California Eastern Laboratories and Renesas Electronics assume no responsibility for any losses incurred by you or third parties arising from such alteration, modification, copy or otherwise misappropriation of Renesas Electronics product.
5. Renesas Electronics products are classified according to the following two quality grades: "Standard" and "High Quality". The recommended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below. "Standard": Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; and industrial robots etc. "High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control systems; anti-disaster systems; anti-crime systems; and safety equipment etc. Renesas Electronics products are neither intended nor authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems, surgical implantations etc.), or may cause serious property damages (nuclear reactor control systems, military equipment etc.). You must check the quality grade of each Renesas Electronics product before using it in a particular application. You may not use any Renesas Electronics product for any application for which it is not intended. California Eastern Laboratories and Renesas Electronics shall not be in any way liable for any damages or losses incurred by you or third parties arising from the use of any Renesas Electronics product for which the product is not intended by California Eastern Laboratories or Renesas Electronics.
6. You should use the Renesas Electronics products described in this document within the range specified by California Eastern Laboratories, especially with respect to the maximum rating, operating supply voltage range, movement power voltage range, heat radiation characteristics, installation and other product characteristics. California Eastern Laboratories shall have no liability for malfunctions or damages arising out of the use of Renesas Electronics products beyond such specified ranges.
7. Although Renesas Electronics endeavors to improve the quality and reliability of its products, semiconductor products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Further, Renesas Electronics products are not subject to radiation resistance design. Please be sure to implement safety measures to guard them against the possibility of physical injury, and injury or damage caused by fire in the event of the failure of a Renesas Electronics product, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult, please evaluate the safety of the final products or systems manufactured by you.
8. Please contact a California Eastern Laboratories sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. Please use Renesas Electronics products in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. California Eastern Laboratories and Renesas Electronics assume no liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
9. Renesas Electronics products and technology may not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You should not use Renesas Electronics products or technology described in this document for any purpose relating to military applications or use by the military, including but not limited to the development of weapons of mass destruction. When exporting the Renesas Electronics products or technology described in this document, you should comply with the applicable export control laws and regulations and follow the procedures required by such laws and regulations.
10. It is the responsibility of the buyer or distributor of California Eastern Laboratories, who distributes, disposes of, or otherwise places the Renesas Electronics product with a third party, to notify such third party in advance of the contents and conditions set forth in this document, California Eastern Laboratories and Renesas Electronics assume no responsibility for any losses incurred by you or third parties as a result of unauthorized use of Renesas Electronics products.
11. This document may not be reproduced or duplicated in any form, in whole or in part, without prior written consent of California Eastern Laboratories.
12. Please contact a California Eastern Laboratories sales office if you have any questions regarding the information contained in this document or Renesas Electronics products, or if you have any other inquiries.

NOTE 1: "Renesas Electronics" as used in this document means Renesas Electronics Corporation and also includes its majority-owned subsidiaries.

NOTE 2: "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics.

NOTE 3: Products and product information are subject to change without notice.

CEL Headquarters • 4590 Patrick Henry Drive, Santa Clara, CA 95054 • Phone (408) 919-2500 • www.cel.com

For a complete list of sales offices, representatives and distributors,
Please visit our website: www.cel.com/contactus