

1.8V, PCIe®, 2 Differential Channel, 2:1 Mux/DeMux Switch w/ Single Enable

Features

- 2 Differential Channel, 2:1 Mux/DeMux
- PCI Express® signal compliance
- Low Bit-to-Bit Skew
- Low Crosstalk: -60dB@10MHz
- Low Off Isolation: -65dB@10MHz
- V_{DD} Operating Range: +1.5V to +2.0V
- ESD Tolerance >2kV HBM
- Packaging (Pb-free & Green):
 - 28 contact TQFN (ZH)

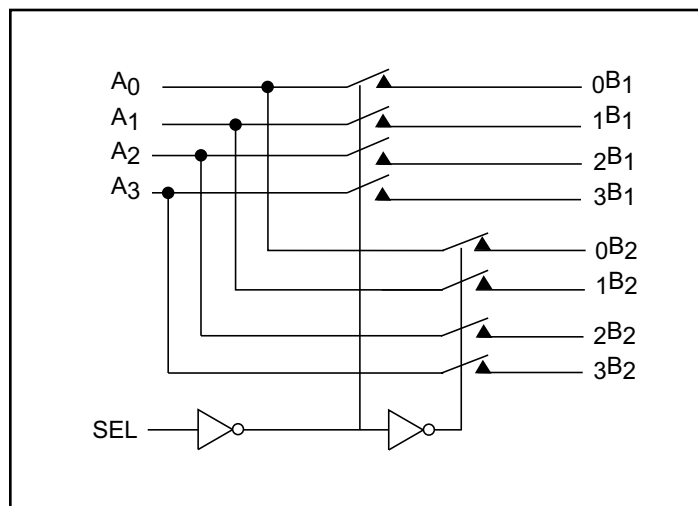
Description

Pericom Semiconductor's PI2PCIE212 is a 4 to 2 differential channel multiplexer/demultiplexer switch. The PI2PCIE family of switch solutions is fully compliant with PCIe® signals at 2.5Gbps. PI2PCIE212 can switch PCIe signals up to a maximum length of 16 inches. Due to its low bit-to-bit skew, high channel-to-channel noise isolation and high bandwidth, this product is ideal for PCI Express switching.

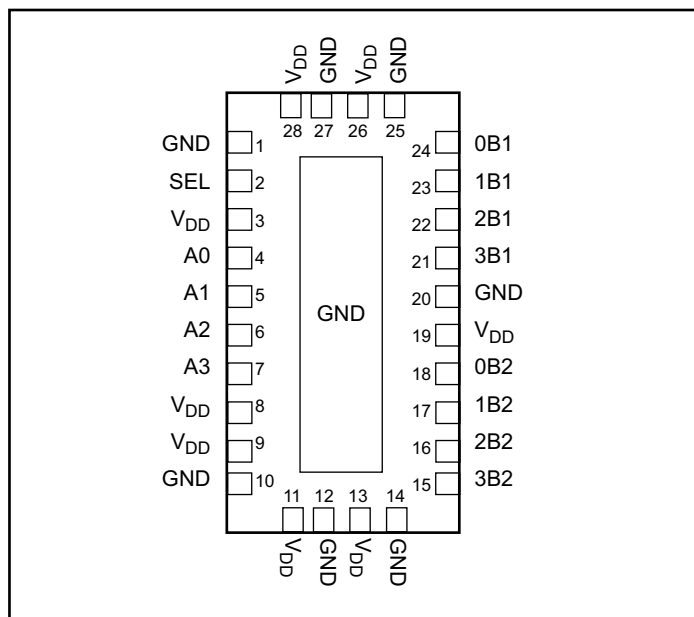
Application

Switch a PCI Express® lane output between two PCI Express lane inputs

Block Diagram



Pin Description



Truth Table

Function	SEL
A _n to nB ₁	L
A _n to nB ₂	H

Maximum Ratings

(Above which useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	-65°C to +150°C
Supply Voltage to Ground Potential	-0.5V to +2.5V
DC Input Voltage	-0.5V to +V _{DD}
DC Output Current	120mA
Power Dissipation	0.5W

Note: Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics for Switching over Operating Range

(T_A = -40°C to +85°C, V_{DD} = 1.5V to 2.0V)

Parameter	Description	Test Conditions	Min.	Typ. (2)	Max.	Units
V _{IH}	Input HIGH Voltage	Guaranteed HIGH level	0.65 x V _{DD}	-	-	V
V _{IL}	Input LOW Voltage	Guaranteed LOW level	-0.5	-	0.35 x V _{DD}	
V _{IK}	Clamp Diode Voltage	V _{DD} = Max., I _{IN} = -18mA	-	-0.7	-1.2	
I _{IH}	Input HIGH Current	V _{DD} = Max., V _{IN} = V _{DD}	-	-	±5	μA
I _{IL}	Input LOW Current	V _{DD} = Max., V _{IN} = GND	-	-	±5	

Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
I _{CC}	Quiescent Power Supply Current	V _{DD} = Max., V _{IN} = GND or V _{DD}		200		μA

Notes:

- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at V_{DD} = 1.8V, T_A = 25°C ambient and maximum loading.

Dynamic Electrical Characteristics Over the Operating Range (T_A = -40° to +85°C, V_{DD} = 1.8V±10%, GND=0V)

Parameter	Description	Test Conditions	Min.	Typ. ⁽²⁾	Max.	Units
X _{TALK}	Crosstalk	See Fig. 1 for Measurement Setup, f = 10 MHz		-60		dB
O _{IRR}	OFF Isolation	See Fig. 2 for Measurement Setup, f = 10 MHz		-65		

Notes:

- Guaranteed by design.

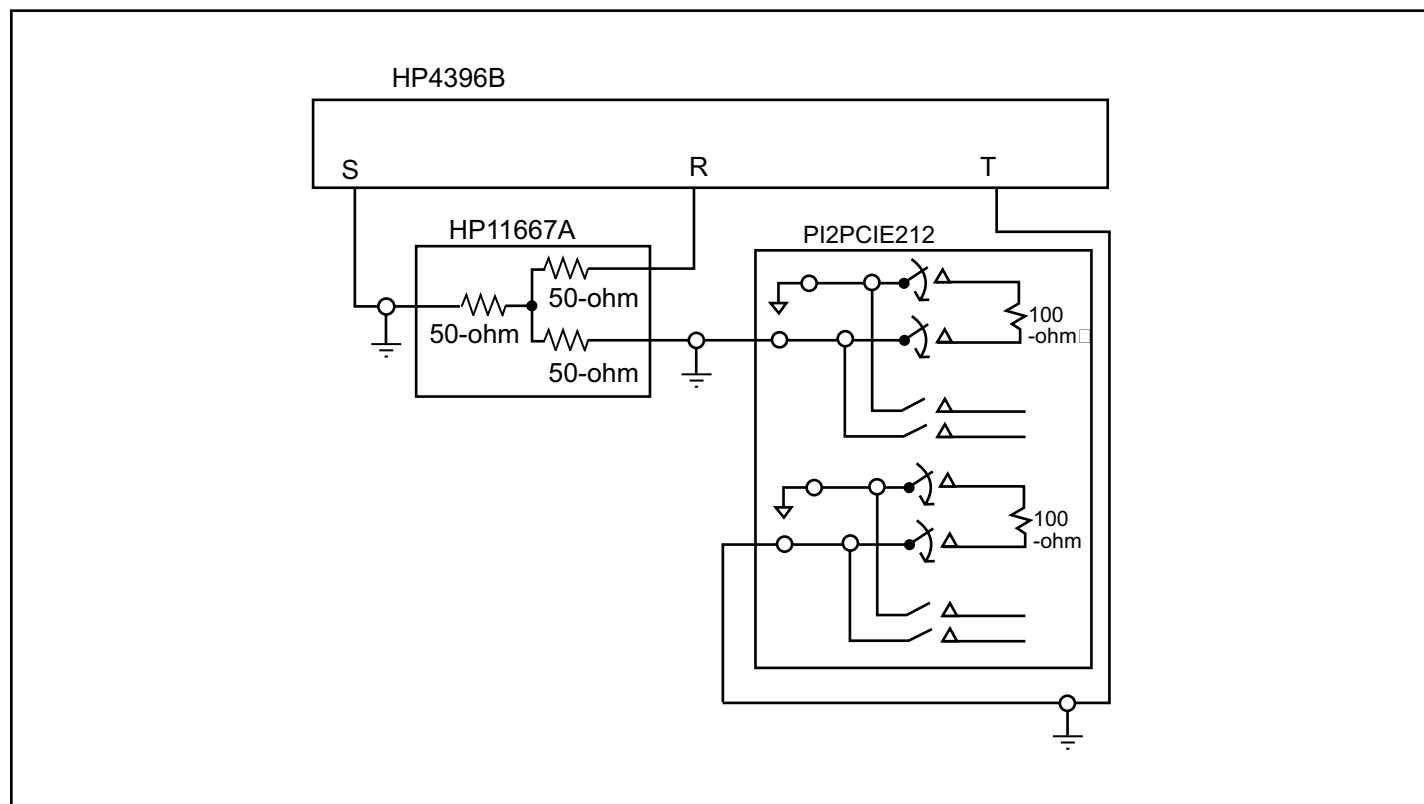


Fig 1. Crosstalk Setup

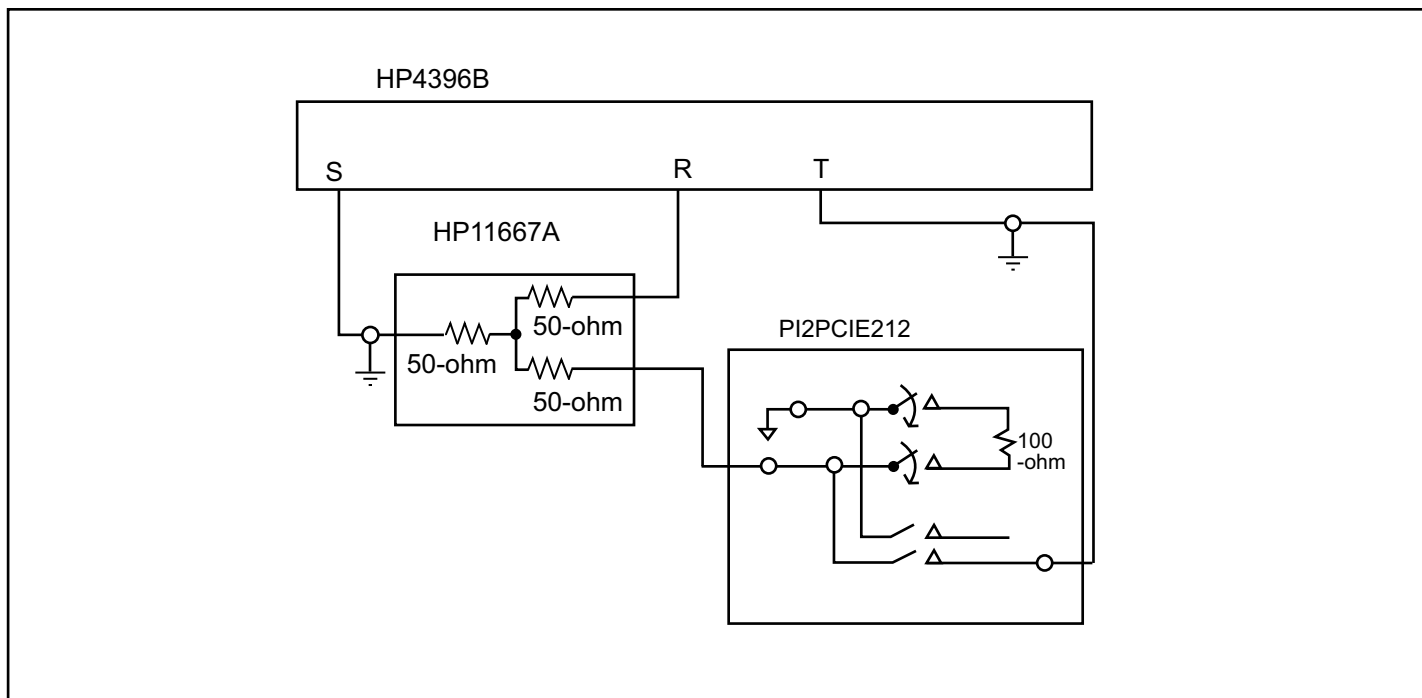


Fig 2. Off-isolation setup

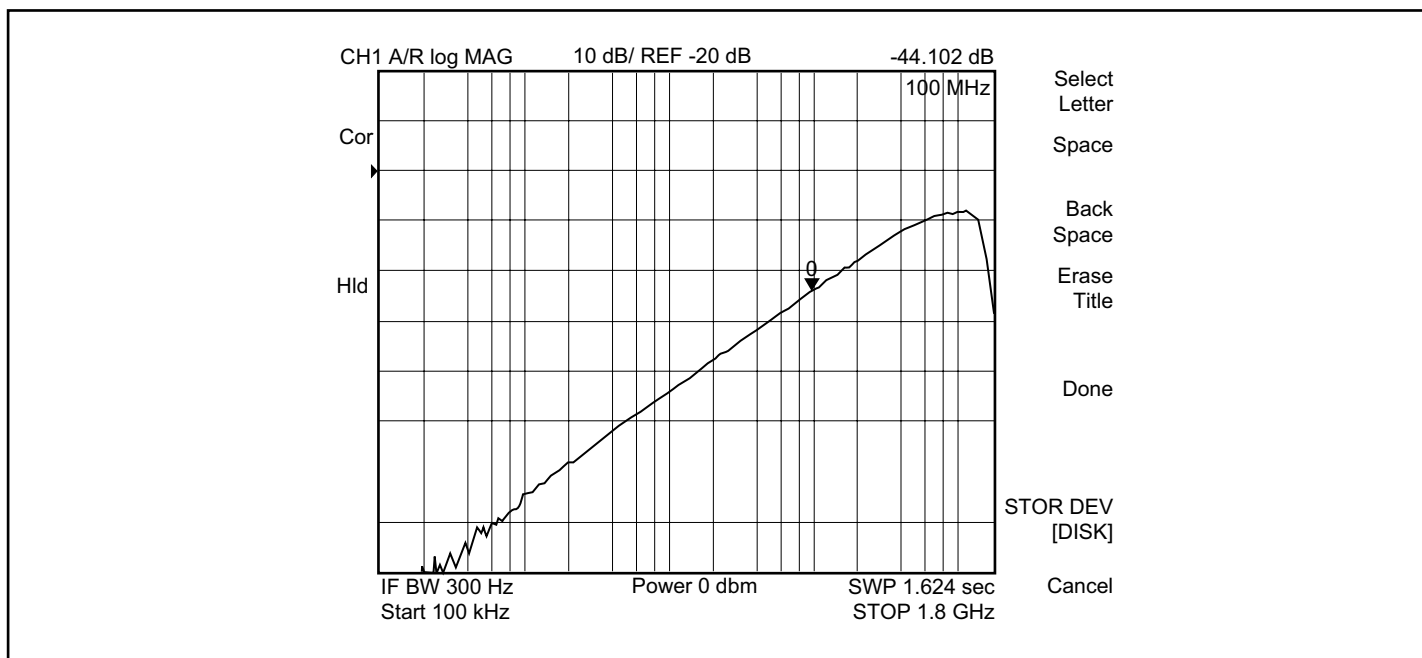


Fig 3. Crosstalk

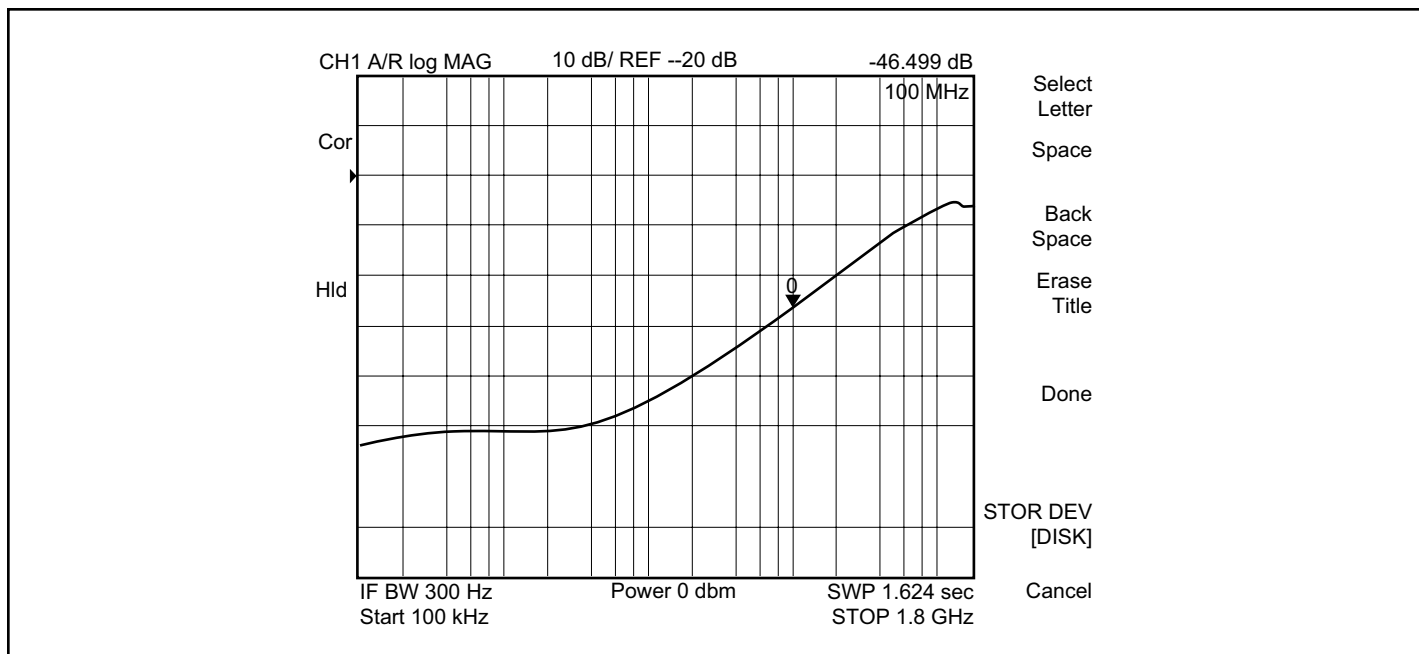


Fig 4. Off Isolation

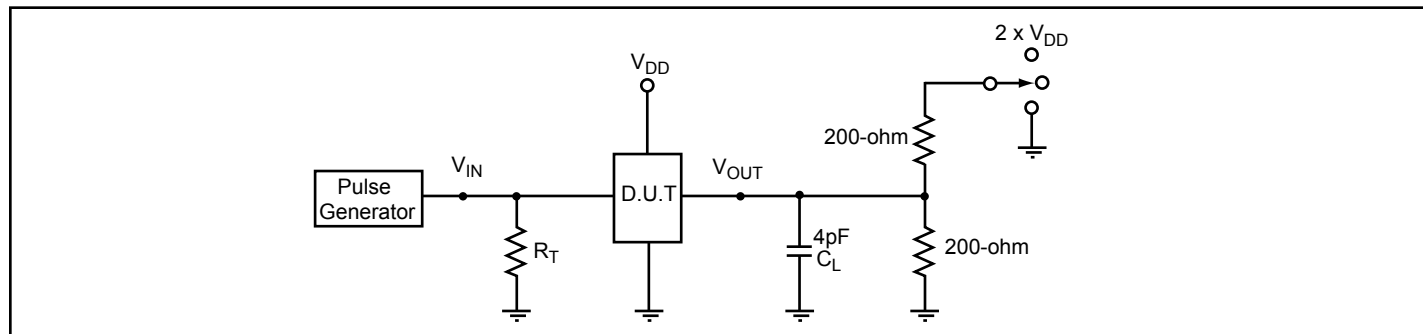
Switching Characteristics ($T_A = -40^\circ$ to $+85^\circ\text{C}$, $V_{DD} = 1.8\text{V} \pm 10\%$)

Parameter	Description	Min.	Typ. ⁽²⁾	Max.	Units
t _{PZH} , t _{PZL}	Line Enable Time - SEL to A _N , B _N	0.5	-	8.0	
t _{PHZ} , t _{PLZ}	Line Disable Time - SEL to A _N , B _N	0.5	-	4.0	

Notes:

- For max. or min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.

Test Circuit for Electrical Characteristics⁽¹⁾



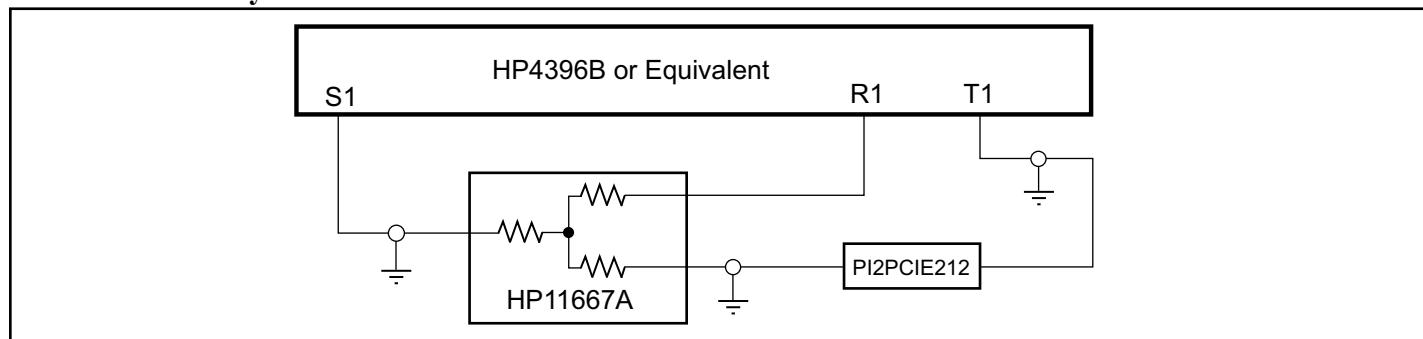
Notes:

1. C_L = Load capacitance: includes jig and probe capacitance.
2. R_T = Termination resistance: should be equal to Z_{OUT} of the Pulse Generator
3. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control.
Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
4. All input impulses are supplied by generators having the following characteristics: $PRR \leq \text{MHz}$, $Z_O = 50\Omega$, $t_R \leq 2.5\text{ns}$, $t_F \leq 2.5\text{ns}$.
5. The outputs are measured one at a time with on transition per measurement.

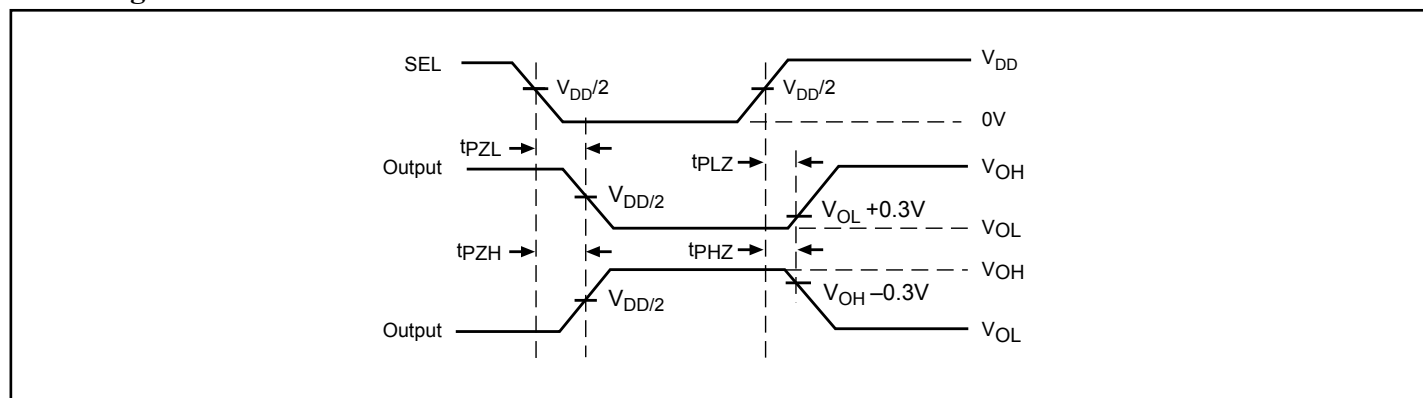
Switch Positions

Test	Switch
t_{PLZ} , t_{PZL} (output on I-side)	$2 \times V_{DD}$
t_{PHZ} , t_{PZH} (output on I-side)	GND
Prop Delay	Open

Test Circuit for Dynamic Electrical Characteristics

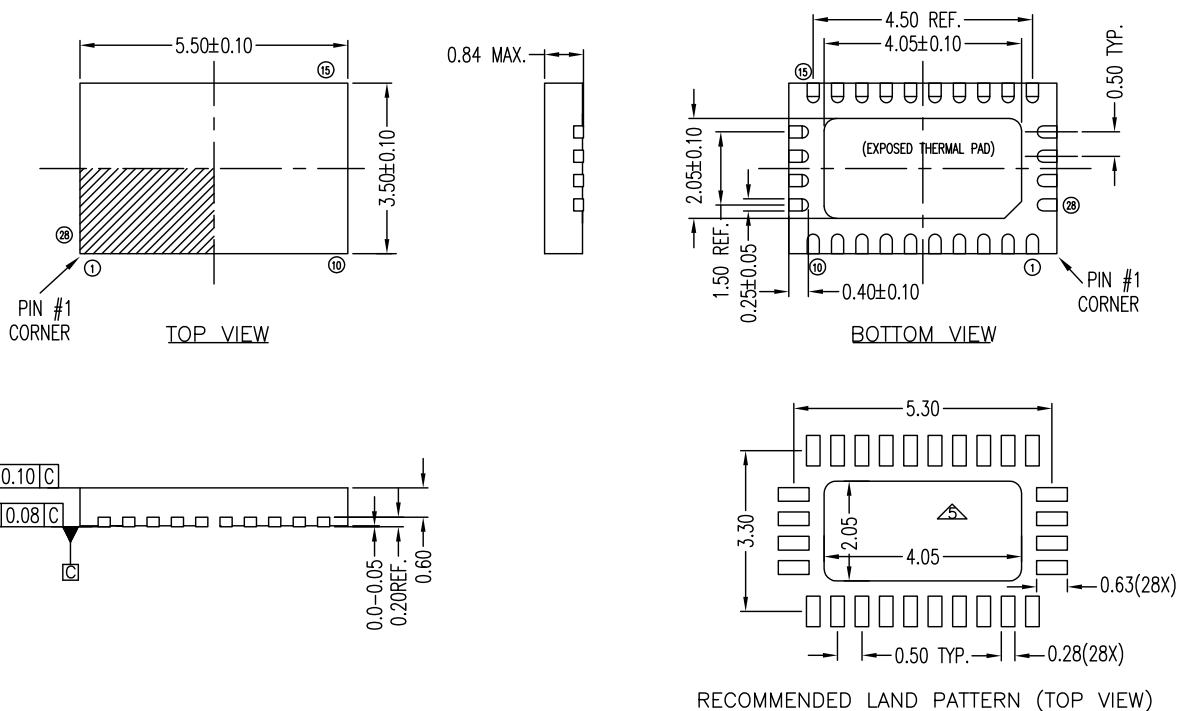


Switching Waveforms



Voltage Waveforms Enable and Disable Times

Package Mechanical: 28-Contact TQFN (ZH)



NOTE :

1. ALL DIMENSIONS ARE IN mm. ANGLES IN DEGREES.
2. COPLANARITY APPLIES TO THE EXPOSED THERMAL PAD AS WELL AS THE TERMINALS.
3. REFER JEDEC MO-220
4. RECOMMENDED LAND PATTERN IS FOR REFERENCE ONLY.
5. THERMAL PAD SOLDERING AREA (MESH STENCIL DESIGN IS RECOMMENDED).



DATE: 07/11/12

DESCRIPTION: 28-Contact, Very Thin Quad Flat No-Lead, TQFN

PACKAGE CODE: ZH28

DOCUMENT CONTROL #: PD-2034

REVISION: C

12-0419

Ordering Information

Ordering Code	Package Code	Package Type
PI2PCIE212ZHE	ZH	Pb-free & Green, 28-Contact TQFN

Notes:

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- E = Pb-free and Green
- Adding an X suffix = Tape/Reel