

Product Summary

BV _{DSS}	R _{DS(ON)} MAX	I _D MAX T _C = +25°C
60V	1.5mΩ @ V _{GS} = 10V	225A

Description and Applications

This new generation N-channel enhancement mode MOSFET is designed to minimize R_{DS(ON)} yet maintain superior switching performance. This device is ideal for use in power management and load switch.

- Engine Management Systems
- Body Control Electronics
- DC-DC Converters

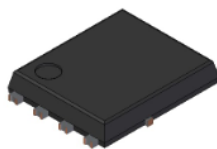
Features

- Rated to +175°C – Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production – Ensures More Reliable and Robust End Application
- High-Conversion Efficiency
- Low R_{DS(ON)} – Minimizes On State Losses
- Low Input Capacitance
- Fast Switching Speed
- Wettable Flank for Improved Optical Inspection
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](https://www.diodes.com/contact-us) or your local Diodes representative.
<https://www.diodes.com/quality/product-definitions/>

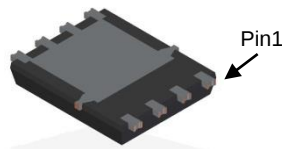
Mechanical Data

- Case: PowerDI®5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 0.097 grams (Approximate)

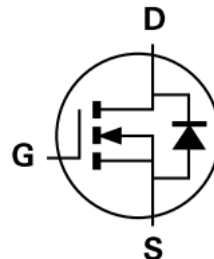
PowerDI5060-8 (SWP) (Standard)



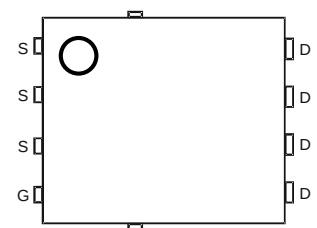
Top View



Bottom View



Internal Schematic



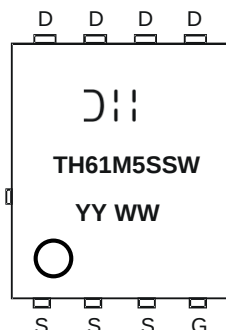
Top View
Pin Configuration

Ordering Information (Note 4)

Part Number	Case	Packaging
DMTH61M5SPSW-13	PowerDI5060-8 (SWP) (Standard)	2500 / Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



D = Manufacturer's Marking
 TH61M5SSW = Product Type Marking Code
 YYWW or YYWW = Date Code Marking
 YY or YY = Year (ex: 20 = 2020)
 WW = Week (01 to 53)

PowerDI is a registered trademark of Diodes Incorporated.

DMTH61M5SPSW
Document number: DS42030 Rev. 3 - 2

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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	60	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current, V _{GS} = 10V (Note 6)	I _D	225	A
		160	
Maximum Continuous Body Diode Forward Current (Note 6)	I _S	225	A
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)	I _{DM}	900	A
Pulsed Body Diode Forward Current (10μs Pulse, Duty Cycle = 1%)	I _{SM}	900	A
Avalanche Current, L = 1mH	I _{AS}	35.8	A
Avalanche Energy, L = 1mH	E _{AS}	640.8	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P _D	3.2	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA}	47	°C/W
Total Power Dissipation (Note 6)	P _D	167	W
Thermal Resistance, Junction to Case (Note 6)	R _{θJC}	0.9	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	60	—	—	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	μA	V _{DS} = 48V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	2	—	4	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	1.1	1.5	mΩ	V _{GS} = 10V, I _D = 30A
Diode Forward Voltage	V _{SD}	—	0.7	1.2	V	V _{GS} = 0V, I _S = 20A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	8306	—	pF	V _{DS} = 30V, V _{GS} = 0V, f = 1MHz
Output Capacitance	C _{oss}	—	2735	—		
Reverse Transfer Capacitance	C _{rss}	—	184	—		
Gate Resistance	R _g	—	3.0	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge	Q _g	—	130.6	—	nC	V _{DS} = 30V, I _D = 30A, V _{GS} = 10V
Gate-Source Charge	Q _{gs}	—	30.4	—		
Gate-Drain Charge	Q _{gd}	—	28.1	—		
Turn-On Delay Time	t _{D(ON)}	—	11.3	—	ns	V _{DD} = 30V, V _{GS} = 10V, I _D = 30A, R _g = 3Ω
Turn-On Rise Time	t _r	—	28.5	—		
Turn-Off Delay Time	t _{D(OFF)}	—	86.2	—		
Turn-Off Fall Time	t _f	—	47.6	—	ns	I _F = 30A, di/dt = 100A/μs
Body Diode Reverse Recovery Time	t _{RR}	—	70.4	—		
Body Diode Reverse Recovery Charge	Q _{RR}	—	127	—	nC	

- Notes:
- Device mounted on FR-4 substrate PCB, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 - Thermal resistance from junction to soldering point (on the exposed drain pad).
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.

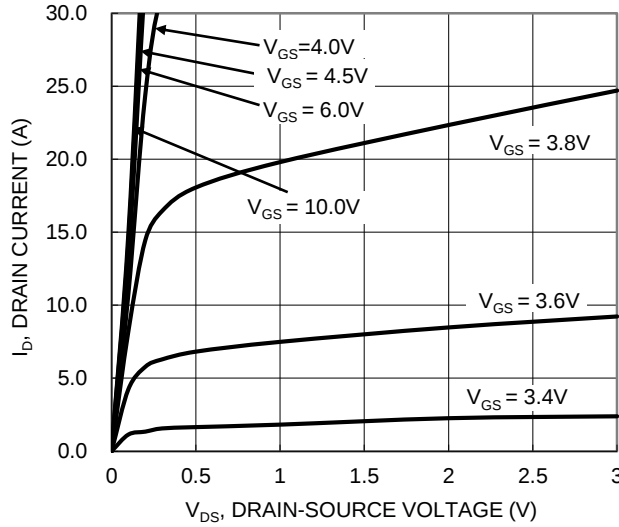


Figure 1. Typical Output Characteristic

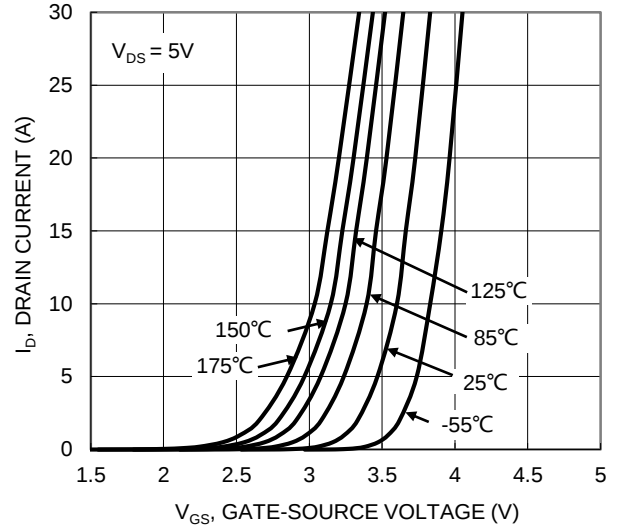


Figure 2. Typical Transfer Characteristic

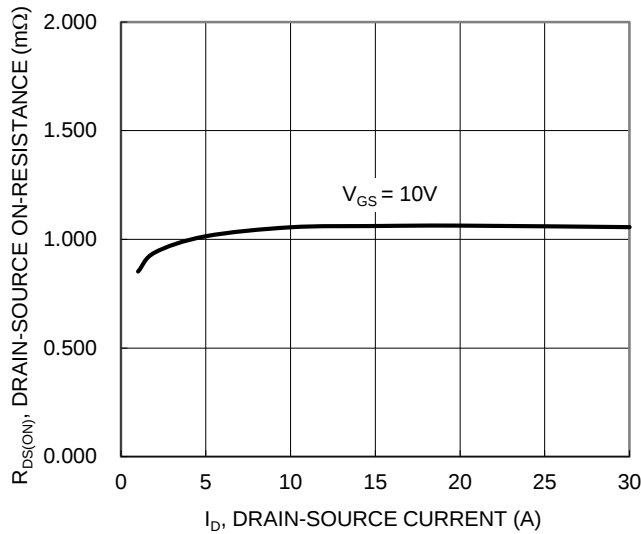


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

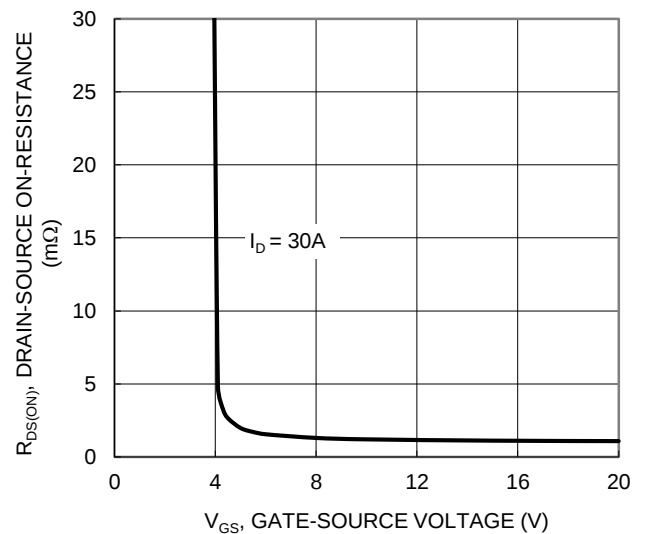


Figure 4. Typical Transfer Characteristic

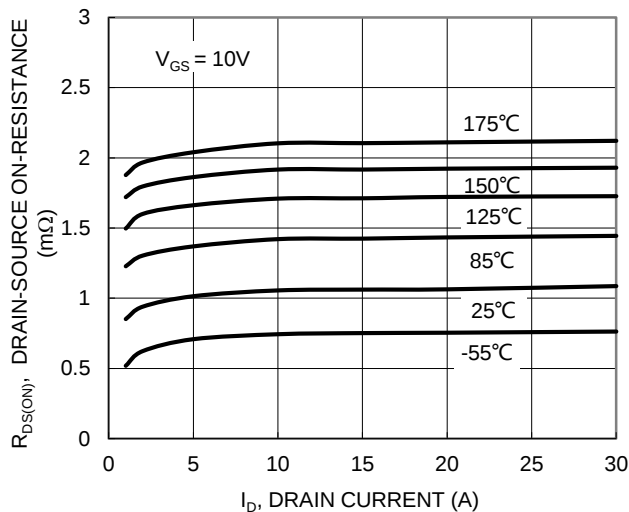


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

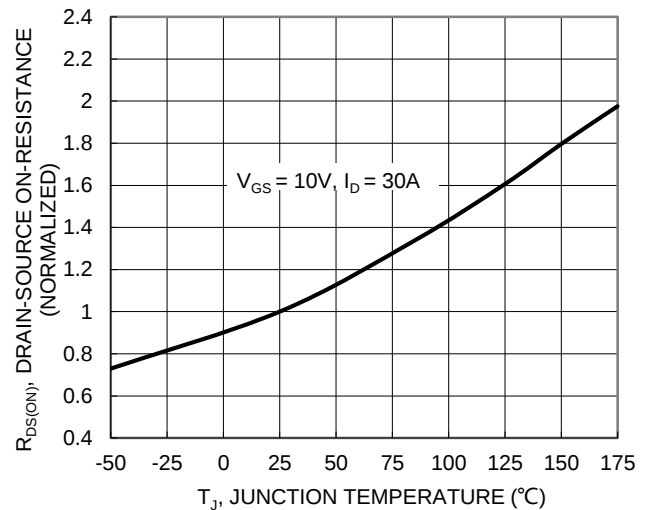
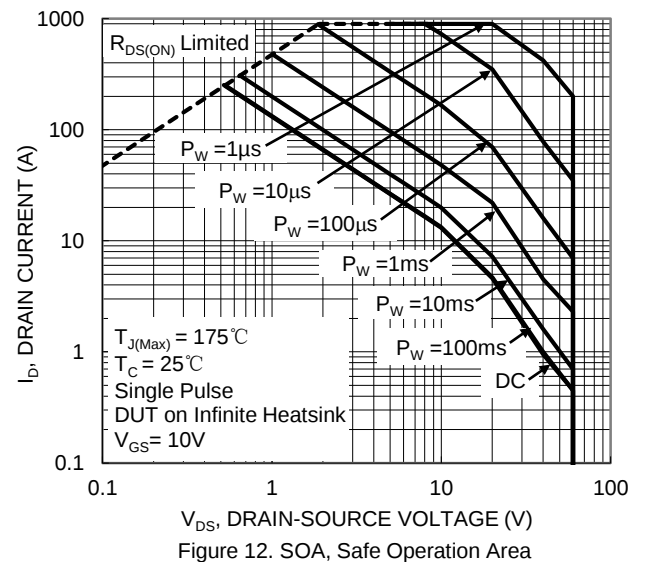
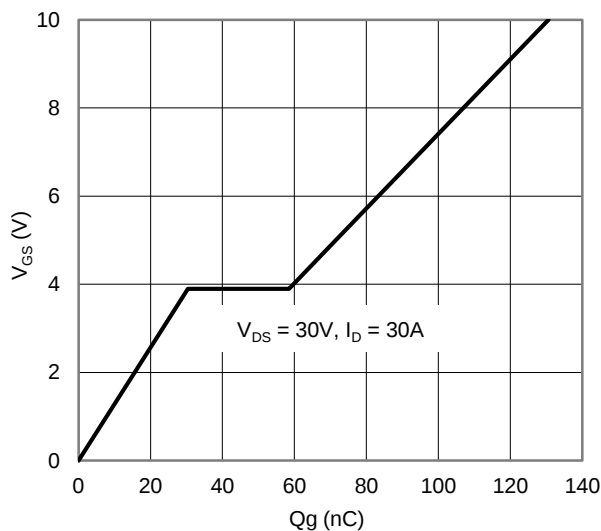
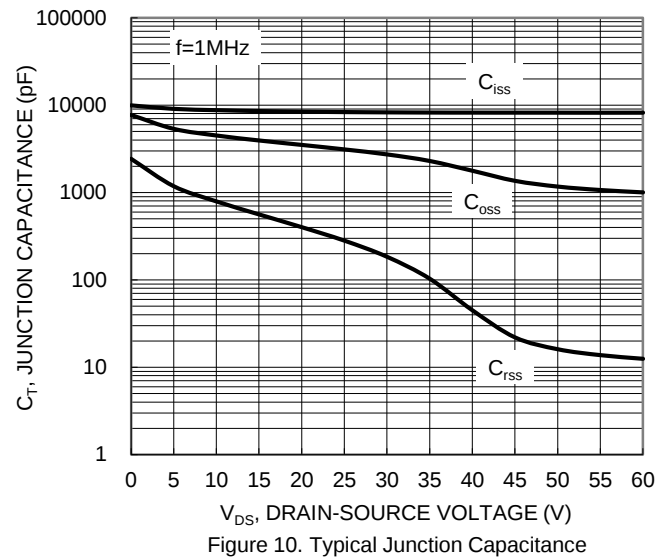
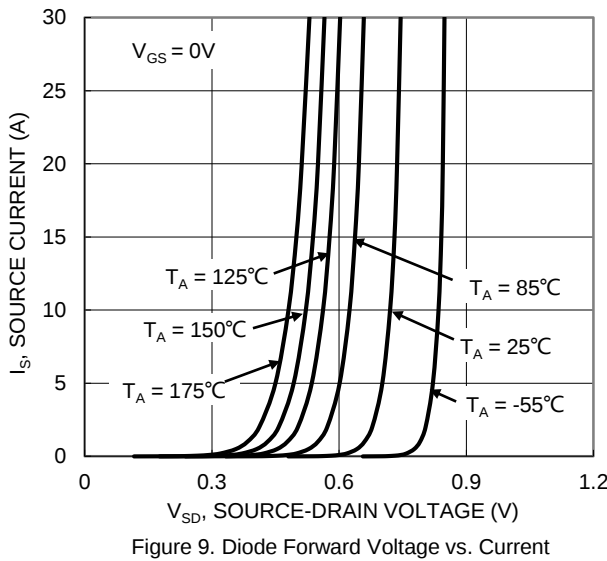
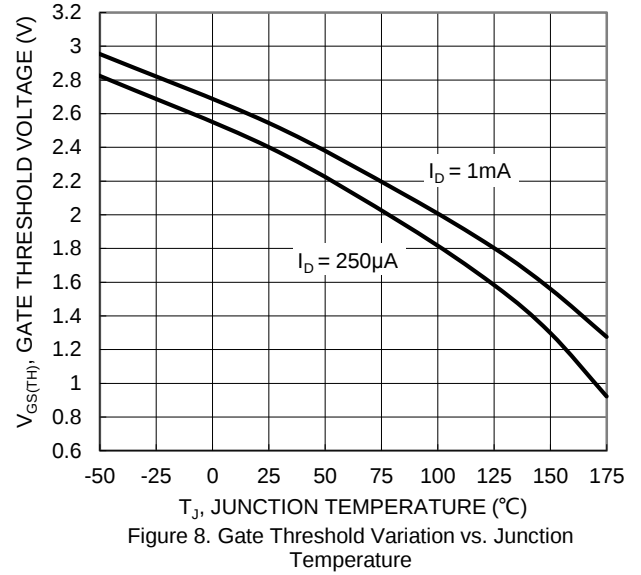
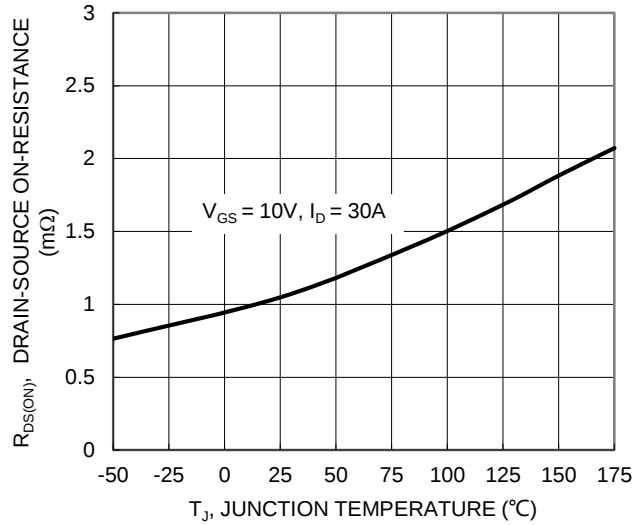


Figure 6. On-Resistance Variation with Temperature



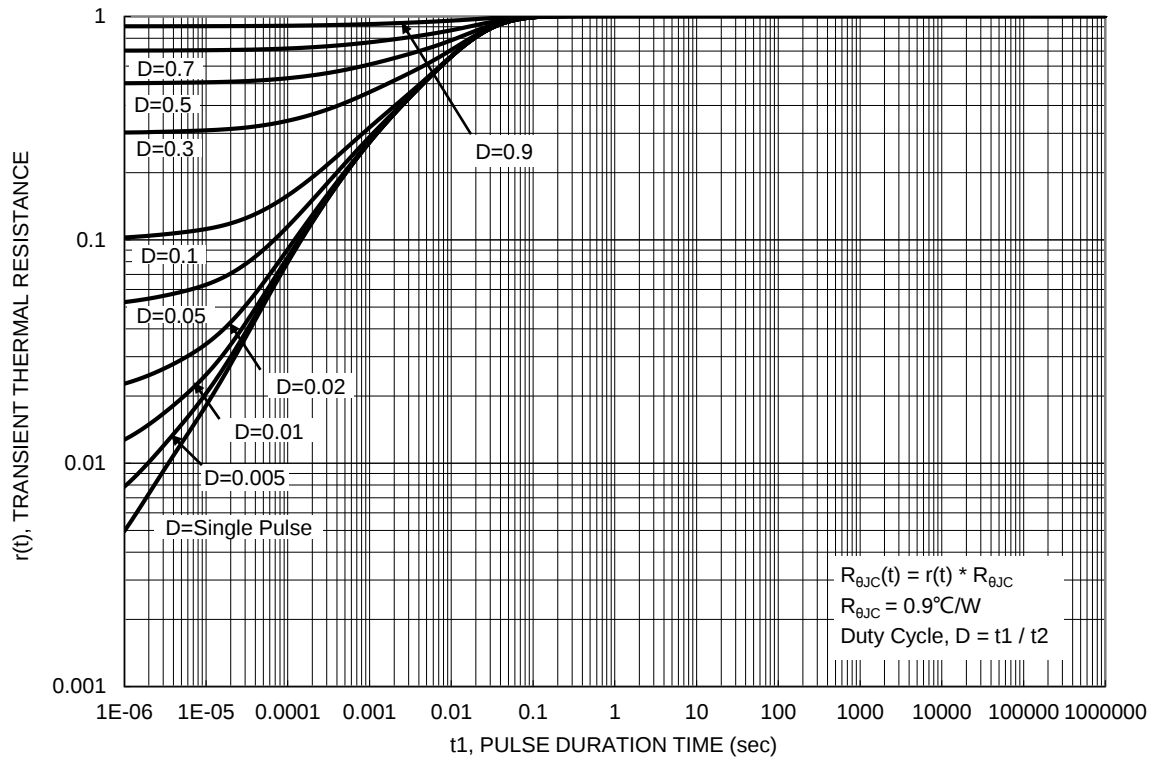
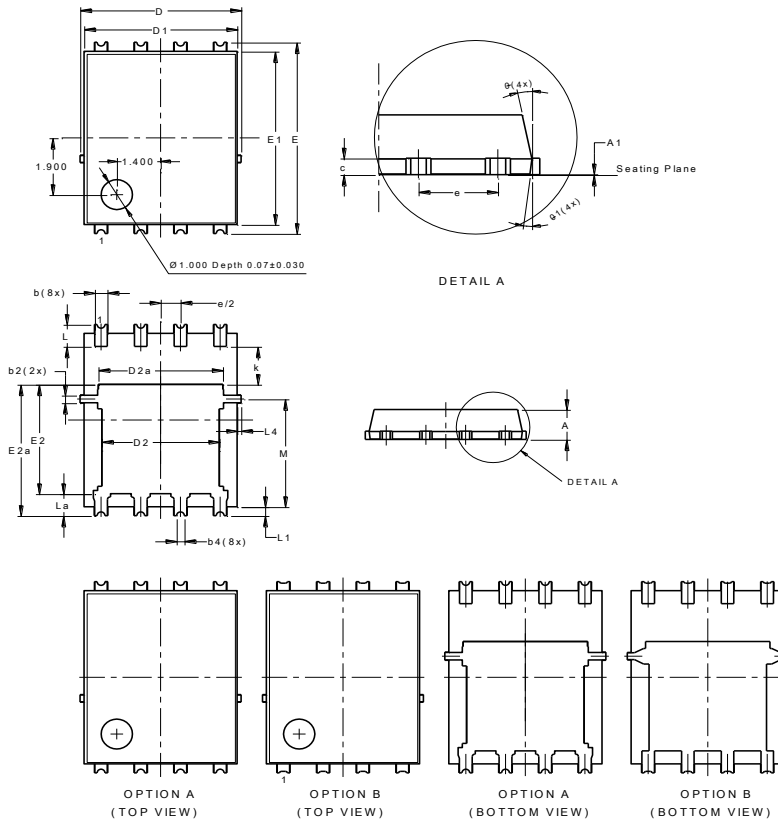


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI5060-8 (SWP) (Standard)

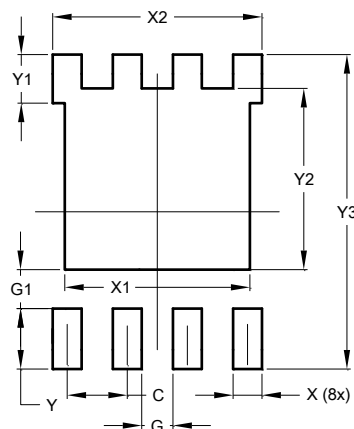


PowerDI5060-8 (SWP) (Standard)			
Dim	Min	Max	Typ
A	0.90	1.10	1.00
A1	0	0.05	--
b	0.30	0.50	0.41
b2	0.20	0.35	0.25
b4	0.25REF		
c	0.230	0.330	0.277
D	5.15 BSC		
D1	4.70	5.10	4.90
D2	3.56	3.96	3.76
D2a	3.78	4.18	3.98
E	6.40 BSC		
E1	5.60	6.00	5.80
E2	3.46	3.86	3.66
E2a	4.195	4.595	4.395
e	1.27BSC		
k	1.05	--	--
L	0.635	0.835	0.735
La	0.635	0.835	0.735
L1	0.200	0.400	0.300
L4	0.025	0.225	0.125
M	3.205	4.005	3.605
θ	10°	12°	11°
θ1	6°	8°	7°
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI5060-8 (SWP) (Standard)



Dimensions	Value (in mm)
C	1.270
G	0.660
G1	0.820
X	0.610
X1	4.100
X2	4.420
Y	1.270
Y1	1.020
Y2	3.810
Y3	6.610

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