

PROTECTION PRODUCTS

Description

RClamp® TVS diodes are designed to protect sensitive electronics from damage or latch-up due to ESD & EOS. These devices offer desirable characteristics for board level protection including fast response time, low operating and clamping voltage, and no device degradation.

RClamp®1221ZA features extremely good ESD protection characteristics highlighted by low typical dynamic resistance, low peak ESD clamping voltage, and high ESD withstand voltage ($\pm 10\text{kV}$ contact per IEC 61000-4-2). Low maximum capacitance (0.45pF at $V_R=0\text{V}$) minimizes loading on sensitive circuits. Each device will protect one high-speed data line operating at 12 Volts.

This device is in a 2-pin SLP0603P2X3F package measuring $0.6 \times 0.3 \text{ mm}$ with a nominal height of only 0.25mm . Leads are finished with NiAu. The small package gives the designer the flexibility to protect single lines in applications where arrays are not practical. The combination of small size and high ESD surge capability makes them ideal for use in portable applications.

Features

- High ESD withstand Voltage: $\pm 10\text{kV}$ (Contact) per IEC 61000-4-2 and $\pm 18\text{kV}$ (air) per IEC 61000-4-2
- Ultra-small package
- Protects one data line
- Low ESD clamping voltage
- Working voltage: 12V
- Low capacitance: 0.45pF maximum
- Low leakage current
- Low dynamic resistance
- Solid-state silicon-avalanche technology

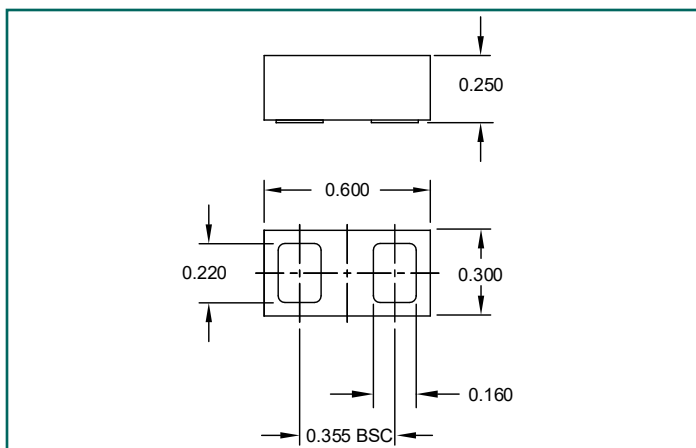
Mechanical Characteristics

- SLP0603P2X3F package
- Pb-Free, Halogen Free, RoHS/WEEE compliant
- Nominal Dimensions: $0.6 \times 0.3 \times 0.25 \text{ mm}$
- Lead Finish: NiAu
- Marking: Marking code
- Packaging: Tape and Reel

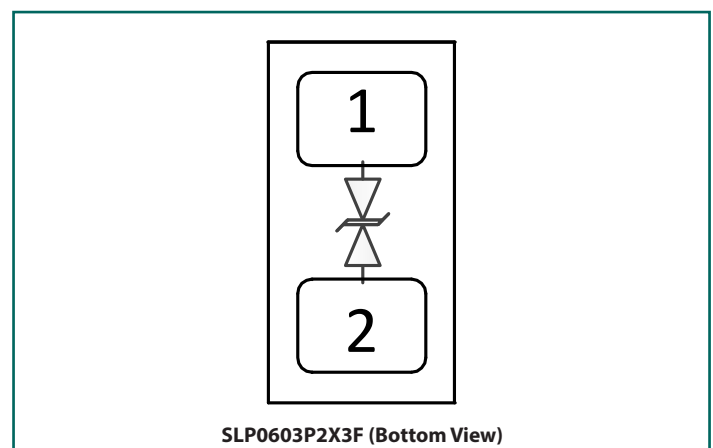
Applications

- Touchscreen Controllers
- USB 3.0 / USB Type-C
- MiPi/MDDI
- MHL
- FM antenna
- Wearables

Package Dimension



Schematic & Pin Configuration



Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Current (tp = 8/20μs)	I _{PP}	4	A
ESD per IEC 61000-4-2 (Air) ⁽¹⁾ ESD per IEC 61000-4-2 (Contact) ⁽¹⁾	V _{ESD}	±18 ±10	kV
Operating Temperature	T _{OP}	-40 to +85	°C
Storage Temperature	T _{STG}	-55 to +150	°C

Electrical Characteristics (T=25°C unless otherwise specified)

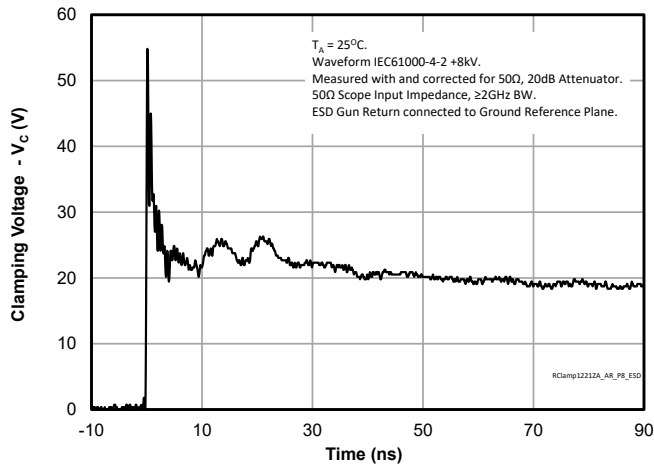
RClamp1221ZA							
Parameter	Symbol	Conditions		Min.	Typ.	Max.	Units
Reverse Stand-Off Voltage	V _{RWM}					12	V
Reverse Breakdown Voltage	V _{BR}	I _t = 1mA		13.5	15.5	17.5	V
Reverse Leakage Current	I _R	V _{RWM} = 12V			<5	100	nA
Clamping Voltage	V _C	tp = 8/20μs	I _{PP} = 1A		16.5	21	V
			I _{PP} = 4A		19.4	24	V
ESD Clamping Voltage ²	V _C	tp = 0.2/100ns	I _{PP} = 4A		17.6		V
			I _{PP} = 16A		24.7		
Dynamic Resistance ^{2,3}	R _{DYN}	tp = 0.2/100ns			0.59		Ω
Junction Capacitance	C _J	V _R = 0V, f = 1MHz	T = 25°C		0.35	0.45	pF

Notes

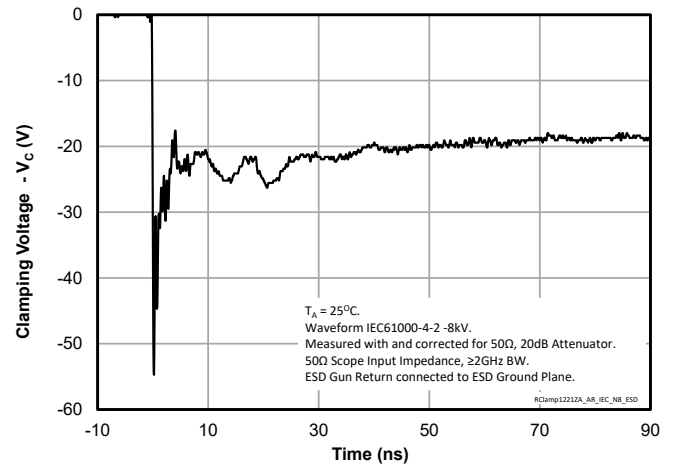
- 1) Measured with a 20dB attenuator, 50 Ohm scope input impedance, 2GHz bandwidth. ESD gun return path connected to ESD ground plane.
- 2) Transmission Line Pulse Test (TLP) Settings: tp = 100ns, tr = 0.2ns, I_{TLP} and V_{TLP} averaging window: t1 = 70ns to t2 = 90ns.
- 3) Dynamic resistance calculated from I_{TLP} = 4A to I_{TLP} = 16A

Typical Characteristics

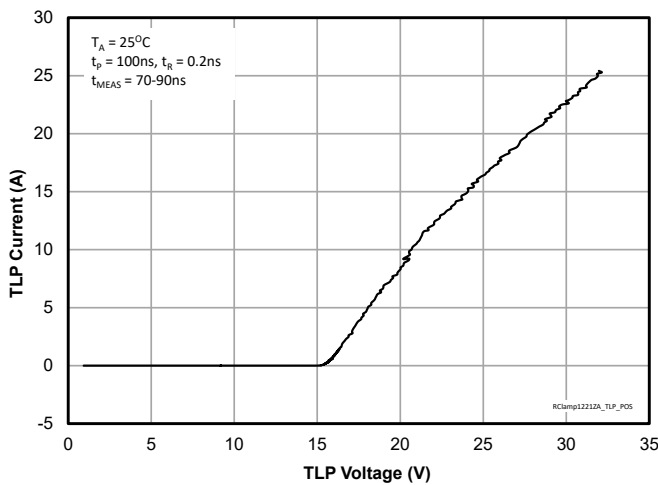
ESD Clamping (8kV Contact per IEC 61000-4-2)



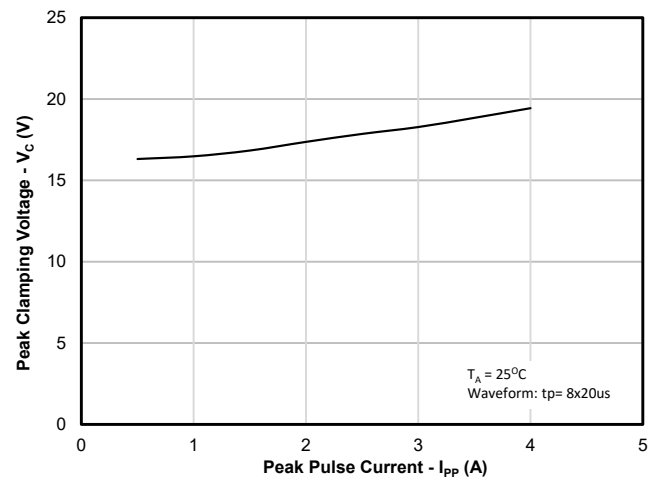
ESD Clamping (-8kV Contact per IEC 61000-4-2)



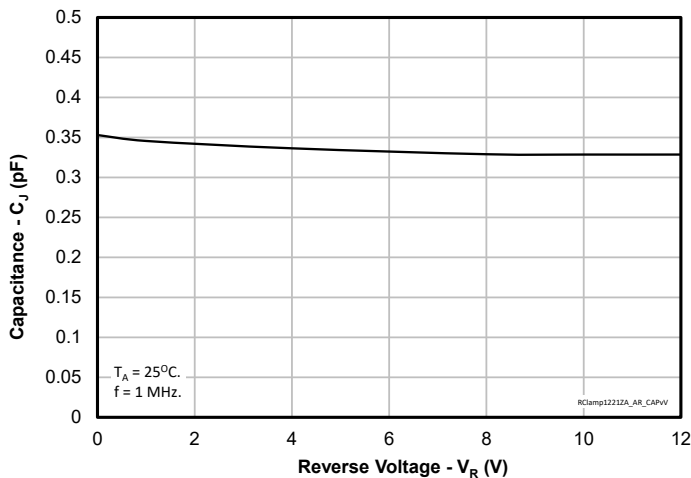
TLP Characteristic (Positive Pulse)



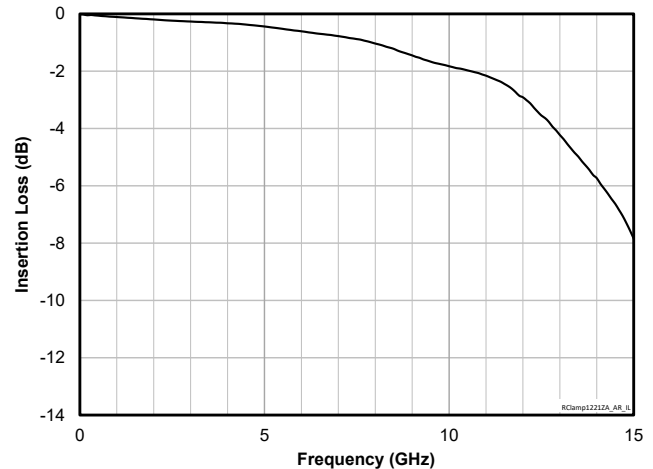
Clamping Voltage ($t_p=8/20\mu\text{s}$)



Capacitance vs. Reverse Voltage



Insertion Loss - S21



Application Information

Assembly Guidelines

Care must be taken during the mounting process to ensure reliable solder joints. The figure at the right details Semtech’s recommended mounting pattern. Recommended assembly guidelines are shown in Table 1. Note that these are only recommendations and should serve only as a starting point for design since there are many factors that affect the assembly process. Exact manufacturing parameters will require some experimentation to get the desired solder application.

Solder Stencil

Stencil design is one of the key factors determining the volume of solder paste which is deposited onto the land pad. The area ratio of the stencil aperture will determine how well the stencil will print. The area ratio takes into account the aperture shape, aperture size, and stencil thickness. A minimum area ratio of 0.66 is preferred for the subject package. The area ratio of a rectangular aperture is given as:

Area Ratio = (L * W) / (2 * (L + W) * T)

Where:

L = Aperture Length

W = Aperture Width

T = Stencil Thickness

Semtech recommends a stencil with square aperture and rounded corners for consistent solder release. The stencil should be laser cut with electro-polished finish. A stencil thickness of 0.075mm (0.003”) is recommended. A 0.100mm (0.004”) stencil may be used, however the stencil opening may need to be increased slightly to achieve the desired area ratio to ensure proper solder coverage on the pad.

Recommended Mounting Pattern

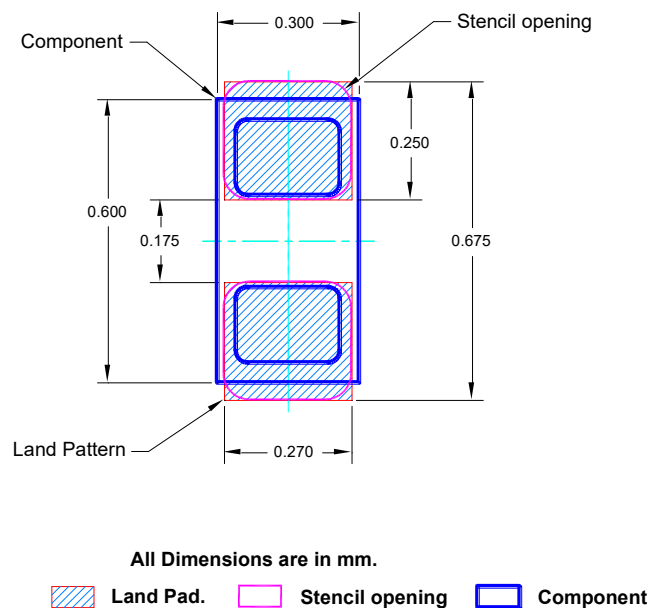
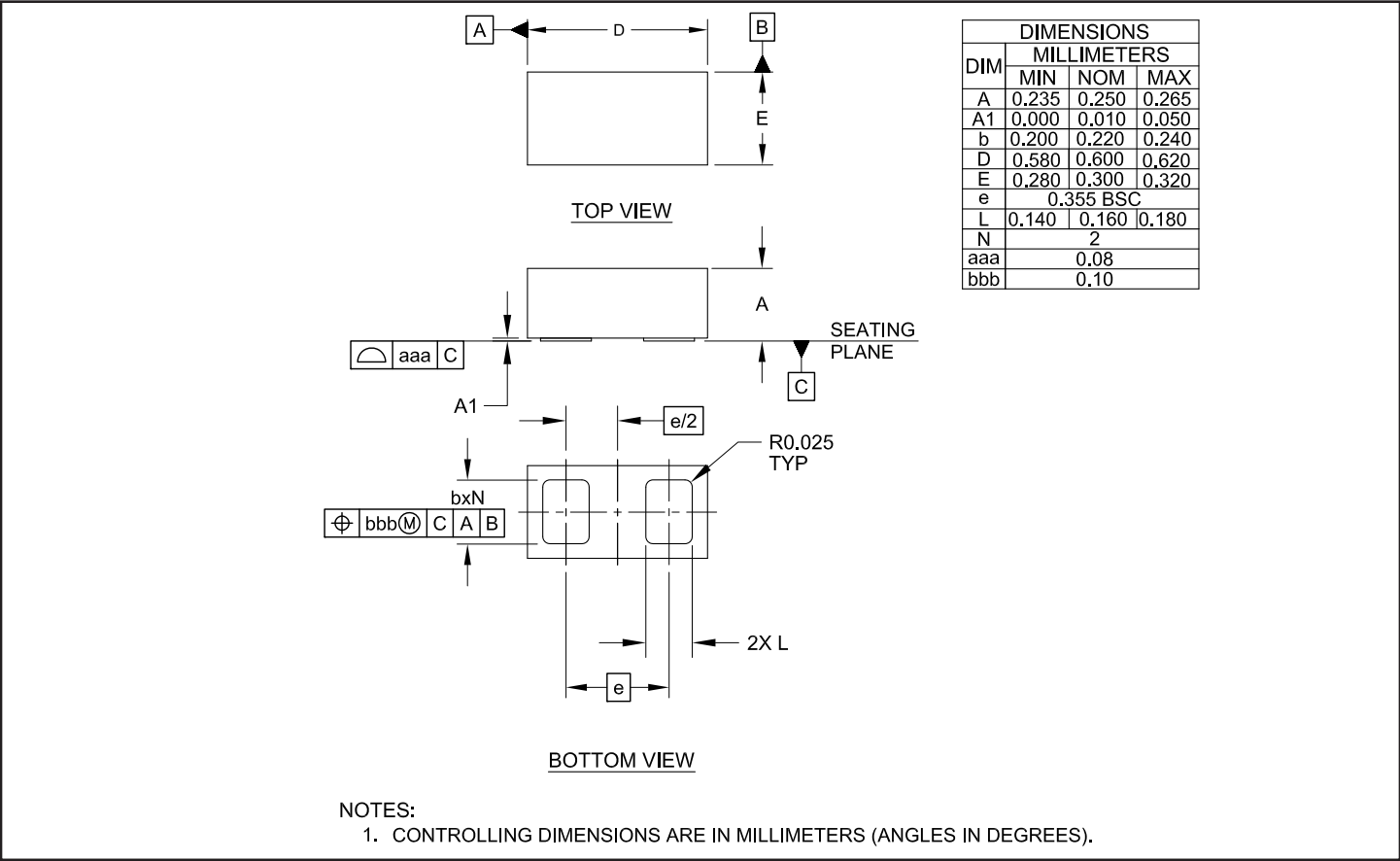


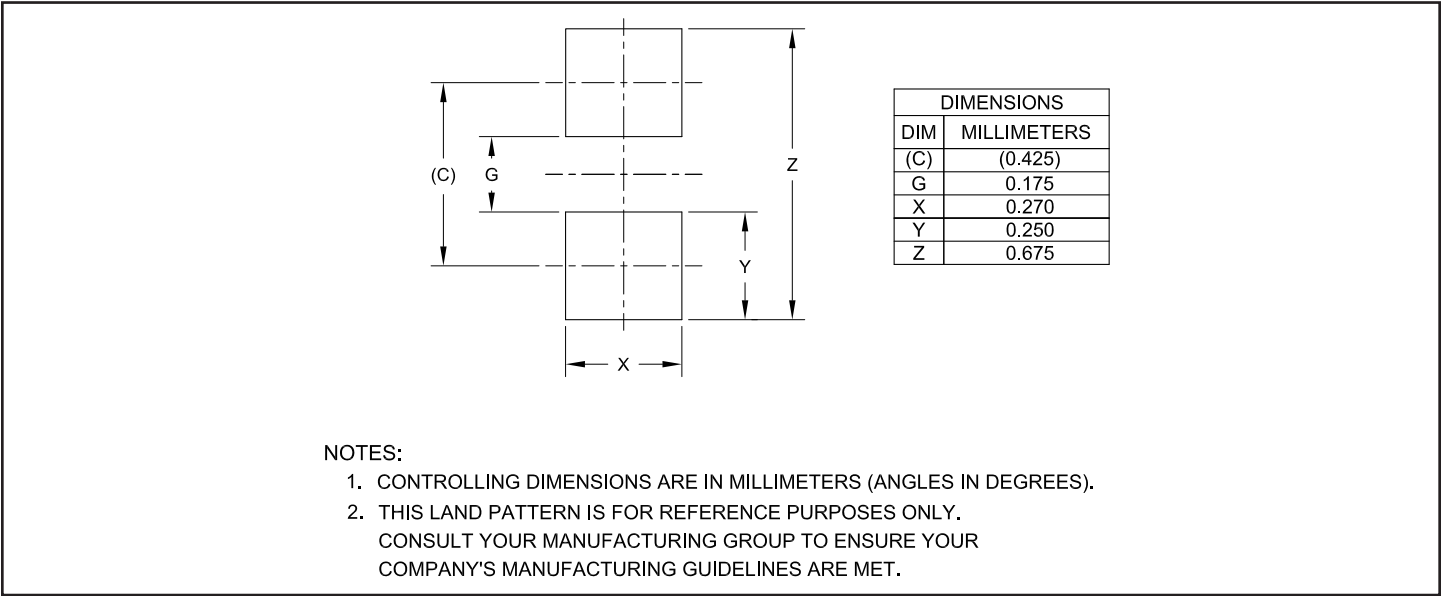
Table 1 - Assembly Guidelines

Assembly Parameter	Recommendation
Solder Stencil Design	Laser Cut, Electro-Polished
Aperture Shape	Rectangular with Rounded Corners
Solder Stencil Thickness	0.075mm (0.003”) or 0.100mm (0.004”)
Solder Paste Type	Type 4 Size Sphere or Smaller
Solder Reflow Profile	Per JEDEC J-STD-020
PCB Solder Pad Design	Solder Mask Defined or Non Solder Mask Defined
PCB Pad Finish	OSP or NiAu

Outline Drawing - SLP0603P2X3F



Land Pattern - SLP0603P2X3F

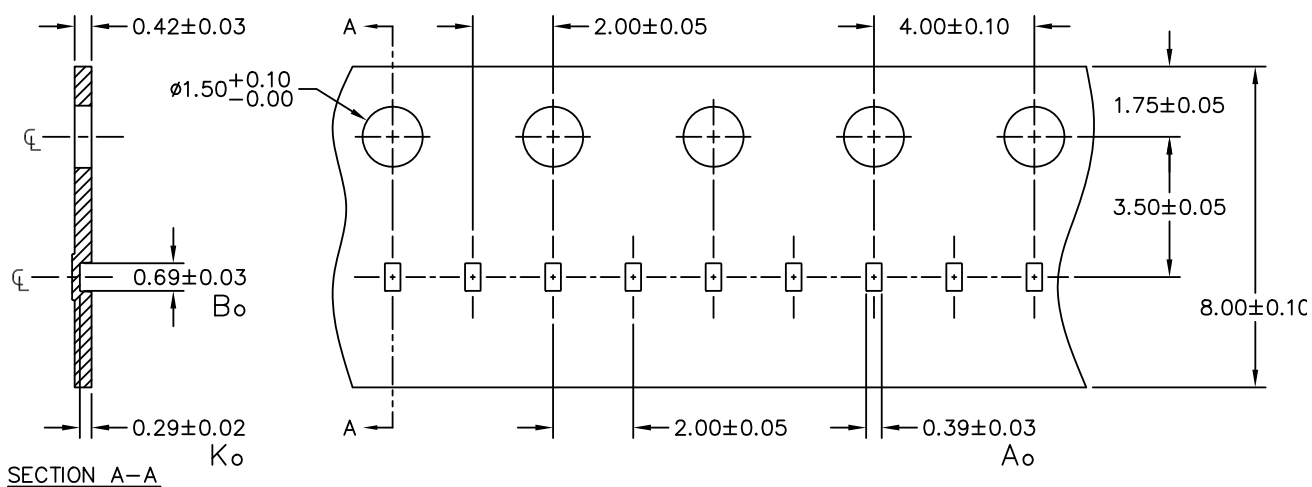


Marking Codes

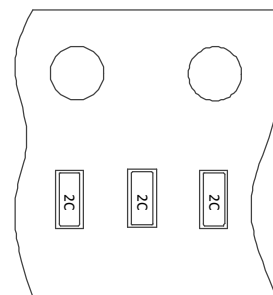
2C

RClamp1221ZA

Tape and Reel Specification



NOTES: ALL DIMENSIONS IN MILLIMETERS UNLESS OTHERWISE SPECIFIED.



Ordering Information

Part Number	Qty per Reel	Reel Size
RClamp1221ZATFT	15,000	7"

Notes:
1) RailClamp and RClamp are trademarks of Semtech Corporation.



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