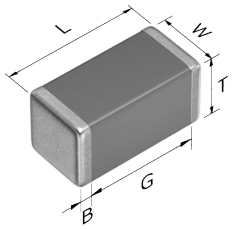
Multilayer Ceramic Chip Capacitors

CGA2B2C0G1H561J050BA

Product Status	Production
TDK Item Description	CGA2B2C0G1H561JT****
Applications	Automotive Grade
Feature	AEC-Q200AEC-Q200 GeneralGeneral (Up to 50V)
Series	CGA2(1005) [EIA 0402]
Brand	TDK
Environmental Compliance	RoHS, REACH, Halogen Free, Lead Free



Size	
Length(L)	1.00mm ±0.05mm
Width(W)	0.50mm ±0.05mm
Thickness(T)	0.50mm ±0.05mm
Terminal Width(B)	0.10mm Min.
Terminal Spacing(G)	0.30mm Min.
Recommended Land Pattern (PA)	0.30mm to 0.50mm
Recommended Land Pattern (PB)	0.35mm to 0.45mm
Recommended Land Pattern (PC)	0.40mm to 0.60mm

Electrical Characteristics	
Capacitance	560pF ±5%
Rated Voltage	50VDC
Temperature Characteristic	C0G(0±30ppm/°C)
Q (Min.)	1000
Insulation Resistance (Min.)	10000MΩ

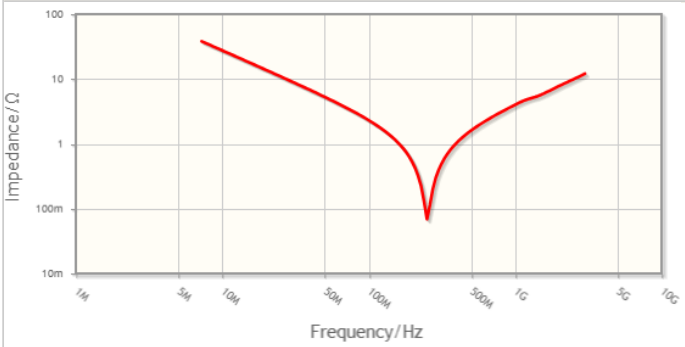
Other	
Operating Temp. Range	-55 to 125°C
Soldering Method	Reflow
AEC-Q200	YES
Packing	Punched (Paper)Taping [180mm Reel]
Package Quantity	10000pcs

! Images are for reference only and show exemplary products.
! This PDF document was created based on the data listed on the TDK Corporation website.
! All specifications are subject to change without notice.

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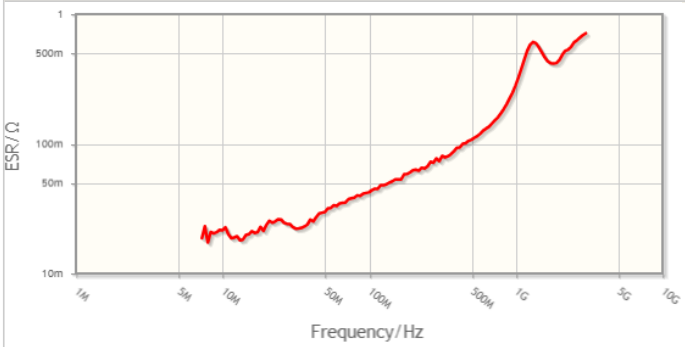
Characteristic Graphs(This is reference data, and does not guarantee the products characteristics.)

Impedance



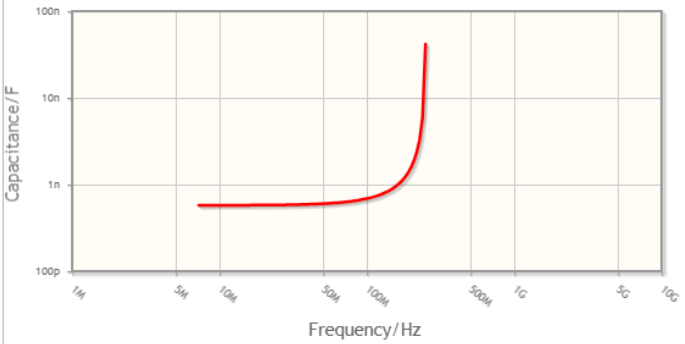
CGA2B2C0G1H561J050BA

ESR



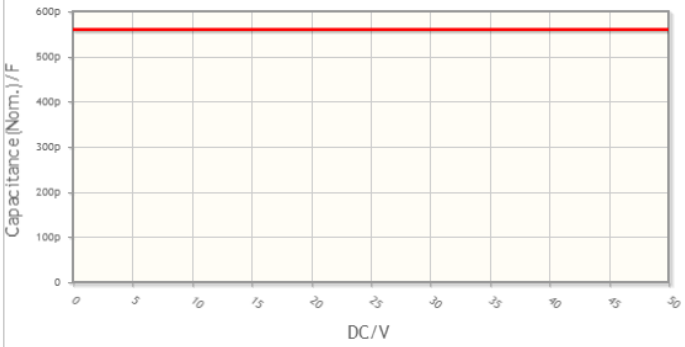
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Capacitance



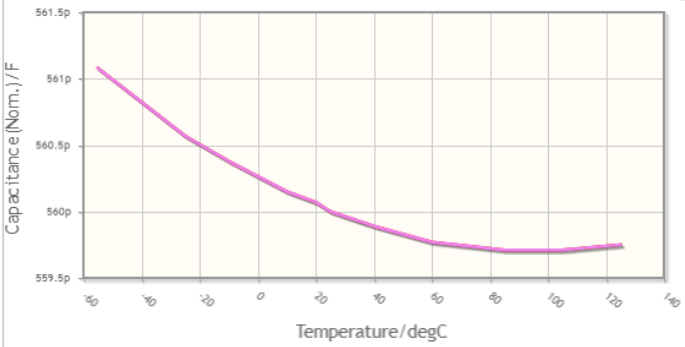
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DC Bias Characteristic



CGA2B2C0G1H561J050BA

Temperature Characteristic



CGA2B2C0G1H561J050BA(No Bias)

CGA2B2C0G1H561J050BA(DC Bias = 25V)

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Associated Images

Land Pattern (Terminal Connection)



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