

4A, 200V - 1000V Standard Surface Mount Rectifier

FEATURES

- AEC-Q101 qualified
- Glass passivated chip junction
- High surge current capability
- Ideal for automated placement
- Wettable flank
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

KEY PARAMETERS		
PARAMETER	VALUE	UNIT
I_F	4	A
V_{RRM}	200 - 1000	V
I_{FSM}	120	A
$T_{J\ MAX}$	150	°C
Package	TO-277A (SMPC4.6U)	
Configuration	Single die	

APPLICATIONS

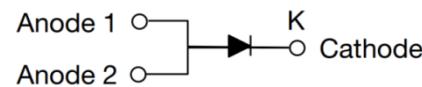
- DC to DC converter
- Automotive application
- Car lighting
- Snubber

MECHANICAL DATA

- Case: TO-277A (SMPC4.6U)
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.107g (approximately)



TO-277A (SMPC4.6U)



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TUAS 4DH	TUAS 4GH	TUAS 4JH	TUAS 4KH	TUAS 4MH	UNIT
Marking code on the device		AS4D	AS4G	AS4J	AS4K	AS4M	
Repetitive peak reverse voltage	V_{RRM}	200	400	600	800	1000	V
Reverse voltage, total rms value	$V_{R(RMS)}$	140	280	420	560	700	V
Forward current	I_F			4			A
Surge peak forward current single half sine-wave superimposed on rated load	$t = 8.3\text{ms}$	I_{FSM}	120				A
	$t = 1.0\text{ms}$		280				
Junction temperature	T_J			-55 to +150			°C
Storage temperature	T_{STG}			-55 to +150			°C

THERMAL PERFORMANCE

PARAMETER	SYMBOL	TYP	UNIT
Junction-to-lead thermal resistance	$R_{\Theta JL}$	5.2	°C/W
Junction-to-ambient thermal resistance	$R_{\Theta JA}$	46	°C/W
Junction-to-case thermal resistance	$R_{\Theta JC}$	8.6	°C/W

Thermal Performance Note: Units mounted on PCB (16mm x 16mm Cu pad test board)

ELECTRICAL SPECIFICATIONS ($T_A = 25^\circ C$ unless otherwise noted)

PARAMETER	CONDITIONS	SYMBOL	TYP	MAX	UNIT
Forward voltage ⁽¹⁾	$I_F = 2A, T_J = 25^\circ C$	V_F	0.89	-	V
	$I_F = 4A, T_J = 25^\circ C$		0.95	1.10	V
	$I_F = 2A, T_J = 125^\circ C$		0.78	-	V
	$I_F = 4A, T_J = 125^\circ C$		0.85	-	V
Reverse current @ rated V_R ⁽²⁾	$T_J = 25^\circ C$	I_R	-	5	μA
	$T_J = 125^\circ C$		10	-	μA
Junction capacitance	TUAS4DH	C_J	33	-	pF
	TUAS4GH TUAS4JH TUAS4KH TUAS4MH		28	-	

Notes:

1. Pulse test with PW = 0.3ms
2. Pulse test with PW = 30ms

ORDERING INFORMATION

ORDERING CODE ⁽¹⁾	PACKAGE	PACKING
TUAS4xH	TO-277A (SMPC4.6U)	6,000 / Tape & Reel

Notes:

1. "x" define voltage from 200V(TUAS4DH) to 1000V(TUAS4MH)

CHARACTERISTICS CURVES

($T_A = 25^\circ\text{C}$ unless otherwise noted)

Fig.1 Forward Current Derating Curve

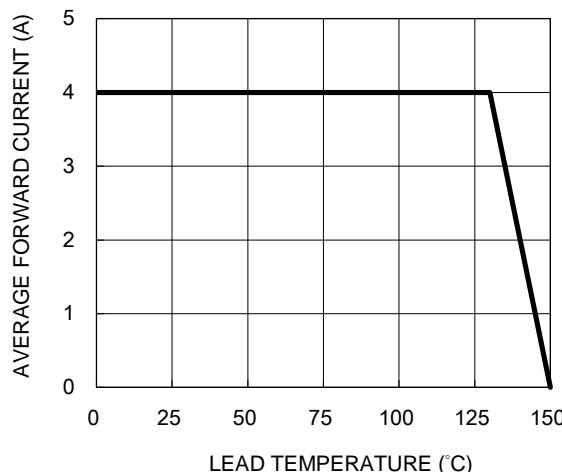


Fig.2 Typical Junction Capacitance

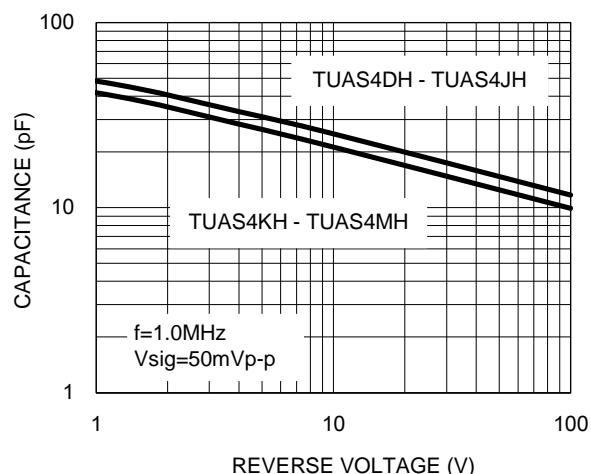


Fig.3 Typical Reverse Characteristics

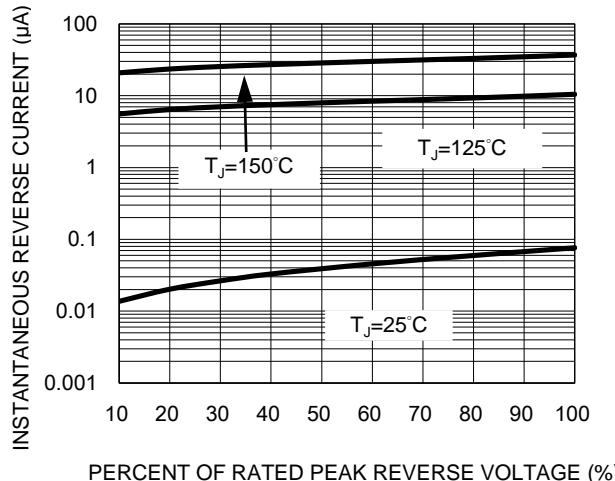


Fig.4 Typical Forward Characteristics

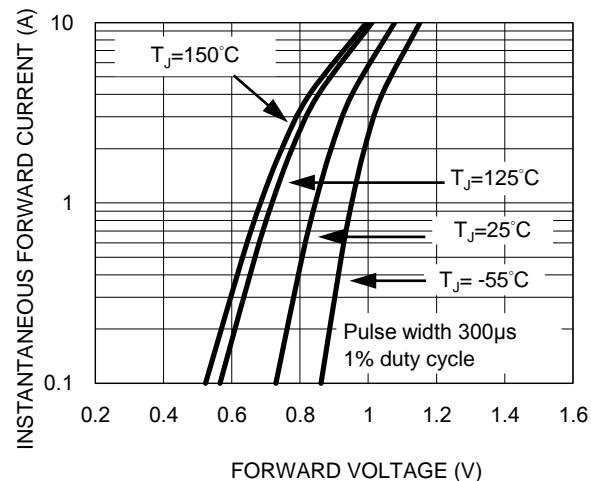
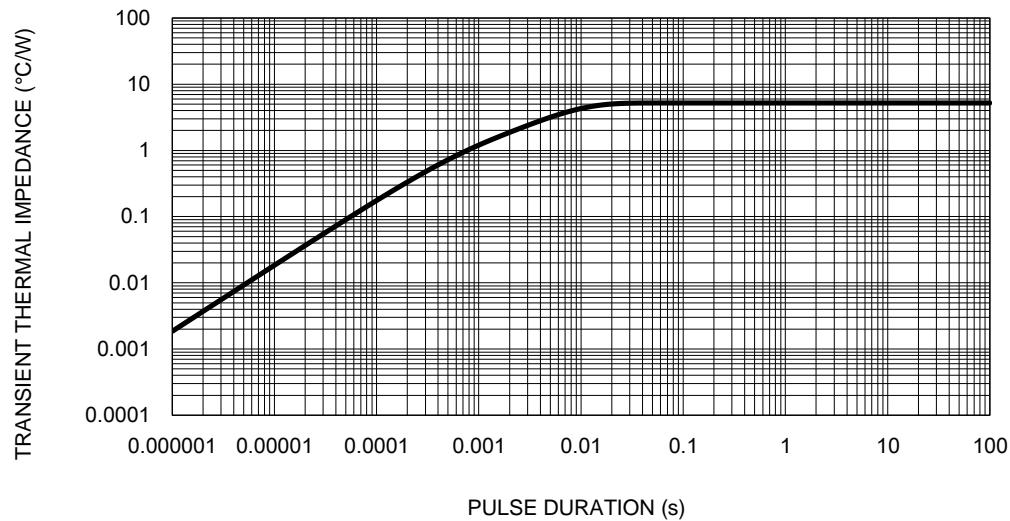
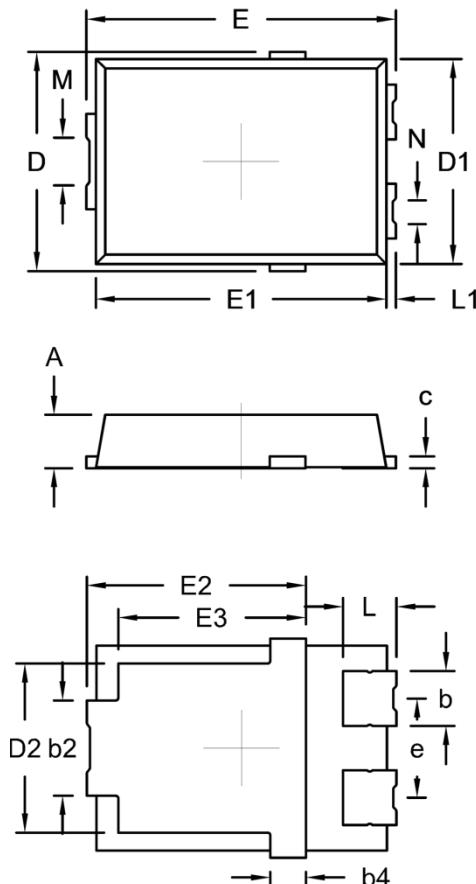


Fig.5 Typical Transient Thermal Impedance



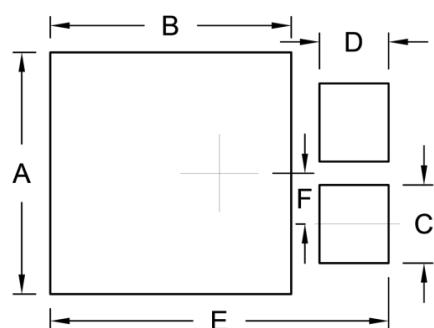
PACKAGE OUTLINE DIMENSIONS

TO-277A (SMPC4.6U)


DIM.	Unit (mm)		Unit (inch)	
	Min.	Max.	Min.	Max.
A	1.00	1.20	0.039	0.047
b	1.05	1.35	0.041	0.053
b2	1.90	2.20	0.075	0.087
b4	0.75 (NOM.)		0.030 (NOM.)	
c	0.15	0.40	0.006	0.016
D	4.45	4.75	0.175	0.187
D1	4.25	4.35	0.167	0.171
D2	3.40	3.70	0.134	0.146
E	6.35	6.65	0.250	0.262
E1	6.05	6.15	0.238	0.242
E2	4.40	4.80	0.173	0.189
E3	3.94 (NOM.)		0.155 (NOM.)	
e	2.08 (NOM.)		0.082 (NOM.)	
L	0.94	1.24	0.037	0.049
L1	0.05	0.35	0.002	0.014
M	0.65	1.15	0.026	0.045
N	0.25	0.75	0.010	0.030

Package body size D1 and E1 do not include mold flash
Mold flash shall not exceed 0.1mm per side

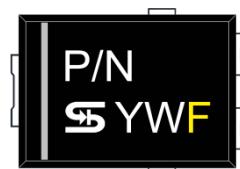
SUGGESTED PAD LAYOUT



Notes:

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.

MARKING DIAGRAM



P/N = Marking Code
 YW = Date Code
 F = Factory Code

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