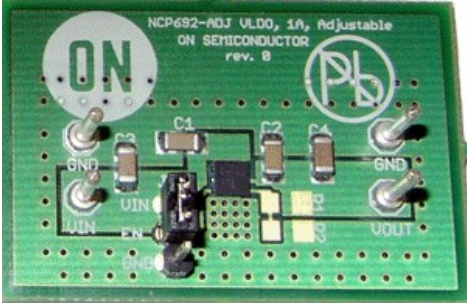


NCP692MNADT2GEVB: CMOS LDO Adjustable Evaluation Board

The NCP692 Adjustable CMOS LDO family provides 1 A of output current with enhanced ESD in fixed output voltage options from 1.25 V to 5.0 V. These devices are designed for space constrained and portable battery powered applications and offer additional features such as low Dropout Voltage, high Power Supply Rejection Ratio (PSRR), low Quiescent and Ground Current consumption, low Noise operation, Short Circuit and Thermal Protection. NCP692 Adjustable LDO is designed to be used with low cost ceramic capacitors and the minimum value of 1uF output capacitance is required. The NCP692 device is equipped with Active High Enable pin, Active Output Discharge, Current Limit and Thermal Shutdown Protection. Finally the Surface Mount DFN3x3 package with Expose Pad allows saving PCB space and effectively dissipating heat through the PCB copper area. This demonstration board operates from a dc input voltage VIN < 6V and produces the output voltage set by the external feedback resistors. External waveform generator could be connected to the EN (Enable) pin in order to verify the ON/OFF operation.



Evaluation/Development Tool Information					
Product	Status	Compliance	Short Description	Parts Used	Action
NCP692MNADT2GEVB	Active	Pb-free	CMOS LDO Adjustable Evaluation Board	NCP692MNADJT2G	Contact Local Sales Office Inventory

Technical Documents			
Type	Document Title	Document ID/Size	Rev
Eval Board: BOM	NCP692MNADT2GEVB Bill of Materials ROHS Compliant	NCP692MNADT2GEVB_BOM_ROHS.REV0.PDF - 103.0 KB	0
Eval Board: Gerber	NCP692MNADT2GEVB Gerber Layout Files (Zip Format)	NCP692MNADT2GEVB_GERBER.ZIP - 19.0 KB	0
Eval Board: Schematic	NCP692MNADT2GEVB Schematic	NCP692MNADT2GEVB_SCHEMATIC.PDF - 68.0 KB	0
Eval Board: Test Procedure	NCP692MNADT2GEVB Test Procedure	NCP692MNADT2GEVB_TEST_PROCEDURE.PDF - 137.0 KB	0

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