BFR92AW

NPN 5 GHz wideband transistor

Rev. 03 — 12 March 2008

Product data sheet

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NXP Semiconductors



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FEATURES

- High power gain
- Gold metallization ensures excellent reliability
- SOT323 (S-mini) package.

APPLICATIONS

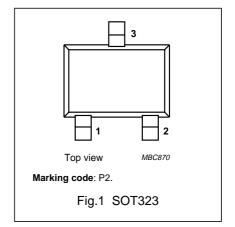
It is designed for use in RF amplifiers, mixers and oscillators with signal frequencies up to 1 GHz.

DESCRIPTION

Silicon NPN transistor encapsulated in a plastic SOT323 (S-mini) package. The BFR92AW uses the same crystal as the SOT23 version, BFR92A.

PINNING

PIN	DESCRIPTION	
1	base	
2	emitter	
3	collector	



QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	_	20	V
V _{CEO}	collector-emitter voltage	open base	_	_	15	V
I _C	collector current (DC)		_	_	25	mA
P _{tot}	total power dissipation	up to T _s = 93 °C; note 1	_	_	300	mW
h _{FE}	current gain	I _C = 15 mA; V _{CE} = 10 V	65	90	135	
C _{re}	feedback capacitance	I _C = 0; V _{CE} = 10 V; f = 1 MHz; T _{amb} = 25 °C	_	0.35	_	pF
f _T	transition frequency	I _C = 15 mA; V _{CE} = 10 V; f = 500 MHz	3.5	5	_	GHz
G _{UM}	maximum unilateral power gain	$I_C = 15 \text{ mA}; V_{CE} = 10 \text{ V}; f = 1 \text{ GHz};$ $T_{amb} = 25 \text{ °C}$	_	14	_	dB
		I_C = 15 mA; V_{CE} = 10 V; f = 2 GHz; T_{amb} = 25 °C	_	8	_	dB
F	noise figure	I_C = 5 mA; V_{CE} = 10 V; f = 1 GHz; $\Gamma_s = \Gamma_{opt}$	_	2	_	dB
Tj	junction temperature		_	_	150	°C

Note

1. T_s is the temperature at the soldering point of the collector pin.

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LIMITING VALUES

In accordance with the Absolute Maximum System (IEC 134).

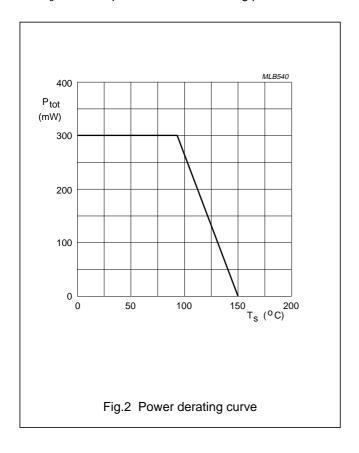
SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	20	V
V _{CEO}	collector-emitter voltage	open base	_	15	V
V _{EBO}	emitter-base voltage	open collector	_	2	V
I _C	collector current (DC)		_	25	mA
P _{tot}	total power dissipation	up to T _s = 93 °C; see Fig.2; note 1	_	300	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-s}	thermal resistance from junction to soldering point	up to T _s = 93 °C; note 1	190	K/W

Note to the Limiting values and Thermal characteristics

1. T_s is the temperature at the soldering point of the collector pin.



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CHARACTERISTICS

 T_j = 25 °C (unless otherwise specified).

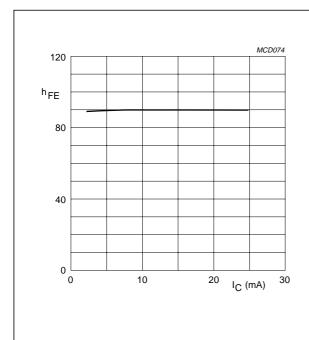
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector leakage current	I _E = 0; V _{CB} = 10 V	_	_	50	nA
h _{FE}	DC current gain	I _C = 15 mA; V _{CE} = 10 V	65	90	135	
C _c	collector capacitance	I _E = i _e = 0; V _{CB} = 10 V; f = 1 MHz	_	0.6	_	pF
C _e	emitter capacitance	$I_C = i_c = 0$; $V_{EB} = 0.5 \text{ V}$; $f = 1 \text{ MHz}$	_	0.9	_	pF
C _{re}	feedback capacitance	I _C = 0; V _{CE} = 10 V; f = 1 MHz	_	0.35	_	pF
f _T	transition frequency	I _C = 15 mA; V _{CE} = 10 V; f = 500 MHz	3.5	5	_	GHz
G _{UM}	maximum unilateral power gain; note 1	I _C = 15 mA; V _{CE} = 10 V; f = 1 GHz; T _{amb} = 25 °C	_	14	_	dB
		I _C = 15 mA; V _{CE} = 10 V; f = 2 GHz; T _{amb} = 25 °C	_	8	_	dB
F	noise figure	$I_C = 5 \text{ mA}; V_{CE} = 10 \text{ V};$ $f = 1 \text{ GHz}; \Gamma_S = \Gamma_{\text{opt}}$	_	2	_	dB
		$I_C = 5 \text{ mA}; V_{CE} = 10 \text{ V};$ $f = 2 \text{ GHz}; \Gamma_S = \Gamma_{\text{opt}}$	_	3	_	dB

Note

1. G_{UM} is the maximum unilateral power gain, assuming s_{12} is zero and $G_{UM} = 10 \log \frac{|s_{21}|^2}{(1-|s_{11}|^2)(1-|s_{22}|^2)}$ dB.

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 $V_{CE} = 10 \text{ V}.$

Fig.3 DC current gain as a function of collector current; typical values.

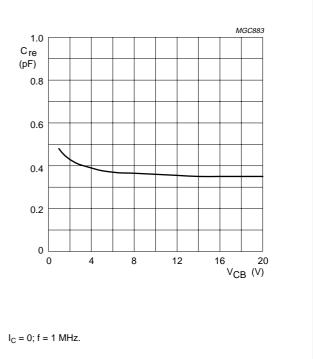
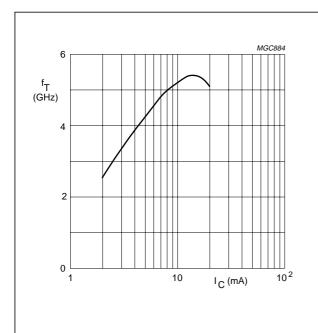


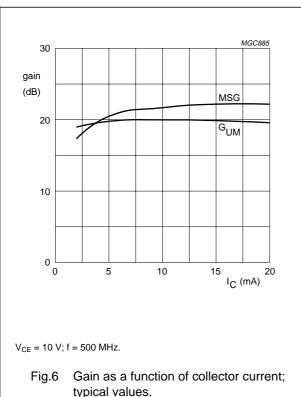
Fig.4 Feedback capacitance as a function of collector-base voltage; typical values.

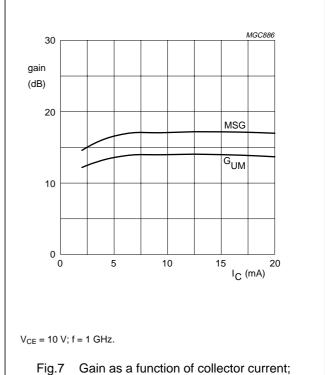


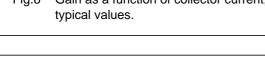
 V_{CE} = 5 V; f = 500 MHz; T_{amb} = 25 $^{\circ}C.$

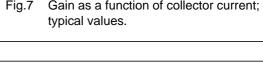
Fig.5 Transition frequency as a function of collector current; typical values.

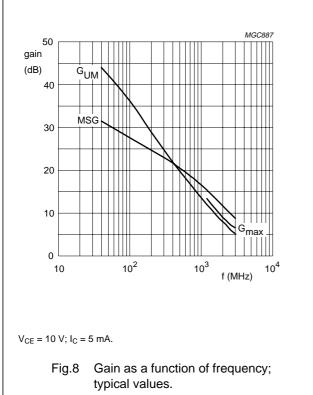
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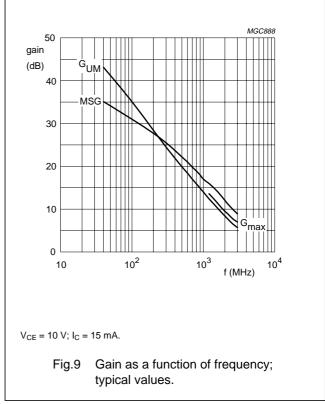




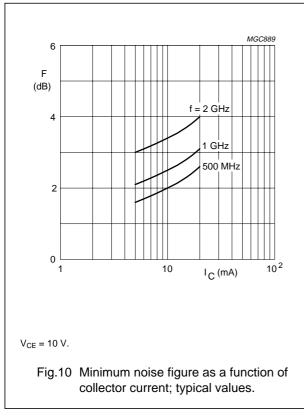


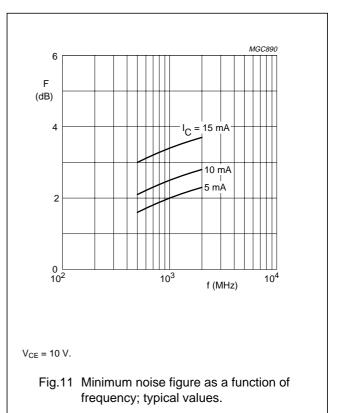


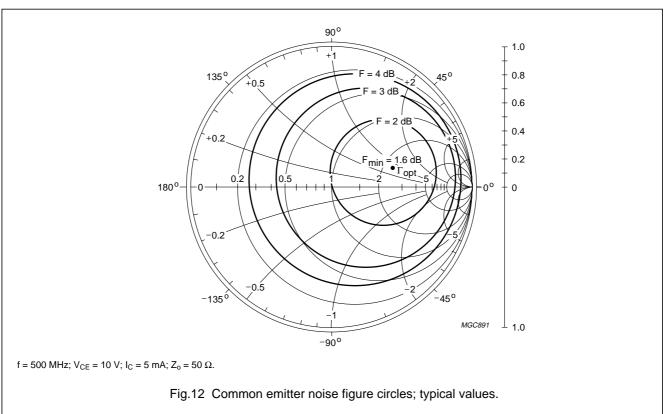




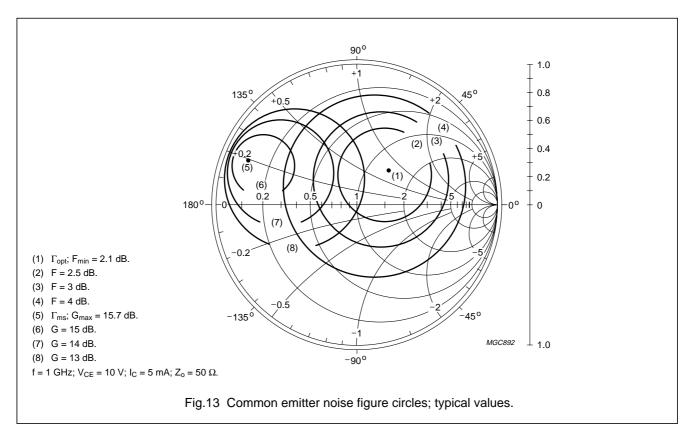
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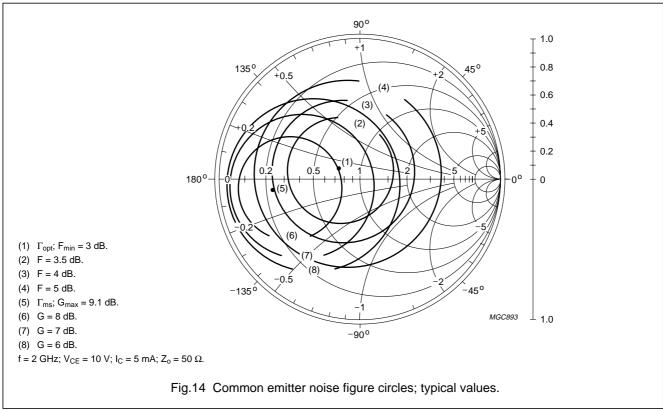




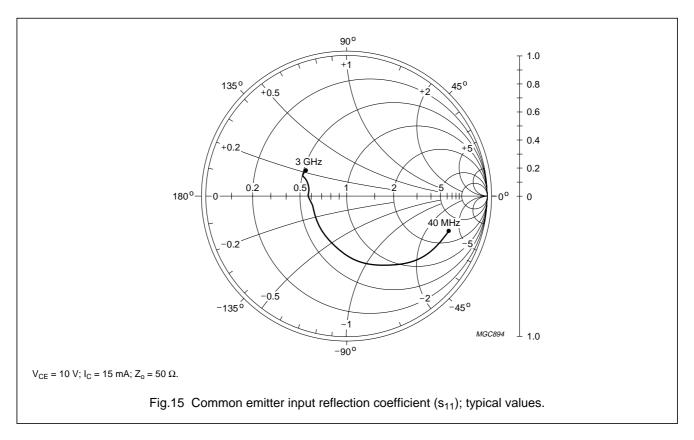


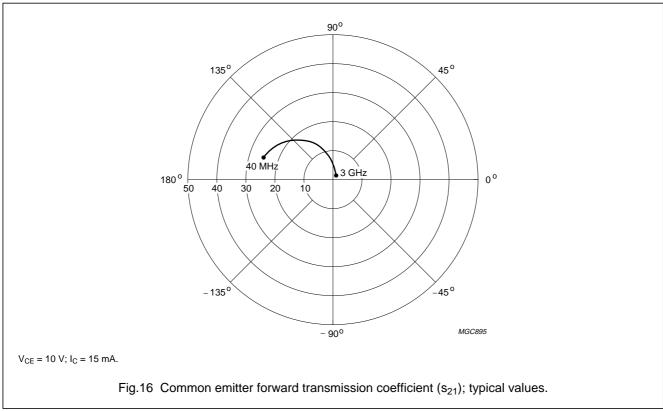
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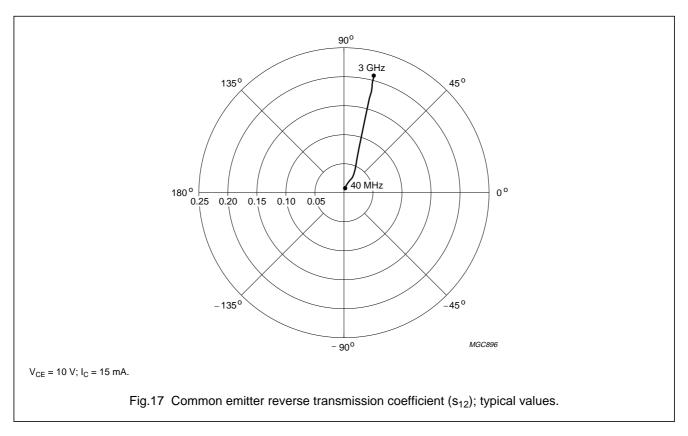


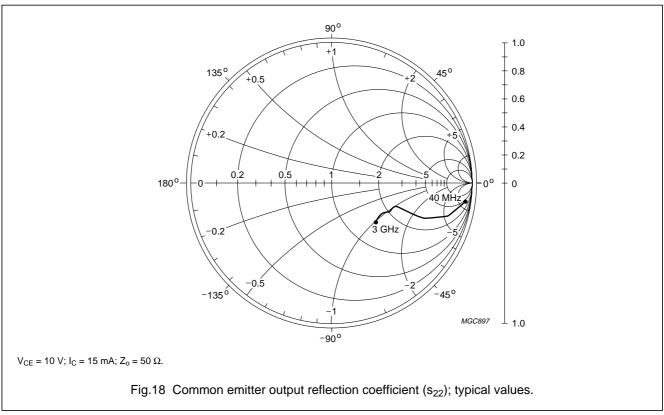
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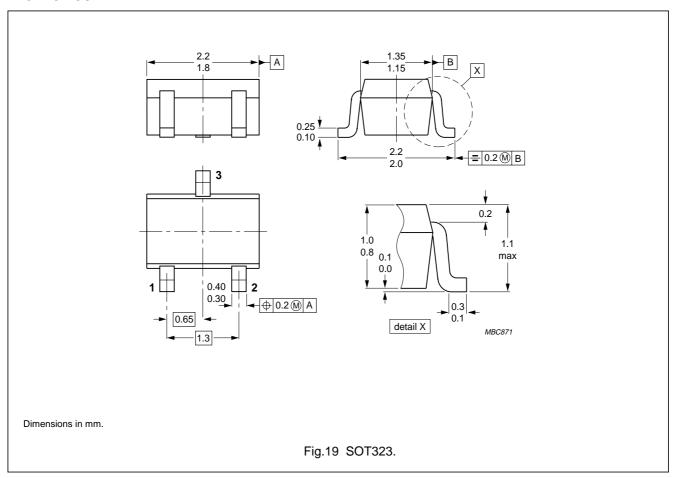




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PACKAGE OUTLINE



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Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Revision history

Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BFR92AW_N_3	20080312	Product data sheet	-	BFR92AW_2
Modifications:	 Quick refere 	ence data and Characteristic	cs Table; DC current gai	n value changed
BFR92AW_2	19950918	Product specification	-	BFR92AW_1
BFR92AW_1	19921001	-	-	-

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