

W5500-EVB-Pico-PoE

Overview

W5500-EVB-Pico-PoE is a microcontroller evaluation board based on the Raspberry Pi RP2040 and fully hardwired TCP/IP controller W5500 – and basically works the same as Raspberry Pi Pico board but with additional Ethernet via W5500.

- [Raspberry Pi Pico Clone](#)
- [Ethernet \(W5500 Hardwired TCP/IP CHIP\)](#)

Features

- RP2040 microcontroller with 2MByte Flash
 - Dual-core cortex M0+ at up to 133MHz
 - 264kByte multi-bank high performance SRAM
 - External Quad-SPI Flash with eExecute In Place (XIP)
 - High performance full-crossbar bus fabric
 - 30 multi-function General Purpose IO (4 can be used for ADC)
 - 1.8-3.3V IO Voltage (NOTE: Pico IO voltage is fixed at 3.3V)
 - 12-bit 500kps Analogue to Digital Converter (ADC)
 - Various digital peripherals
 - 2 × UART, 2 × I2C, 2 × SPI, 16 × PWM channels
 - 1 × Timer with 4 alarms, 1 × Real Time Counter
 - 2 × Programmable IO (PIO) blocks, 8 state machines total
 - Flexible, user-programmable high-speed IO
 - Can emulate interfaces such as SD Card and VGA
- Includes W5500
 - Supports Hardwired Internet Protocols: TCP, UDP, ICMP, IPv4, ARP, IGMP, PPPoE
 - Supports 8 Independent Hardware SOCKETS simultaneously
 - Internal 32 Kbytes Memory for TX/ RX Buffers
 - Supports High Speed Serial Peripheral Interface(SPI MODE 0, 3)
- USB TYPE C port for power and data (and for reprogramming the Flash)
- 40 pin 21x51 "DIP" style 1mm thick PCB with 0.1" through-hole pins also with edge castellations
- 3-pin ARM Serial Wire Debug (SWD) port
- 10 / 100 Ethernet PHY embedded
- Supports Auto Negotiation
 - Full / Half Duplex
 - 10 / 100 Based
- Built-in RJ45
- Built-in LDO

Hardware Specification

Pin-out

W5500-EVB-Pico-PoE pinout is directly connected to the GPIO of RP2040 as shown in the picture above. It has the same pinout as the Raspberry Pi Pico board. However, GPIO16, GPIO17, GPIO18, GPIO19, GPIO20, GPIO21 are connected to W5500 inside the board. These pins enable SPI communication with W5500 to use Ethernet function. If you are using the Ethernet function, these pins cannot be used for any other purpose.

The RP2040 GPIO used inside W5500-EVB-Pico-PoE is as follows.

I/O	Pin Name	Description
I	GPIO16	Connected to MISO on W5500
O	GPIO17	Connected to CSn on W5500
O	GPIO18	Connected to SCLK on W5500
O	GPIO19	Connected to MOSI on W5500
O	GPIO20	Connected to RSTn on W5500
I	GPIO21	Connected to INTn on W5500
I	GPIO24	VBUS sense - high if VBUS is present, else low
O	GPIO25	Connected to user LED
I	GPIO29	Used in ADC mode (ADC3) to measure VSYS/3

Apart from GPIO and ground pins, there are 7 other pins on the main 40-pin interface:

Pin No.	Pin Name	Description
PIN40	VBUS	Micro-USB input voltage, connected to micro-USB port pin 1. Nominally 5V.
PIN39	VSYS	Main system input voltage, which can vary in the allowed range 4.3V to 5.5V, and is used by the on-board LDO to generate the 3.3V .
PIN37	3V3_EN	Connects to the on-board LDO enable pin. To disable the 3.3V (which also de-powers the RP2040 and W5500), short this pin low.
PIN36	3V3	Main 3.3V supply to RP2040 and W5500, generated by the on-board LDO.
PIN35	ADC_VREF	ADC power supply (and reference) voltage, and is generated on W5500-EVB-Pico-PoE by filtering the 3.3V supply.
PIN33	AGND	Ground reference for GPIO26-29.
PIN30	RUN	RP2040 enable pin. To reset RP2040, short this pin low.

Operation Condition

Item	Description
Operation Temperature MAX	85C (including self-heating)
Operation Temperature MIN	-20C
VBUS	DC 5V (+/- 10%)
VSYS Min	DC 4.3V
VSYS Max	DC 5.5V

Recommended maximum ambient temperature of operation is 70C.

Technical Reference

RP2040 Datasheet

- [Download](#)

W5500 Datasheet

- [W5500 overview](#)

Schematic

Schematic & Part list & Gerber File

- [Go to Github](#)

3D File

- [Download](#)

Dimension (Unit : mil)

Firmware Example

Please refer to below links to find firmware example.

- C/C++
 - [Ethernet Examples](#)
 - [AWS Examples](#)
 - [Azure Examples](#)
 - [LwIP Examples](#)
 - [FreeRTOS Examples](#)
 - [CircuitPython Examples](#)
 - [MicroPython Examples](#)

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