To our customers,

Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

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Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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GaAs MULTI-CHIP MODULE
MC-7832

870 MHz CATV 22 dB PUSH-PULL AMPLIFIER

DESCRIPTION

The MC-7832 is a GaAs Multi-chip Module designed for use in CATV applications up to 870 MHz. This unit has low distortion, low noise figure and return loss across the entire frequency band.

Reliability and performance uniformity are assured by our stringent quality and control procedures.

FEATURES

- Low distortion
- High linear gain G_L = 22.0 dB MIN. @f = 870 MHz
- Low return loss

ORDERING INFORMATION

Part Number	Order Number	Package	Supplying Form
<R> MC-7832	MC-7832-AZ	7-pin special with heatsink (Pb-Free)	25 pcs MAX./Tray

Remark To order evaluation samples, contact your nearby sales office.
Part number for sample order: MC-7832

ABSOLUTE MAXIMUM RATINGS (T_A = +25°C)

Parameter	Symbol	Ratings	Unit
Supply Voltage	V_{DD}	30	V
Input Voltage Note	V_i	65.0	dBmV
Operating Case Temperature	T_C	–30 to +100	°C
Storage Temperature	T_{stg}	–40 to +100	°C

Note In case of single tone

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Caution Observe precautions when handling because these devices are sensitive to electrostatic discharge.

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Date Published December 2006 NS CP(N)
Printed in Japan

The mark <R> shows major revised points.

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The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

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RECOMMENDED OPERATING CONDITIONS ($Z_S = Z_L = 75\ \Omega$)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Supply Voltage	V_{DD}		23.5	24.0	24.5	V
Input Voltage	V_i	110 channel, Flat	–	21.0	27.5	dBmV
Operating Case Temperature	T_C		–30	+25	+85	°C

ELECTRICAL CHARACTERISTICS ($T_C = 30 \pm 5°C$, $V_{DD} = 24$ V, $Z_S = Z_L = 75\ \Omega$)

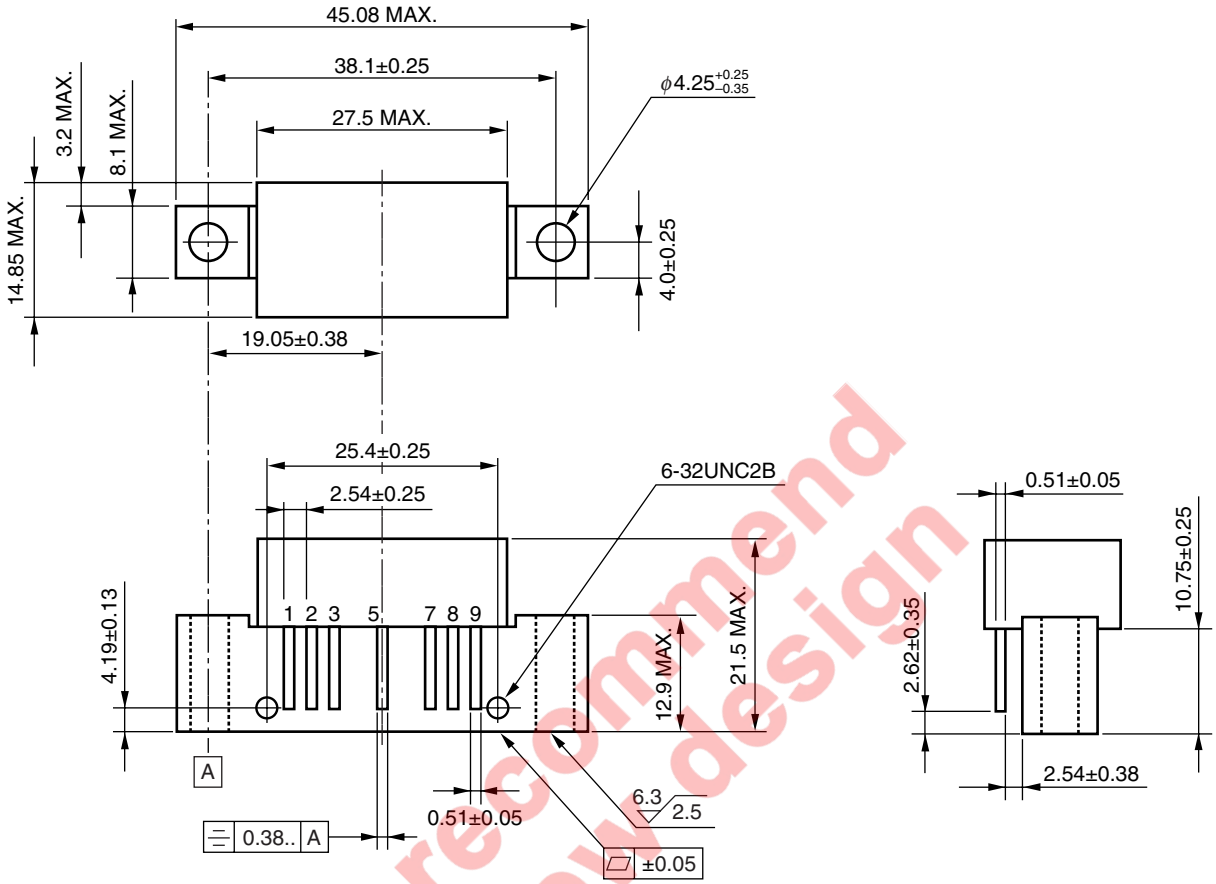
Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Linear Gain	G_L	f = 870 MHz	22.0	–	23.0	dB
Gain Slope	G_{Slope}	f = 40 to 870 MHz	0.6	–	1.4	dB
Gain Flatness	$G_{Flatness}$	f = 40 to 870 MHz, Peak to valley	–	–	0.7	dB
Noise Figure 1	NF1	f = 50 MHz	–	–	6.0	dB
Noise Figure 2	NF2	f = 870 MHz	–	–	6.5	dB
Operating Current	I_{DD}	P_{in} = None	180	–	240	mA
Composite Triple Beat	CTB	110 channel,	–	–	–57	dBc
Cross Modulation	XM	V_O = 44 dBmV, Flat	–	–	–50	dBc
Composite 2nd Order Beat	CSO		–	–	–57	dBc
Input / Output Return Loss 1	RL1	f = 40 to 160 MHz	20	–	–	dB
Input / Output Return Loss 2	RL2	f = 160 to 320 MHz	19	–	–	dB
Input / Output Return Loss 3	RL3	f = 320 to 640 MHz	17.5	–	–	dB
Input / Output Return Loss 4	RL4	f = 640 to 870 MHz	16	–	–	dB

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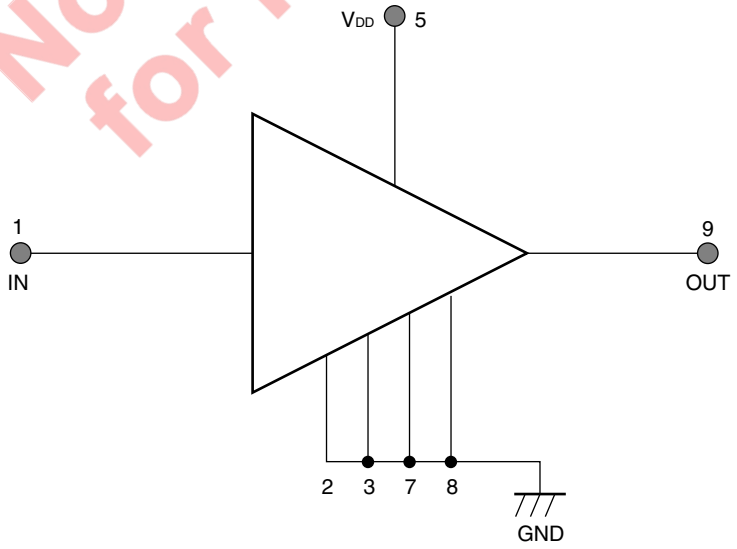
PACKAGE DIMENSIONS

7-PIN SPECIAL WITH HEATSINK (UNIT: mm)



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PIN CONNECTION



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NOTES ON CORRECT USE

(1) The space between PC board and root of the lead should be kept more than 1 mm to prevent undesired stress to the lead and also should be kept less than 4 mm to prevent undesired parasitic inductance.
Recommended that space is 2.0 to 3.0 mm typical.

(2) Recommended torque strength of the screw is 59 to 78 Ncm.

(3) Form the ground pattern as wide as possible to minimize ground impedance.
(to prevent undesired oscillation)
All the ground pins must be connected together with wide ground pattern to decrease impedance difference.

RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions	Recommended Condition Symbol
Partial Heating	Peak temperature (pin temperature) : 350°C or below[Note] Soldering time (per pin of device) : 3 seconds or less	–

Note The point of pin part heating must be kept more than 1.2 mm distance from the root of lead.

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