

74VHC126-Q100;

74VHCT126-Q100

Quad buffer/line driver; 3-state

Rev. 3 — 1 May 2024

Product data sheet

1. General description

The 74VHC126-Q100; 74VHCT126-Q100 are high-speed Si-gate CMOS devices and are pin compatible with Low-power Schottky TTL (LSTTL). They are specified in compliance with JEDEC standard No. 7-A.

The 74VHC126-Q100; 74VHCT126-Q100 provide four non-inverting buffer/line drivers with 3-state outputs. The output enable input (nOE) controls the 3-state outputs (nY). A LOW-level at pin nOE causes the outputs to assume a high-impedance OFF-state.

This product has been qualified to the Automotive Electronics Council (AEC) standard Q100 (Grade 1) and is suitable for use in automotive applications.

2. Features and benefits

- Automotive product qualification in accordance with AEC-Q100 (Grade 1)
 - Specified from -40 °C to +85 °C and from -40 °C to +125 °C
- Balanced propagation delays
- All inputs have Schmitt-trigger action
- Inputs accept voltages higher than V_{CC}
- Input levels:
 - The 74VHC126-Q100 operates with CMOS input level
 - The 74VHCT126-Q100 operates with TTL input level
- ESD protection:
 - HBM: ANSI/ESDA/JEDEC JS-001 class 2 exceeds 2000 V
 - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V
- Multiple package options
- DHVQFN package with Side-Wettable Flanks enabling Automatic Optical Inspection (AOI) of solder joints

3. Ordering information

Table 1. Ordering information

| Type number | Package | | | |
|---|-------------------|----------|--|--------------------------|
| | Temperature range | Name | Description | Version |
| 74VHC126D-Q100 74VHCT126D-Q100 | -40 °C to +125 °C | SO14 | plastic small outline package; 14 leads; body width 3.9 mm | SOT108-1 |
| 74VHC126PW-Q100 74VHCT126PW-Q100 | -40 °C to +125 °C | TSSOP14 | plastic thin shrink small outline package; 14 leads; body width 4.4 mm | SOT402-1 |
| 74VHC126BQ-Q100 74VHCT126BQ-Q100 | -40 °C to +125 °C | DHVQFN14 | plastic dual in-line compatible thermal enhanced very thin quad flat package; no leads; 14 terminals; body 2.5 × 3 × 0.85 mm | SOT762-1 |

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4. Functional diagram

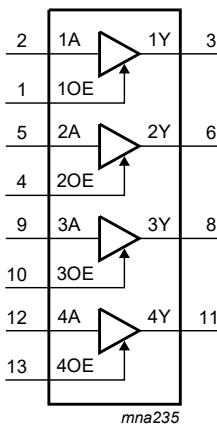


Fig. 1. Functional diagram

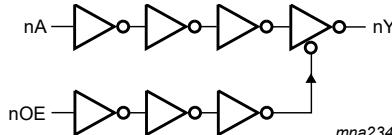


Fig. 2. Logic symbol

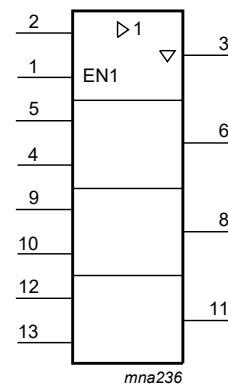
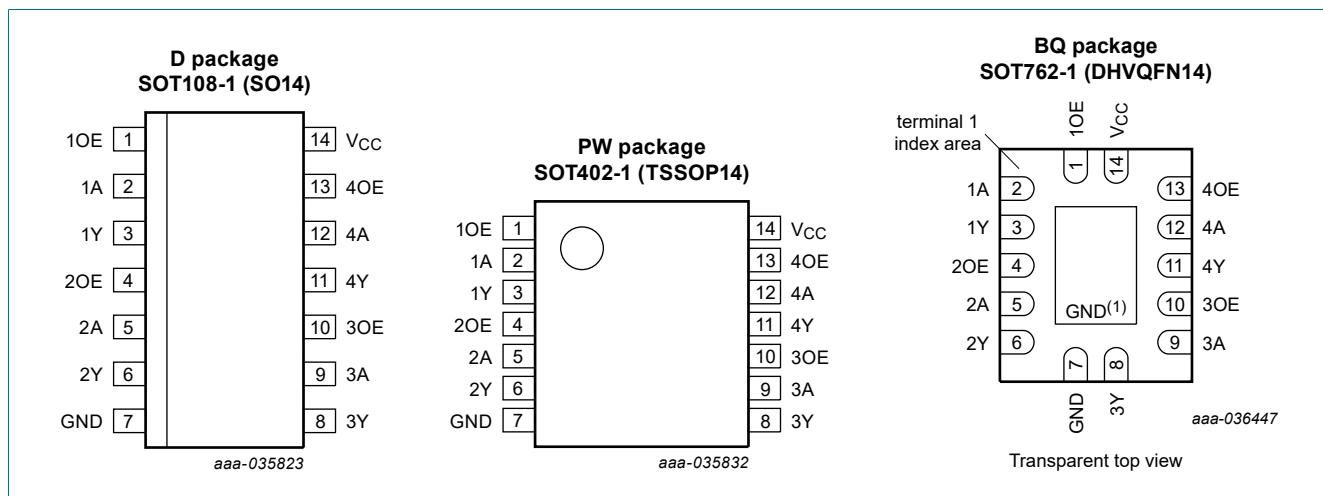


Fig. 3. IEC logic symbol

5. Pinning information

5.1. Pinning



5.2. Pin description

Table 2. Pin description

| Symbol | Pin | Description |
|--------------------|--------------|-----------------------------------|
| 1OE, 2OE, 3OE, 4OE | 1, 4, 10, 13 | output enable input (active HIGH) |
| 1A, 2A, 3A, 4A | 2, 5, 9, 12 | data input |
| 1Y, 2Y, 3Y, 4Y | 3, 6, 8, 11 | data output |
| GND | 7 | ground (0 V) |
| V _{CC} | 14 | supply voltage |

6. Functional description

Table 3. Function table

H = HIGH voltage level; L = LOW voltage level; X = don't care; Z = high-impedance OFF-state.

| Control | Input | Output |
|---------|-------|--------|
| nOE | nA | nY |
| H | L | L |
| H | H | H |
| L | X | Z |

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

| Symbol | Parameter | Conditions | Min | Max | Unit |
|------------------|-------------------------|---|------|------|--------|
| V _{CC} | supply voltage | | -0.5 | +7.0 | V |
| V _I | input voltage | | -0.5 | +7.0 | V |
| I _{IK} | input clamping current | V _I < -0.5 V | [1] | -20 | - mA |
| I _{OK} | output clamping current | V _O < -0.5 V or V _O > V _{CC} + 0.5 V | [1] | -20 | +20 mA |
| I _O | output current | V _O = -0.5 V to (V _{CC} + 0.5 V) | -25 | +25 | mA |
| I _{CC} | supply current | | - | +75 | mA |
| I _{GND} | ground current | | -75 | - | mA |
| T _{stg} | storage temperature | | -65 | +150 | °C |
| P _{tot} | total power dissipation | T _{amb} = -40 °C to +125 °C | [2] | - | 500 mW |

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SOT108-1 (SO14) package: P_{tot} derates linearly with 10.1 mW/K above 100 °C.

For SOT402-1 (TSSOP14) package: P_{tot} derates linearly with 7.3 mW/K above 81 °C.

For SOT762-1 (DHVQFN14) package: P_{tot} derates linearly with 9.6 mW/K above 98 °C.

8. Recommended operating conditions

Table 5. Operating conditions

| Symbol | Parameter | Conditions | 74VHC126-Q100 | | | 74VHCT126-Q100 | | | Unit |
|------------------|-------------------------------------|----------------------------------|---------------|-----|-----------------|----------------|-----|-----------------|------|
| | | | Min | Typ | Max | Min | Typ | Max | |
| V _{CC} | supply voltage | | 2.0 | 5.0 | 5.5 | 4.5 | 5.0 | 5.5 | V |
| V _I | input voltage | | 0 | - | 5.5 | 0 | - | 5.5 | V |
| V _O | output voltage | | 0 | - | V _{CC} | 0 | - | V _{CC} | V |
| T _{amb} | ambient temperature | | -40 | +25 | +125 | -40 | +25 | +125 | °C |
| Δt/ΔV | input transition rise and fall rate | V _{CC} = 3.0 V to 3.6 V | - | - | 100 | - | - | - | ns/V |
| | | V _{CC} = 4.5 V to 5.5 V | - | - | 20 | - | - | 20 | ns/V |

9. Static characteristics

Table 6. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

| Symbol | Parameter | Conditions | 25 °C | | | -40 °C to +85 °C | | -40 °C to +125 °C | | Unit |
|-----------------------|---------------------------|--|-------|-----|-------|------------------|------|-------------------|-------|------|
| | | | Min | Typ | Max | Min | Max | Min | Max | |
| 74VHC126-Q100 | | | | | | | | | | |
| V _{IH} | HIGH-level input voltage | V _{CC} = 2.0 V | 1.5 | - | - | 1.5 | - | 1.5 | - | V |
| | | V _{CC} = 3.0 V | 2.1 | - | - | 2.1 | - | 2.1 | - | V |
| | | V _{CC} = 5.5 V | 3.85 | - | - | 3.85 | - | 3.85 | - | V |
| V _{IL} | LOW-level input voltage | V _{CC} = 2.0 V | - | - | 0.5 | - | 0.5 | - | 0.5 | V |
| | | V _{CC} = 3.0 V | - | - | 0.9 | - | 0.9 | - | 0.9 | V |
| | | V _{CC} = 5.5 V | - | - | 1.65 | - | 1.65 | - | 1.65 | V |
| V _{OH} | HIGH-level output voltage | V _I = V _{IH} or V _{IL} | | | | | | | | |
| | | I _O = -50 µA; V _{CC} = 2.0 V | 1.9 | 2.0 | - | 1.9 | - | 1.9 | - | V |
| | | I _O = -50 µA; V _{CC} = 3.0 V | 2.9 | 3.0 | - | 2.9 | - | 2.9 | - | V |
| | | I _O = -50 µA; V _{CC} = 4.5 V | 4.4 | 4.5 | - | 4.4 | - | 4.4 | - | V |
| | | I _O = -4.0 mA; V _{CC} = 3.0 V | 2.58 | - | - | 2.48 | - | 2.40 | - | V |
| | | I _O = -8.0 mA; V _{CC} = 4.5 V | 3.94 | - | - | 3.80 | - | 3.70 | - | V |
| V _{OL} | LOW-level output voltage | V _I = V _{IH} or V _{IL} | | | | | | | | |
| | | I _O = 50 µA; V _{CC} = 2.0 V | - | 0 | 0.1 | - | 0.1 | - | 0.1 | V |
| | | I _O = 50 µA; V _{CC} = 3.0 V | - | 0 | 0.1 | - | 0.1 | - | 0.1 | V |
| | | I _O = 50 µA; V _{CC} = 4.5 V | - | 0 | 0.1 | - | 0.1 | - | 0.1 | V |
| | | I _O = 4.0 mA; V _{CC} = 3.0 V | - | - | 0.36 | - | 0.44 | - | 0.55 | V |
| | | I _O = 8.0 mA; V _{CC} = 4.5 V | - | - | 0.36 | - | 0.44 | - | 0.55 | V |
| I _I | input leakage current | V _I = 5.5 V or GND; V _{CC} = 0 V to 5.5 V | - | - | 0.1 | - | 1.0 | - | 2.0 | µA |
| I _{OZ} | OFF-state output current | V _I = V _{IH} or V _{IL} ; V _O = V _{CC} or GND; V _{CC} = 5.5 V | - | - | ±0.25 | - | ±2.5 | - | ±10.0 | µA |
| I _{CC} | supply current | V _I = V _{CC} or GND; I _O = 0 A; V _{CC} = 5.5 V | - | - | 2.0 | - | 20 | - | 40 | µA |
| C _I | input capacitance | V _I = V _{CC} or GND | - | 3 | 10 | - | 10 | - | 10 | pF |
| C _O | output capacitance | | - | 4 | - | - | - | - | - | pF |
| 74VHCT126-Q100 | | | | | | | | | | |
| V _{IH} | HIGH-level input voltage | V _{CC} = 4.5 V to 5.5 V | 2.0 | - | - | 2.0 | - | 2.0 | - | V |
| V _{IL} | LOW-level input voltage | V _{CC} = 4.5 V to 5.5 V | - | - | 0.8 | - | 0.8 | - | 0.8 | V |
| V _{OH} | HIGH-level output voltage | V _I = V _{IH} or V _{IL} ; V _{CC} = 4.5 V | | | | | | | | |
| | | I _O = -50 µA | 4.4 | 4.5 | - | 4.4 | - | 4.4 | - | V |
| | | I _O = -8.0 mA | 3.94 | - | - | 3.80 | - | 3.70 | - | V |
| V _{OL} | LOW-level output voltage | V _I = V _{IH} or V _{IL} ; V _{CC} = 4.5 V | | | | | | | | |
| | | I _O = 50 µA | - | 0 | 0.1 | - | 0.1 | - | 0.1 | V |
| | | I _O = 8.0 mA | - | - | 0.36 | - | 0.44 | - | 0.55 | V |

| Symbol | Parameter | Conditions | 25 °C | | | -40 °C to +85 °C | | -40 °C to +125 °C | | Unit |
|-----------------|---------------------------|---|-------|-----|-------|------------------|------|-------------------|-------|------|
| | | | Min | Typ | Max | Min | Max | Min | Max | |
| I_I | input leakage current | $V_I = 5.5 \text{ V}$ or GND; $V_{CC} = 0 \text{ V}$ to 5.5 V | - | - | 0.1 | - | 1.0 | - | 2.0 | μA |
| I_{OZ} | OFF-state output current | Per input pin; $V_I = V_{IH}$ or V_{IL} ; $V_O = V_{CC}$ or GND; other inputs at V_{CC} or GND; $I_O = 0 \text{ A}$; $V_{CC} = 5.5 \text{ V}$ | - | - | ±0.25 | - | ±2.5 | - | ±10.0 | μA |
| I_{CC} | supply current | $V_I = V_{CC}$ or GND; $I_O = 0 \text{ A}$; $V_{CC} = 5.5 \text{ V}$ | - | - | 2.0 | - | 20 | - | 40 | μA |
| ΔI_{CC} | additional supply current | per input pin; $V_I = V_{CC} - 2.1 \text{ V}$; other pins at V_{CC} or GND; $I_O = 0 \text{ A}$; $V_{CC} = 4.5 \text{ V}$ to 5.5 V | - | - | 1.35 | - | 1.5 | - | 1.5 | mA |
| C_I | input capacitance | $V_I = V_{CC}$ or GND | - | 3 | 10 | - | 10 | - | 10 | pF |
| C_O | output capacitance | | - | 4 | - | - | - | - | - | pF |

10. Dynamic characteristics

Table 7. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see [Fig. 6](#).

| Symbol | Parameter | Conditions | 25 °C | | | -40 °C to +85 °C | | -40 °C to +125 °C | | Unit |
|----------------------|-------------------------------|--|-------|-------------------------|------|------------------|------|-------------------|------|------|
| | | | Min | Typ [1] | Max | Min | Max | Min | Max | |
| 74VHC126-Q100 | | | | | | | | | | |
| t_{pd} | propagation delay | nA to nY; see Fig. 4 [2] | | | | | | | | |
| | | $V_{CC} = 3.0 \text{ V}$ to 3.6 V ; $C_L = 15 \text{ pF}$ | - | 4.7 | 8.0 | 1.0 | 9.5 | 1.0 | 10.0 | ns |
| | | $V_{CC} = 3.0 \text{ V}$ to 3.6 V ; $C_L = 50 \text{ pF}$ | - | 6.7 | 11.5 | 1.0 | 13.0 | 1.0 | 14.5 | ns |
| | | $V_{CC} = 4.5 \text{ V}$ to 5.5 V ; $C_L = 15 \text{ pF}$ | - | 3.3 | 5.5 | 1.0 | 6.5 | 1.0 | 7.0 | ns |
| | | $V_{CC} = 4.5 \text{ V}$ to 5.5 V ; $C_L = 50 \text{ pF}$ | - | 4.7 | 7.5 | 1.0 | 8.5 | 1.0 | 9.5 | ns |
| t_{en} | enable time | nOE to nY; see Fig. 5 [3] | | | | | | | | |
| | | $V_{CC} = 3.0 \text{ V}$ to 3.6 V ; $C_L = 15 \text{ pF}$ | - | 5.3 | 8.0 | 1.0 | 9.5 | 1.0 | 10.0 | ns |
| | | $V_{CC} = 3.0 \text{ V}$ to 3.6 V ; $C_L = 50 \text{ pF}$ | - | 7.6 | 11.5 | 1.0 | 13.0 | 1.0 | 14.5 | ns |
| | | $V_{CC} = 4.5 \text{ V}$ to 5.5 V ; $C_L = 15 \text{ pF}$ | - | 3.6 | 5.3 | 1.0 | 6.1 | 1.0 | 7.0 | ns |
| | | $V_{CC} = 4.5 \text{ V}$ to 5.5 V ; $C_L = 50 \text{ pF}$ | - | 5.1 | 7.6 | 1.0 | 8.7 | 1.0 | 9.5 | ns |
| t_{dis} | disable time | nOE to nY; see Fig. 5 [4] | | | | | | | | |
| | | $V_{CC} = 3.0 \text{ V}$ to 3.6 V ; $C_L = 15 \text{ pF}$ | - | 6.6 | 9.7 | 1.0 | 11.5 | 1.0 | 12.5 | ns |
| | | $V_{CC} = 3.0 \text{ V}$ to 3.6 V ; $C_L = 50 \text{ pF}$ | - | 9.4 | 13.2 | 1.0 | 15.0 | 1.0 | 16.5 | ns |
| | | $V_{CC} = 4.5 \text{ V}$ to 5.5 V ; $C_L = 15 \text{ pF}$ | - | 4.7 | 6.8 | 1.0 | 8.0 | 1.0 | 8.5 | ns |
| | | $V_{CC} = 4.5 \text{ V}$ to 5.5 V ; $C_L = 50 \text{ pF}$ | - | 6.7 | 8.8 | 1.0 | 10.0 | 1.0 | 11.0 | ns |
| C_{PD} | power dissipation capacitance | $f_I = 1 \text{ MHz}$; $V_I = \text{GND}$ to V_{CC} [5] | - | 10 | - | - | - | - | - | pF |

| Symbol | Parameter | Conditions | 25 °C | | | -40 °C to +85 °C | | -40 °C to +125 °C | | Unit |
|-----------------------|-------------------------------|---|-------|---------|-----|------------------|------|-------------------|------|------|
| | | | Min | Typ [1] | Max | Min | Max | Min | Max | |
| 74VHCT126-Q100 | | | | | | | | | | |
| t_{pd} | propagation delay | nA to nY; see Fig. 4 [2] | | | | | | | | |
| | | $V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}; C_L = 15 \text{ pF}$ | - | 3.0 | 5.5 | 1.0 | 6.5 | 1.0 | 7.0 | ns |
| | | $V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}; C_L = 50 \text{ pF}$ | - | 4.3 | 7.5 | 1.0 | 8.5 | 1.0 | 9.5 | ns |
| t_{en} | enable time | nOE to nY; see Fig. 5 [3] | | | | | | | | |
| | | $V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}; C_L = 15 \text{ pF}$ | - | 3.3 | 5.1 | 1.0 | 6.0 | 1.0 | 6.5 | ns |
| | | $V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}; C_L = 50 \text{ pF}$ | - | 4.7 | 7.1 | 1.0 | 8.0 | 1.0 | 9.0 | ns |
| t_{dis} | disable time | nOE to nY; see Fig. 5 [4] | | | | | | | | |
| | | $V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}; C_L = 15 \text{ pF}$ | - | 4.8 | 6.8 | 1.0 | 8.0 | 1.0 | 8.5 | ns |
| | | $V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}; C_L = 50 \text{ pF}$ | - | 6.9 | 8.9 | 1.0 | 10.0 | 1.0 | 11.5 | ns |
| C_{PD} | power dissipation capacitance | $f_i = 1 \text{ MHz}; V_i = \text{GND to } V_{CC}; [5]$ $V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}$ | - | 12 | - | - | - | - | - | pF |

[1] Typical values are measured at nominal supply voltage ($V_{CC} = 3.3 \text{ V}$ and $V_{CC} = 5.0 \text{ V}$).

[2] t_{pd} is the same as t_{PLH} and t_{PHL} .

[3] t_{en} is the same as t_{PZL} and t_{PZH} .

[4] t_{dis} is the same as t_{PLZ} and t_{PHZ} .

[5] C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \sum(C_L \times V_{CC}^2 \times f_o)$ where:

f_i = input frequency in MHz;

f_o = output frequency in MHz;

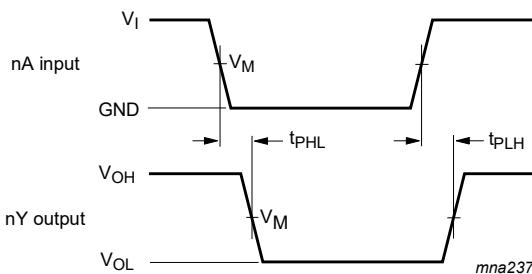
C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

$\sum(C_L \times V_{CC}^2 \times f_o)$ = sum of the outputs.

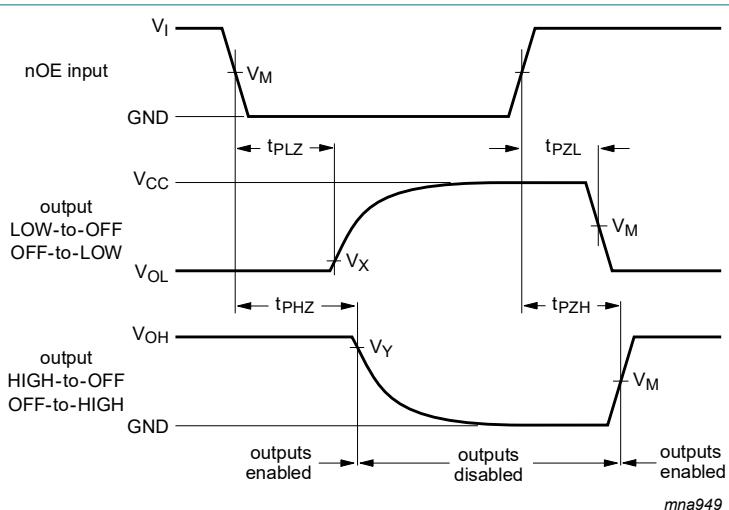
10.1. Waveforms



Measurement points are given in [Table 8](#).

V_{OL} and V_{OH} are typical voltage output levels that occur with the output load.

Fig. 4. Input to output propagation delays



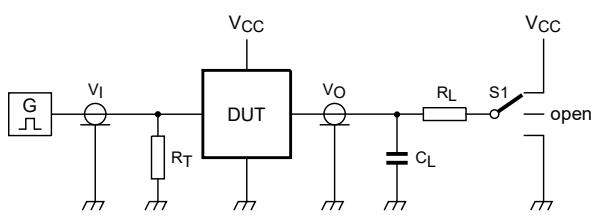
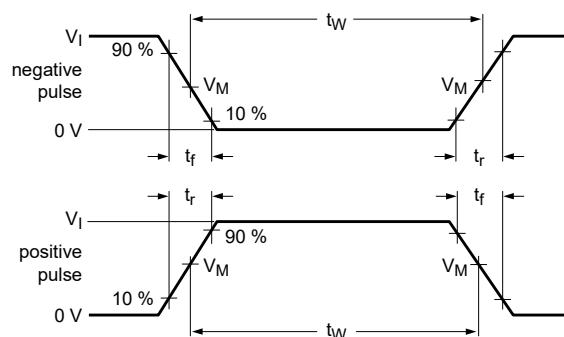
Measurement points are given in [Table 8](#).

V_{OL} and V_{OH} are typical voltage output levels that occur with the output load.

Fig. 5. Enable and disable times

Table 8. Measurement points

| Type | Input | Output | | | |
|----------------|--------------------|--------------------|-------|------------------|------------------|
| | | V_M | V_M | V_X | V_Y |
| 74VHC126-Q100 | 0.5V _{CC} | 0.5V _{CC} | | $V_{OL} + 0.3$ V | $V_{OH} - 0.3$ V |
| 74VHCT126-Q100 | 1.5 V | 0.5V _{CC} | | $V_{OL} + 0.3$ V | $V_{OH} - 0.3$ V |



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Test data is given in [Table 9](#).

Definitions test circuit:

R_T = termination resistance should be equal to output impedance Z_0 of the pulse generator.

C_L = load capacitance including jig and probe capacitance.

R_L = load resistance.

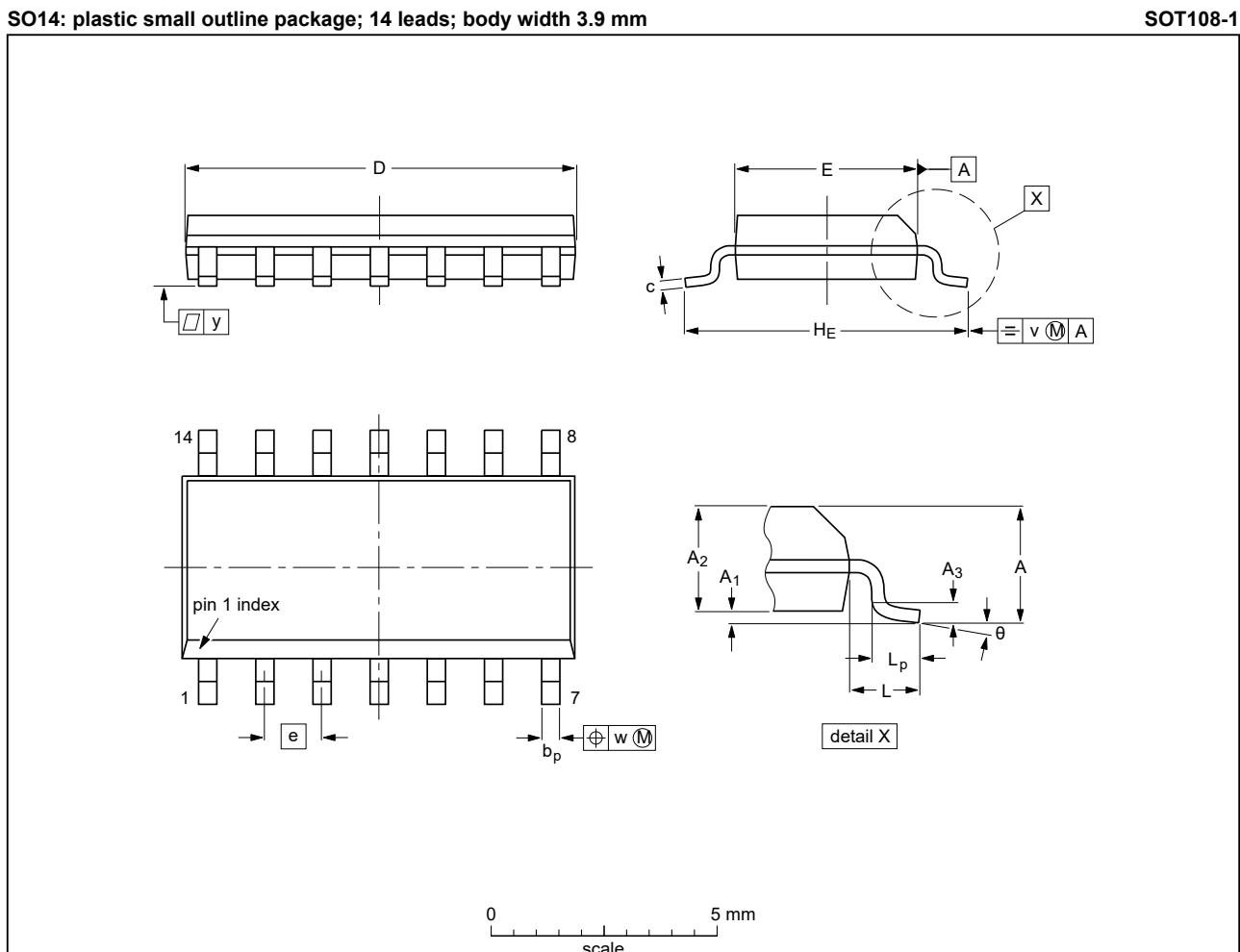
S1 = test selection switch

Fig. 6. Test circuit for measuring switching times

Table 9. Test data

| Type | Input | | Load | | S1 position | | |
|----------------|-----------------|---------------------------------|----------------|----------------|-------------------------------------|-------------------------------------|-------------------------------------|
| | V _I | t _r , t _f | C _L | R _L | t _{PHL} , t _{PLH} | t _{PZH} , t _{PHZ} | t _{PZL} , t _{PLZ} |
| 74VHC126-Q100 | V _{CC} | ≤ 3.0 ns | 15 pF, 50 pF | 1 kΩ | open | GND | V _{CC} |
| 74VHCT126-Q100 | 3.0 V | ≤ 3.0 ns | 15 pF, 50 pF | 1 kΩ | open | GND | V _{CC} |

11. Package outline



Dimensions (inch dimensions are derived from the original mm dimensions)

| Unit | A | A ₁ | A ₂ | A ₃ | b _p | c | D ⁽¹⁾ | E ⁽¹⁾ | e | H _E | L | L _p | v | w | y | θ |
|--------|-----------|----------------|----------------|----------------|----------------|-------|------------------|------------------|---|----------------|---|----------------|-------|------|-------|----|
| mm | max 1.75 | 0.25 | | | 0.51 | 0.25 | 8.75 | 4.0 | | 6.2 | | 1.27 | 0.2 | 0.25 | 0.1 | 8° |
| nom | 1.00 | 0.10 | 1.25 | | 0.31 | 0.10 | 8.55 | 3.8 | | 5.8 | | 1.05 | 0.4 | | | 0° |
| min | 0.10 | 0.10 | 0.25 | | 0.31 | 0.10 | 8.55 | 3.8 | | 5.8 | | 0.4 | | | | 0° |
| inches | max 0.069 | 0.010 | | | 0.020 | 0.010 | 0.344 | 0.16 | | 0.244 | | 0.05 | 0.008 | 0.01 | 0.004 | 8° |
| nom | 0.049 | 0.004 | 0.049 | | 0.012 | 0.004 | 0.337 | 0.15 | | 0.228 | | 0.041 | 0.016 | | | 0° |
| min | 0.044 | 0.004 | 0.044 | | 0.012 | 0.004 | 0.337 | 0.15 | | 0.228 | | 0.016 | | | | 0° |

Note

1. Plastic or metal protrusions of 0.15 mm (0.006 inch) maximum per side are not included.

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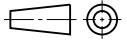
| Outline version | References | | | | European projection | Issue date |
|-----------------|------------|--------|-------|--|---|----------------------|
| | IEC | JEDEC | JEITA | | | |
| SOT108-1 | | MS-012 | | |  | 03-02-19 23-10-27 |

Fig. 7. Package outline SOT108-1 (SO14)

TSSOP14: plastic thin shrink small outline package; 14 leads; body width 4.4 mm

SOT402-1

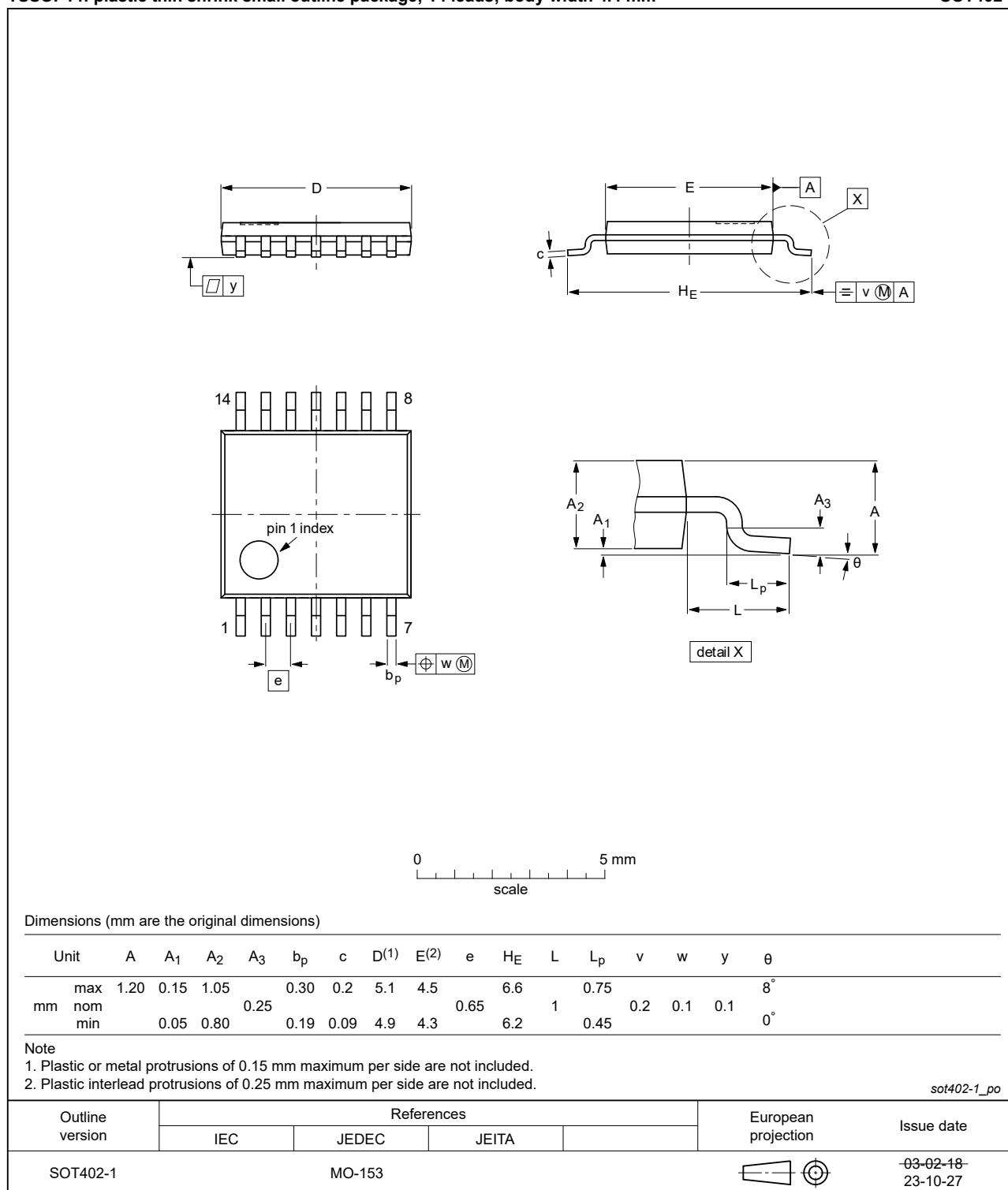


Fig. 8. Package outline SOT402-1 (TSSOP14)

DHVQFN14: plastic dual in-line compatible thermal enhanced very thin quad flat package; no leads;
14 terminals; body 2.5 x 3 x 0.85 mm

SOT762-1

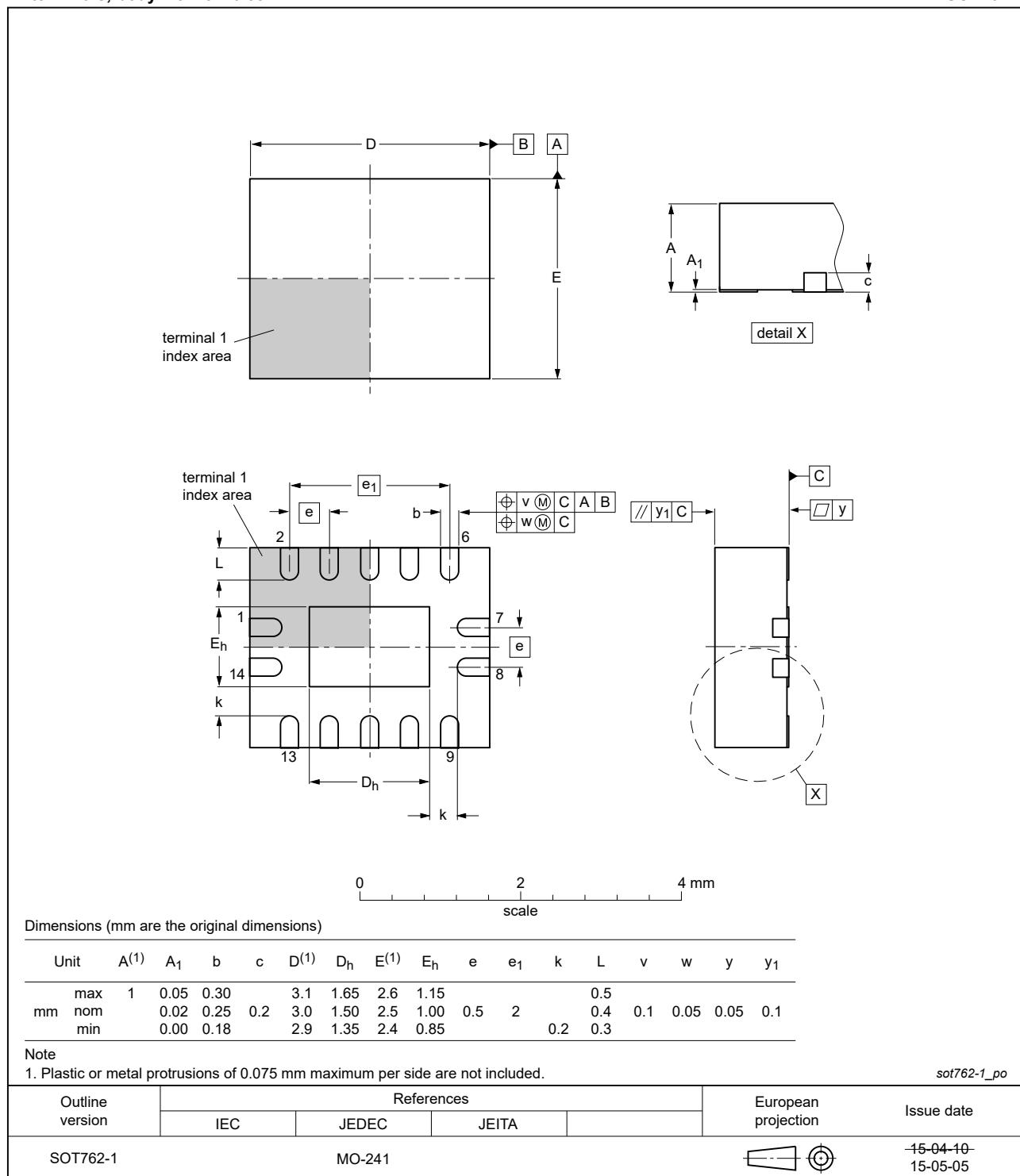


Fig. 9. Package outline SOT762-1 (DHVQFN14)

12. Abbreviations

Table 10. Abbreviations

| Acronym | Description |
|---------|--|
| CDM | Charged Device Model |
| CMOS | Complementary Metal-Oxide Semiconductor |
| DUT | Device Under Test |
| ESD | ElectroStatic Discharge |
| HBM | Human Body Model |
| LSTTL | Low-power Schottky Transistor-Transistor Logic |
| TTL | Transistor-Transistor Logic |

13. Revision history

Table 11. Revision history

| Document ID | Release date | Data sheet status | Change notice | Supersedes |
|------------------------|---|--------------------|---------------|------------------------|
| 74VHC_VHCT126_Q100 v.3 | 20240501 | Product data sheet | - | 74VHC_VHCT126_Q100 v.2 |
| Modifications: | <ul style="list-style-type: none"> Section 2: ESD specification updated according to the latest JEDEC standard. Fig. 7 and Fig. 8: Aligned SO and TSSOP package outline drawings to JEDEC MS-012 and MO-153. | | | |
| 74VHC_VHCT126_Q100 v.2 | 20200406 | Product data sheet | - | 74VHC_VHCT126_Q100 v.1 |
| Modifications: | <ul style="list-style-type: none"> The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Section 2 updated. Table 4: Derating values for P_{tot} total power dissipation have been updated. Package outline drawing SOT762-1 (DHVQFN14) updated. | | | |
| 74VHC_VHCT126_Q100 v.1 | 20131115 | Product data sheet | - | - |

14. Legal information

Data sheet status

| Document status [1][2] | Product status [3] | Definition |
|--------------------------------|--------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

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