

P4080NSN1MMB :Part Detail

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General Information

Part Number	P4080NSN1MMB
Description	P4080 Std NonEnc PbFr 1200/1200 Rev 2
Material Type	Tested Packaged Device
Life Cycle Description (code)	REMOVED FROM ACTIVE PORTFOLIO
Status	No Longer Manufactured
Application/Qualification Tier	10-YEARS APPLICATION LIFE

Package Information

Package Type and Termination Count	FCPBGA 1295
Package Description and Mechanical Drawing	FCPBGA 1295 37.5SQ3.55P1
Device Weight(g)	13.97780
Package Length (nominal)(mm)	37.500
Package Width (nominal)(mm)	37.500
Package Thickness (nominal)(mm)	3.190
Maximum Height Above Board(mm)	3.530
Tape & Reel	No

Environmental and Compliance Information

Pb-Free	No
RoHS Compliant	
RoHS technical exemption(s)	15
Halogen Free	Yes
Material Composition Declaration (MCD)	Download MCD Report Download MCD Report
RoHS Certificate of Analysis (CoA)	Contact Us
2nd Level Interconnect	e1

Floor Life	168 HOURS
Peak Package Body Temperature (PPT)(°C)	245
Maximum Time at Peak Temperature (s)	30
Number of Reflow Cycles	3
REACH SVHC	NXP REACH Statement

Replacement Parts information for P4080NSN1MMB

Replacement Part	Replacement Type	Replacement Reason	Status	Device Sample Availability	Device Production Availability	Further Replacements	Replacement Part Details
P4080NSN7MMC	Same Form, Fit & Function, Pin-for-Pin Equivalent	Fabrication Site New Product Qualification Subcontractor Assembly Site Device Specification or Errata	Active		12 Apr 2013	01 Nov 2013	-

Ordering Information

Exempt from Minimum Delivery Value	Yes
Export Control Classification Number (US)	3A991A1
Harmonized Tariff (	
CCATS Document	-
ENC Status	-
Other Trade Compliance Documents	-
Budgetary Price excluding tax(US Currency	-
Order	Distributor

Product/Process Change Notice (PCN)

Number	Type	Title	Issue Date	Effectivity Date
15020	Product Bulletin	P4080 SGMII MAY FAIL LINK INITIALIZATION	19 Mar 2012	20 Mar 2012
15008	Product Bulletin	ESDHC Host May Not Detect SD Card Insertion/Removal Using DAT3 Signal	13 Feb 2012	14 Feb 2013

Operating Characteristics

Ambient Operating Temperature (Min-Max) (°C)	0 to 70
Bus Frequency (Max) (MHz)	600
Cache (KB)	32
Core Type	e500mc

Device Production Availability	01 Nov 2013
Device Sample Availability	12 Apr 2013
Ethernet Type	1GBaseT SGMII
External Bus Interface	RapidIO-Serial
External Memory Supported	DDR3 DDR2
GPIOs	32
I2C	4
Junction Temperature (Max) (°C)	105
L2 Cache (Max) (KB)	128
L3 Cache (Max) (KB)	2000
Operating Frequency (Max) (MHz)	1200
Sample Exception Availability	N
SPI	1
USB features	USB 2.0

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