

Single-In-Line Conformal Coated Diode Array

D Series

Features

- 4 to 9 Leads
- Standard and custom circuits
- Space saving design

OBSOLETE



 All parts are Pb-free and comply with EU Directive 2011/65/EU amended by (EU) 2015/863 (RoHS3)

Specification

Reverse Voltage, V_R	80V
Reverse Current, I_R	1.0 μ A ($V_R = 70$ V)
Forward Current, I_F	100mA Average, 300mA Surge (1 μ s Max.)
Forward Voltage, V_F	1.2V @ $I_F = 100$ mA
Package Power, P_{PKG}	200mW @ 25°C
Reverse Recovery Time, t_{rr}	4ns ($V_R = 6$ V, $I_F = 5$ mA, $R_L = 50\Omega$)
Capacitance, C	5.5pF ($V_R = 6$ V, $f = 1$ MHz)
Storage Temperature Range	- 55°C to 125°C
Operating Temperature Range	- 25°C to 80°C

General Note
TT Electronics reserves the right to make changes in product specification without notice or liability.
All information is subject to TT Electronics' own data and is considered accurate at time of going to print.

Specification

Circuit Type	Polarity "A"	Polarity "C"
1 N = 4 to 9		
2 N = 5 to 9 (Odd #)		
3 N = 4 to 8 (Even #)		
5 N = 4 to 9		

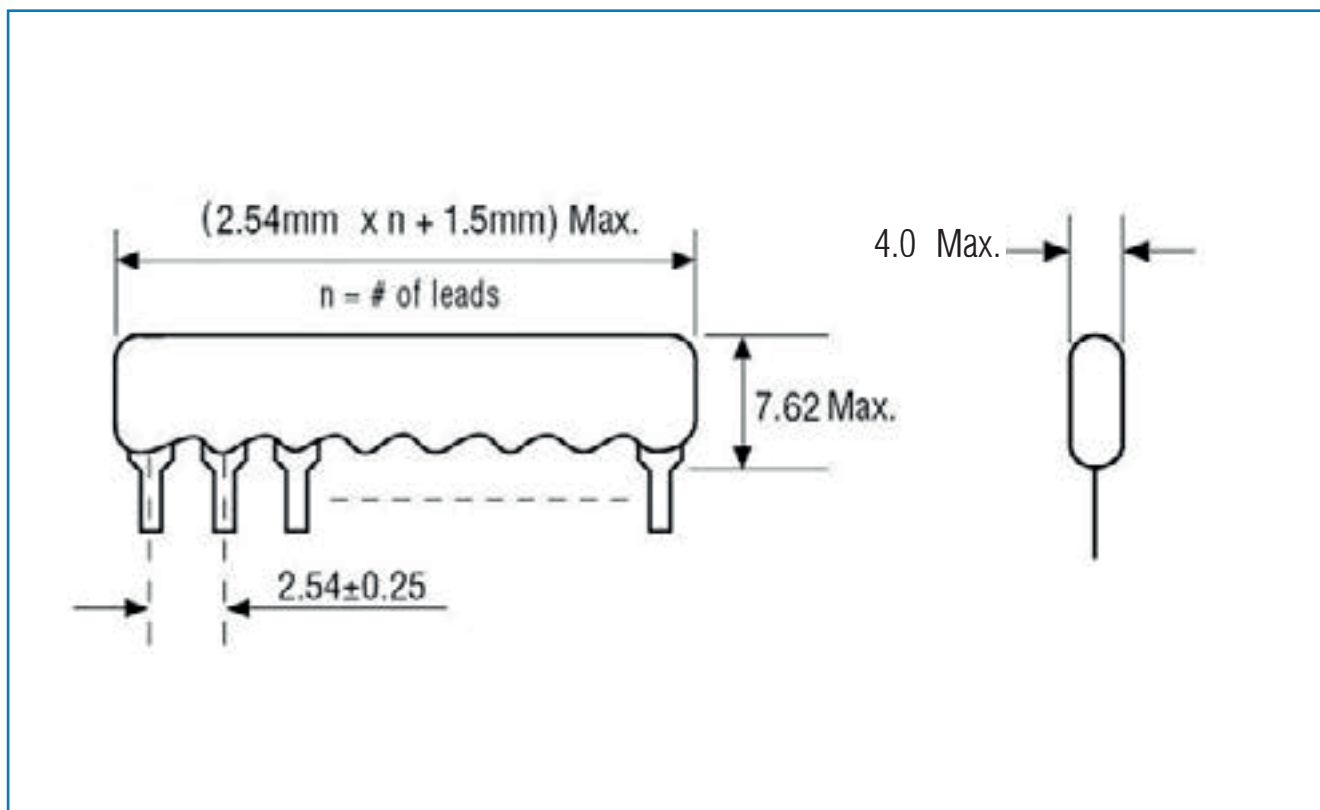
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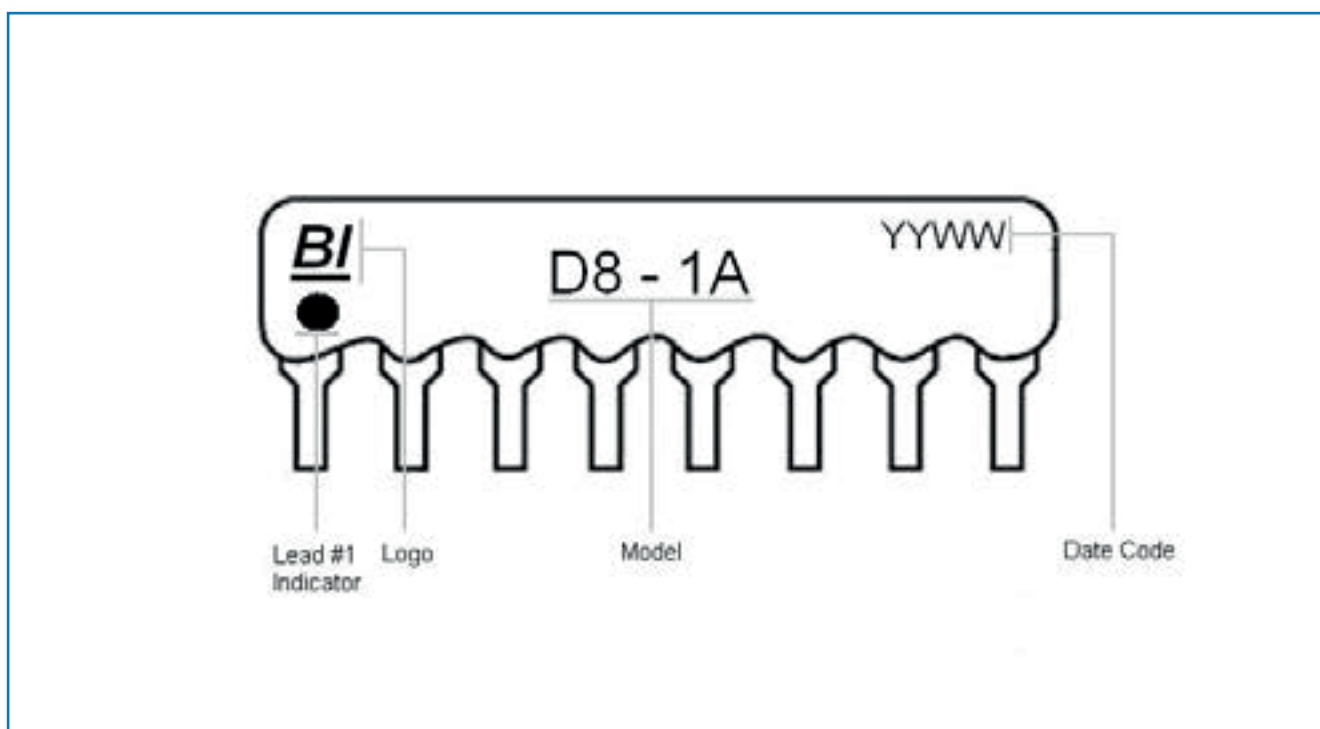
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Outline Dimensions (Inch/mm)



Typical Part Marking



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Ordering Procedure

Example: D83C (Diode array with 8 pins, circuit 3 (isolated) with cathode at pin 1, Pb-free)

D	8		3	C
1	2	3	4	

1 Series	2 ¹ Number of Pins	3 ¹ Circuit Type	4 Polarity
D	4	1 = Bussed, pin 1 common	A Anode at common pin (circuits 1 & 2) or at pin 1 (circuits 3 & 5)
	5	2 = Bussed, centre pin common	
	6	3 = Isolated	C Cathode at common pin (circuits 1 & 2) or at pin 1 (circuits 3 & 5)
	7	5 = Dual termination	
	8		
	9		

Note 1 – see schematics for valid combinations of number of pins and circuit type.