



ECH8664R

N-Channel Power MOSFET 30V, 7A, 23.5mΩ, Dual ECH8

ON Semiconductor®

<http://onsemi.com>

Features

- Low ON-resistance
- 2.5V drive
- Common-drain type
- Protection diode in
- Built-in gate protection resistor
- Best suited for LiB charging and discharging switch
- Halogen free compliance

Specifications

Absolute Maximum Ratings at Ta=25°C

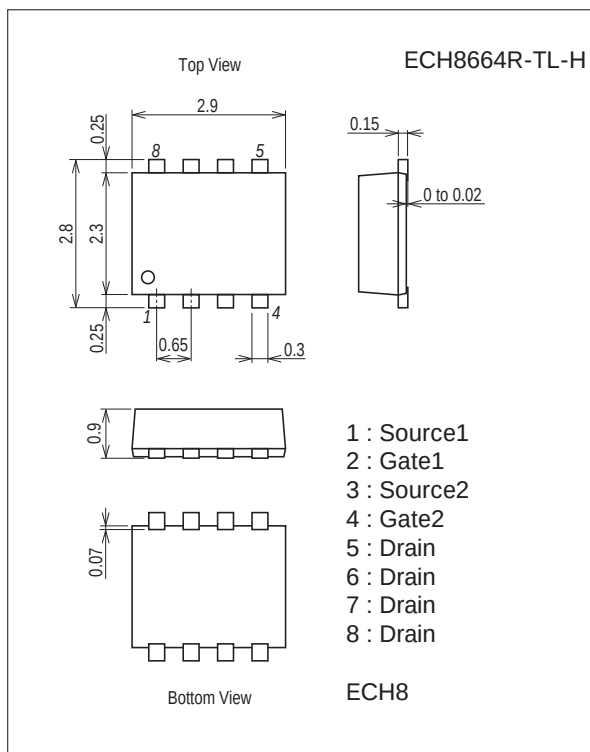
Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		30	V
Gate-to-Source Voltage	V _{GSS}		±12	V
Drain Current (DC)	I _D		7	A
Drain Current (Pulse)	I _{DP}	PW≤10μs, duty cycle≤1%	60	A
Allowable Power Dissipation	P _D	When mounted on ceramic substrate (900mm ² ×0.8mm) 1unit	1.3	W
Total Power Dissipation	P _T	When mounted on ceramic substrate (900mm ² ×0.8mm)	1.4	W
Channel Temperature	T _{ch}		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Package Dimensions

unit : mm (typ)

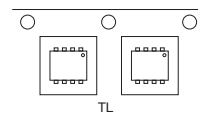
7011A-003



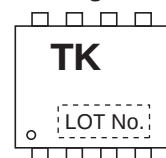
Product & Package Information

- Package : ECH8
- JEITA, JEDEC : -
- Minimum Packing Quantity : 3,000 pcs./reel

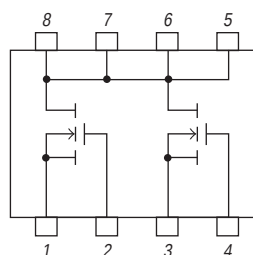
Packing Type : TL



Marking



Electrical Connection

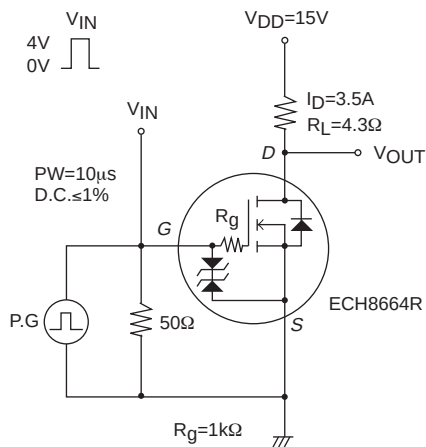


ECH8664R

Electrical Characteristics at Ta=25°C

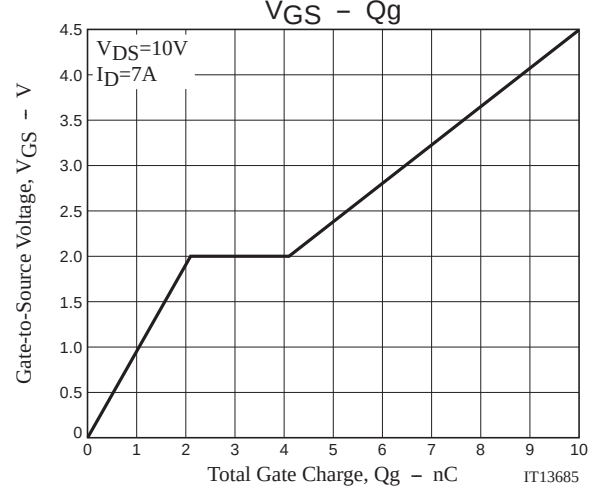
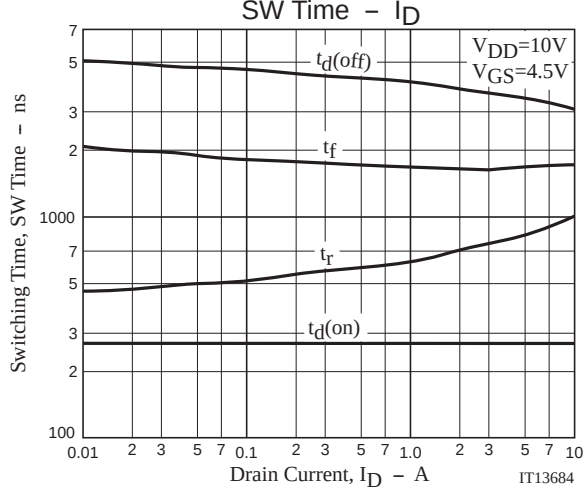
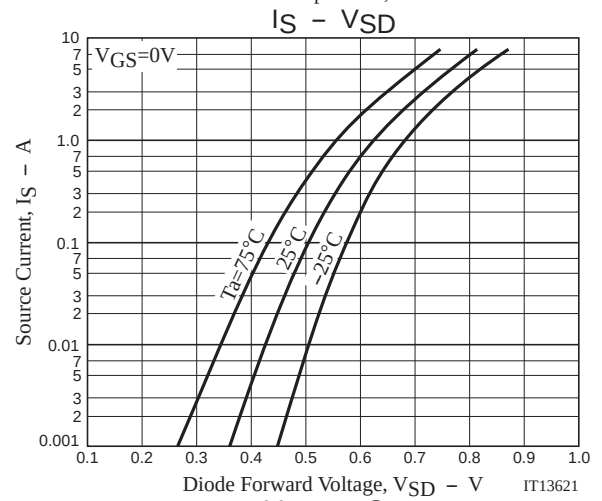
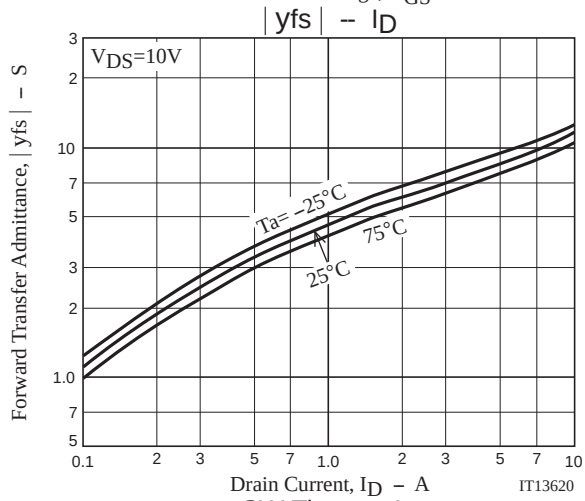
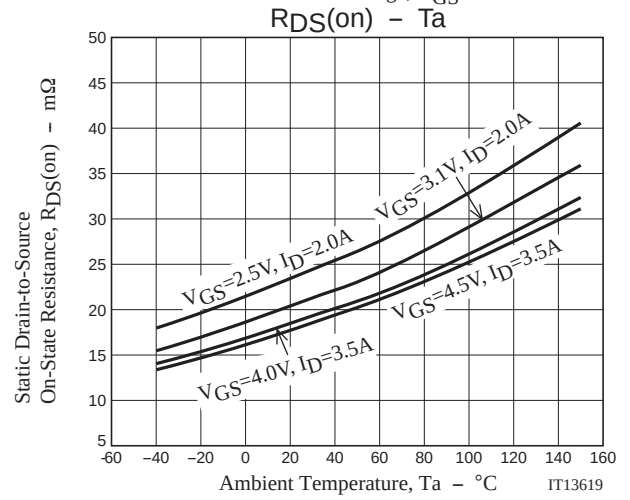
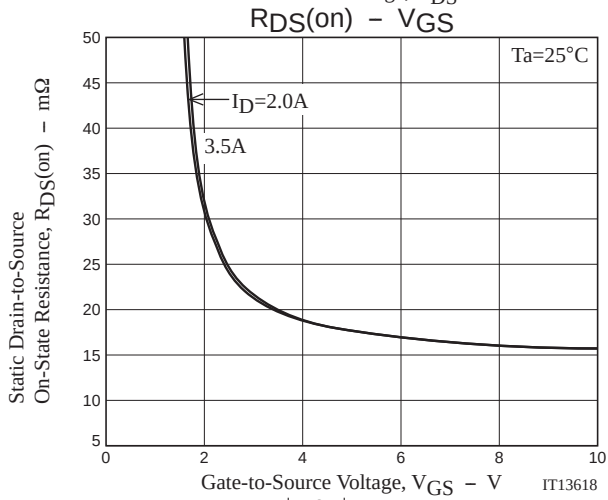
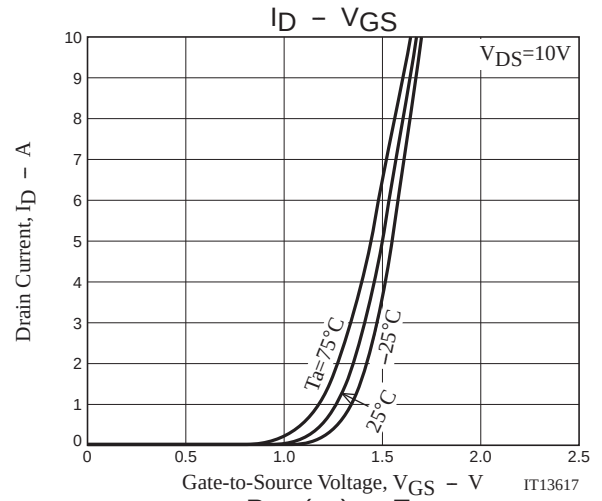
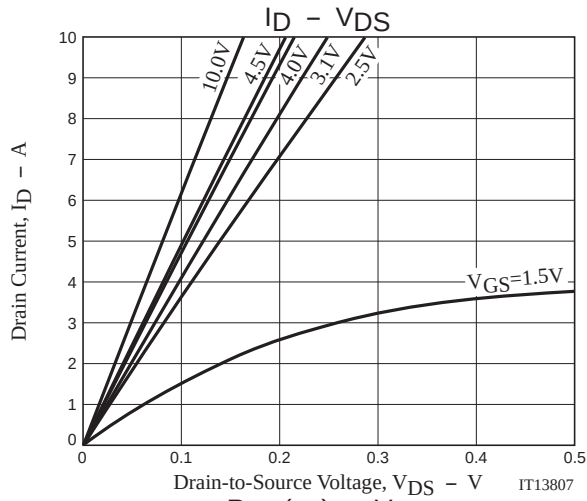
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=1mA, V_{GS}=0V$	30			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS}=30V, V_{GS}=0V$			1	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 8V, V_{DS}=0V$			± 10	μA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS}=10V, I_D=1mA$	0.5		1.3	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS}=10V, I_D=3.5A$	4.5	7.5		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D=3.5A, V_{GS}=4.5V$	12.5	18	23.5	$m\Omega$
	$R_{DS(on)2}$	$I_D=3.5A, V_{GS}=4.0V$	13	19	25	$m\Omega$
	$R_{DS(on)3}$	$I_D=2A, V_{GS}=3.1V$	14.5	21	27.3	$m\Omega$
	$R_{DS(on)4}$	$I_D=2A, V_{GS}=2.5V$	14.5	24	34	$m\Omega$
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit.		270		ns
Rise Time	t_r			850		ns
Turn-OFF Delay Time	$t_{d(off)}$			3300		ns
Fall Time	t_f			1700		ns
Total Gate Charge	Q_g	$V_{DS}=10V, V_{GS}=4.5V, I_D=7A$		10		nC
Gate-to-Source Charge	Q_{gs}			2.1		nC
Gate-to-Drain "Miller" Charge	Q_{gd}			2.0		nC
Diode Forward Voltage	V_{SD}	$I_S=7A, V_{GS}=0V$		0.75	1.2	V

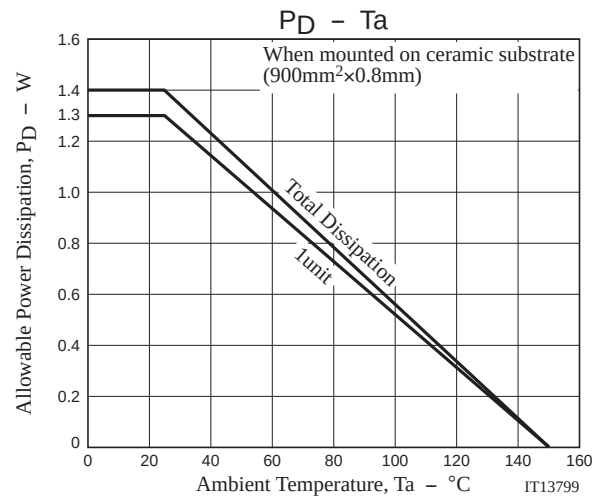
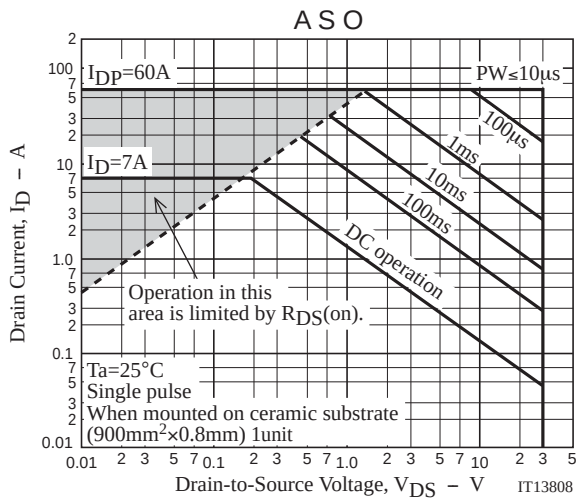
Switching Time Test Circuit



Ordering Information

Device	Package	Shipping	memo
ECH8664R-TL-H	ECH8	3,000pcs./reel	Pb Free and Halogen Free





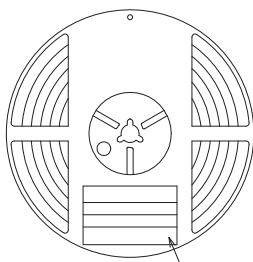
Embossed Taping Specification

ECH8664R-TL-H

1. Packing Format

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
ECH8	CPH6	3,000	15,000	90,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

Packing method



Reel label

Type No.
LOT No.
Quantity
Origin

Reel label, Inner box label
(unit:mm)

(P) TYPE	000000000
(1) LOT	00
(Q) QTY	0,000 (1) LEAD FREE *
(2) SPECIAL	
	20722005310C
	ASSEMBLY:**** (DIFFUSION:****)

Outer box label

It is a label at the time of factory shipments.
The form of a label may change in physical distribution process.

TYPE CODE	00000000000000000000
TYPE	00000000
QTY	0,000 PCS (1) LEAD FREE *
LOT	00000000
PACKAGE	00000000
SPECIAL	
	20722005310C
	ASSEMBLY:**** (DIFFUSION:****)

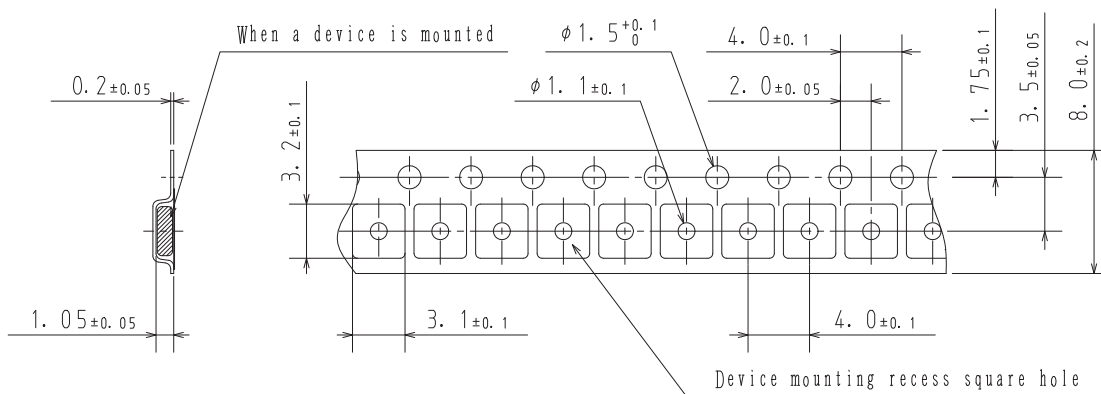
NOTE (1)

The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

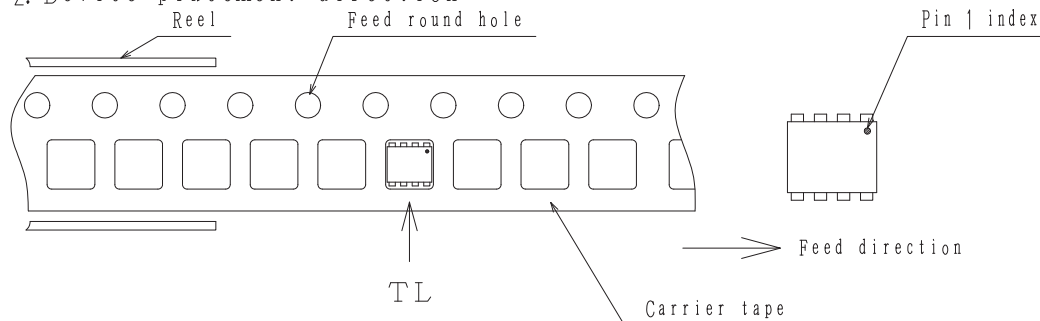
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)



2-2. Device placement direction

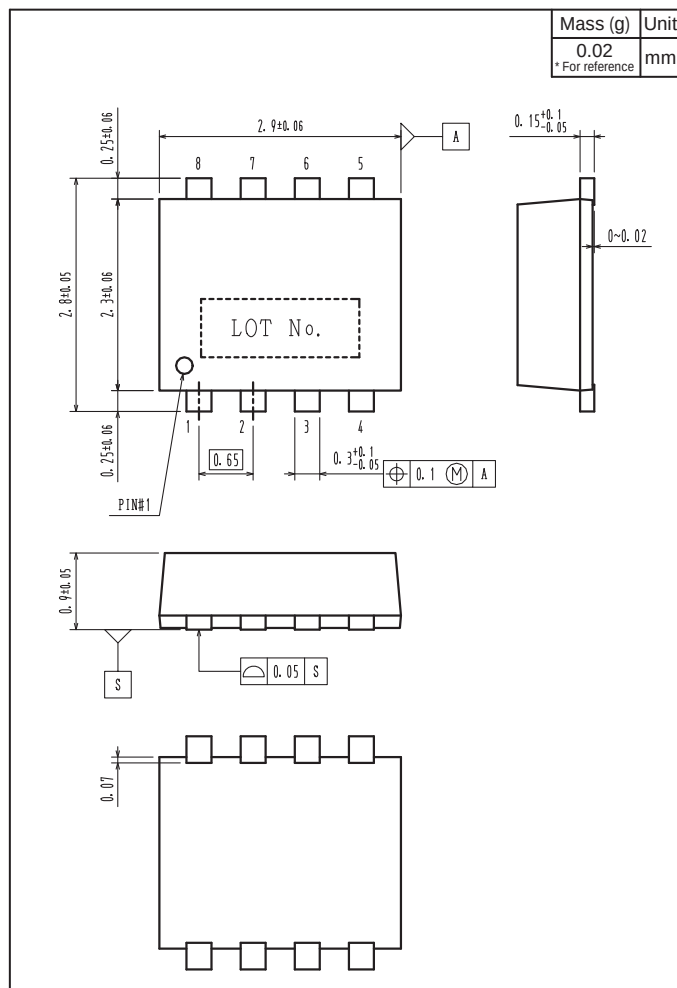


Those with pin 1 index on the feed hole side.....TL

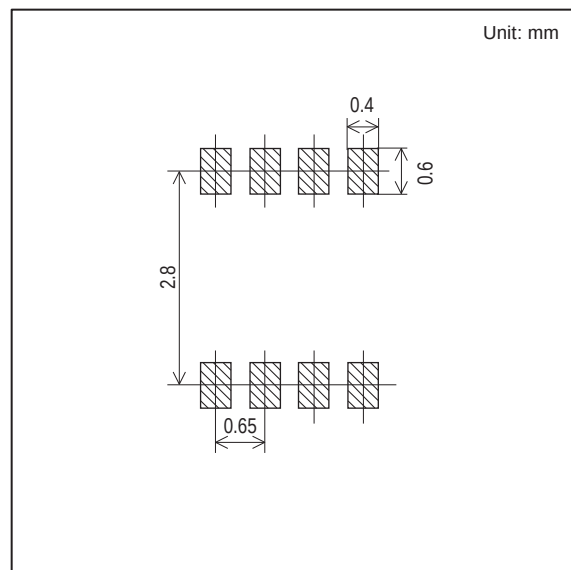
ECH8664R

Outline Drawing

ECH8664R-TL-H



Land Pattern Example



Note on usage : Since the ECH8664R is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

ON Semiconductor and the ON logo are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.