Dual General Purpose Transistors

NPN/PNP Duals (Complementary)

These transistors are designed for general purpose amplifier applications. They are housed in the SOT-363/SC-88 which is designed for low power surface mount applications.

Features

• Pb-Free Packages are Available

MAXIMUM RATINGS - NPN

Rating		Symbol	Value	Unit
Collector-Emitter Voltage	BC846 BC847 BC848	V _{CEO}	65 45 30	V
Collector-Base Voltage	BC846 BC847 BC848	V _{CBO}	80 50 30	V
Emitter-Base Voltage		V _{EBO}	6.0	V
Collector Current – Continu	Ic	100	mAdc	

MAXIMUM RATINGS - PNP

Rating		Symbol	Value	Unit
Collector-Emitter Voltage	BC846 BC847 BC848	V _{CEO}	-65 -45 -30	V
Collector-Base Voltage	BC846 BC847 BC848	V _{CBO}	-80 -50 -30	V
Emitter-Base Voltage		V _{EBO}	-5.0	V
Collector Current – Continu	Ic	-100	mAdc	

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

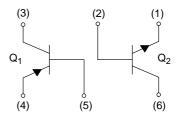
Characteristic	Symbol	Max	Unit
Total Device Dissipation Per Device FR-5 Board (Note 1) T _A = 25°C Derate above 25°C	P _D	380 250	mW
Thermal Resistance, Junction–to–Ambient	$R_{ heta JA}$	3.0	°C/W
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C

1. $FR-5 = 1.0 \times 0.75 \times 0.062$ in.



ON Semiconductor®

http://onsemi.com



MARKING DIAGRAM



SOT-363 CASE 419B STYLE 1



xx = Device Code

M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Mark	Package	Shipping [†]
BC846BPDW1T1	ВВ	SOT-363	3000 Units/Reel
BC846BPDW1T1G	BB	SOT-363 (Pb-Free)	3000 Units/Reel
BC847BPDW1T1	BF	SOT-363	3000 Units/Reel
BC847BPDW1T1G	BF	SOT-363 (Pb-Free)	3000 Units/Reel
BC847CPDW1T1	BG	SOT-363	3000 Units/Reel
BC848CPDW1T1	BL	SOT-363	3000 Units/Reel
BC848CPDW1T1G	BL	SOT-363 (Pb-Free)	3000 Units/Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure. BRD8011/D.

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS			•		•
Collector – Emitter Breakdown Voltage (I _C = 10 mA) BC846 BC847 BC848	Series	65 45 30	- - -	- - -	V
Collector – Emitter Breakdown Voltage (I _C = 10 μA, V _{EB} = 0) BC8476 BC848	B Only	80 50 30	- - -	- - -	V
Collector – Base Breakdown Voltage (I_C = 10 μ A) BC846 BC847 BC848	Series	80 50 30	- - -	- - -	V
Emitter – Base Breakdown Voltage $(I_E = 1.0 \; \mu A) \\ BC846 \\ BC847 \\ BC848$	Series	6.0 6.0 5.0	- - -	- - -	V
Collector Cutoff Current ($V_{CB} = 30 \text{ V}$) ($V_{CB} = 30 \text{ V}$, $T_A = 150^{\circ}\text{C}$)	І _{СВО}	- -	- -	15 5.0	nA μA
ON CHARACTERISTICS		_	-	_	-
DC Current Gain $(I_C = 10~\mu\text{A},~V_{CE} = 5.0~\text{V}) \\ BC846B,~BC847B \\ BC847C,~BC848C$	h _{FE}	- -	150 270	- -	_
$(I_C = 2.0 \text{ mA}, V_{CE} = 5.0 \text{ V})$ BC846B, BC847B BC847C, BC848C		200 420	290 520	475 800	
Collector – Emitter Saturation Voltage (I_C = 10 mA, I_B = 0.5 m (I_C = 100 mA, I_B = 5.0 m		-	- -	0.25 0.6	V
Base – Emitter Saturation Voltage (I_C = 10 mA, I_B = 0.5 mA) (I_C = 100 mA, I_B = 5.0 mA)	V _{BE} (sat)	-	0.7 0.9	- -	V
Base-Emitter Voltage ($I_C = 2.0$ mA, $V_{CE} = 5.0$ V) ($I_C = 10$ mA, $V_{CE} = 5.0$ V)	V _{BE(on)}	580 -	660 -	700 770	mV
SMALL-SIGNAL CHARACTERISTICS	•		•		•
Current – Gain – Bandwidth Product ($I_C = 10 \text{ mA}, V_{CE} = 5.0 \text{ Vdc}, f = 100 \text{ MHz}$)	f _T	100	_	I	MHz
Output Capacitance (V _{CB} = 10 V, f = 1.0 MHz)	C _{obo}	_	_	4.5	pF
Noise Figure (I _C = 0.2 mA, V _{CE} = 5.0 Vdc, R _S = 2.0 k Ω , f = 1.0 kHz, BW	/ = 200 Hz)	-	_	10	dB

ELECTRICAL CHARACTERISTICS (PNP) ($T_A = 25$ °C unless otherwise noted)

Chara	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERISTICS		•	•	•	•	
Collector – Emitter Breakdown Voltage (I _C = –10 mA)	BC846 Series BC847 Series BC848 Series	V _(BR) CEO	-65 -45 -30	- - -	- - -	V
Collector – Emitter Breakdown Voltage ($I_C = -10 \mu A, V_{EB} = 0$)	BC846 Series BC847 Series BC848 Series	V _(BR) CES	-80 -50 -30	- - -	- - -	V
Collector – Base Breakdown Voltage ($I_C = -10 \mu A$)	BC846 Series BC847 Series BC848 Series	V _(BR) CBO	-80 -50 -30	- - -	- - -	V
Emitter – Base Breakdown Voltage ($I_E = -1.0 \mu A$)	BC846 Series BC847 Series BC848 Series	V _{(BR)EBO}	-5.0 -5.0 -5.0	- - -	- - -	V
Collector Cutoff Current ($V_{CB} = -30 \text{ V}$ ($V_{CB} = -30 \text{ V}$		I _{CBO}	<u>-</u>	_ _	-15 -4.0	nA μA
ON CHARACTERISTICS			T	1	1	T
(0 () 0L	2846B, BC847B 2847C, BC848C	h _{FE}	- -	150 270	- -	_
	:846B, BC847B :847C, BC848C		200 420	290 520	475 800	
		V _{CE(sat)}	_ _	_ _	-0.3 -0.65	V
Base – Emitter Saturation Voltage ($I_C = -10 \text{ mA}, I_B = -0.5 \text{ mA}$) ($I_C = -100 \text{ mA}, I_B = -5.0 \text{ mA}$)		V _{BE(sat)}	- -	-0.7 -0.9	- -	V
Base – Emitter On Voltage (I_C = -2.0 mA, V_{CE} = -5.0 V) (I_C = -10 mA, V_{CE} = -5.0 V)		V _{BE(on)}	-0.6 -	_ _	-0.75 -0.82	V
SMALL-SIGNAL CHARACTERISTIC	s					
Current – Gain – Bandwidth Product ($I_C = -10$ mA, $V_{CE} = -5.0$ Vdc, $f = 1$	00 MHz)	f _T	100	_	_	MHz
Output Capacitance (V _{CB} = -10 V, f = 1.0 MHz)		C _{ob}	_	-	4.5	pF
Noise Figure (I _C = -0.2 mA, V _{CE} = -5.0 Vdc, R _S f = 1.0 kHz, BW = 200 Hz)	= 2.0 kΩ,	NF	-	-	10	dB

TYPICAL NPN CHARACTERISTICS - BC846

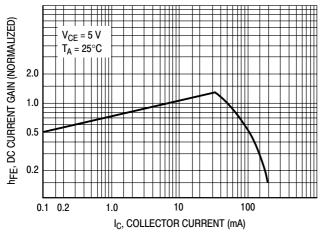


Figure 1. DC Current Gain

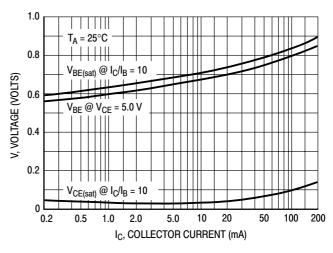


Figure 2. "On" Voltage

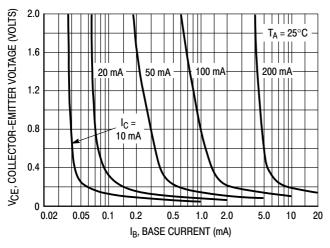


Figure 3. Collector Saturation Region

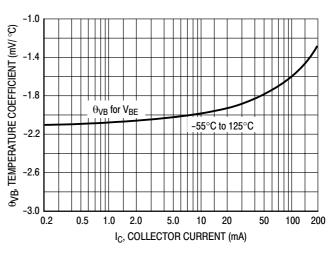


Figure 4. Base-Emitter Temperature Coefficient

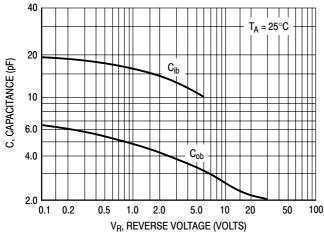


Figure 5. Capacitance

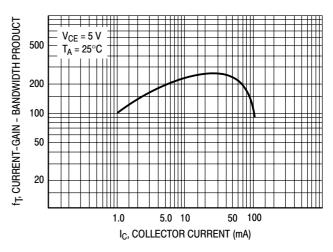


Figure 6. Current-Gain - Bandwidth Product

TYPICAL PNP CHARACTERISTICS — BC846

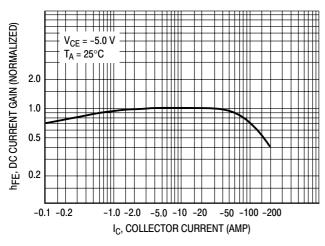


Figure 7. DC Current Gain

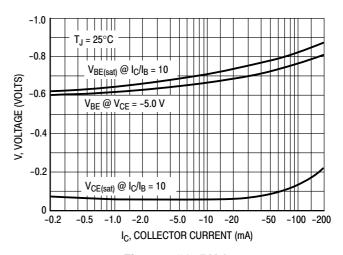


Figure 8. "On" Voltage

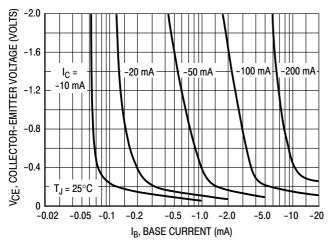


Figure 9. Collector Saturation Region

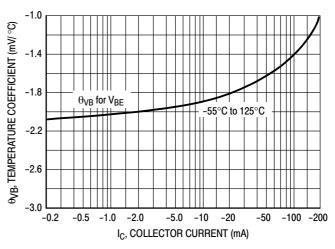


Figure 10. Base-Emitter Temperature Coefficient

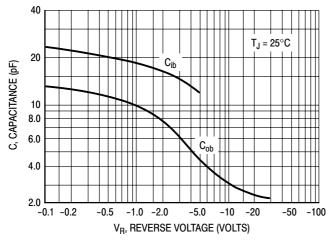


Figure 11. Capacitance

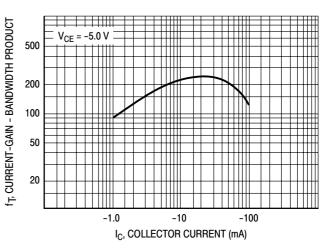


Figure 12. Current-Gain - Bandwidth Product

TYPICAL NPN CHARACTERISTICS - BC847 SERIES & BC848 SERIES

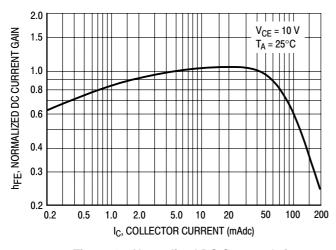


Figure 13. Normalized DC Current Gain

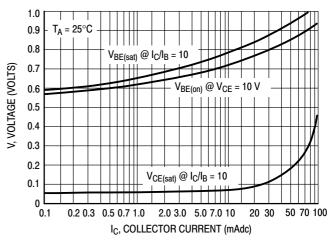


Figure 14. "Saturation" and "On" Voltages

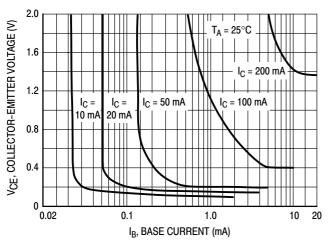


Figure 15. Collector Saturation Region

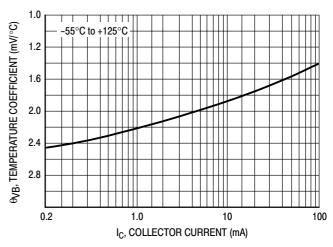


Figure 16. Base–Emitter Temperature Coefficient

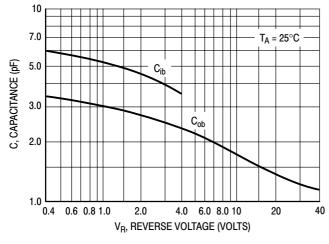


Figure 17. Capacitances

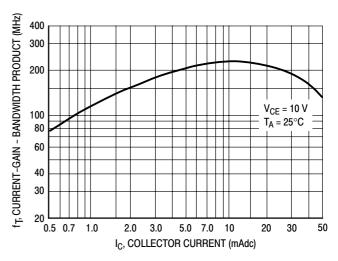
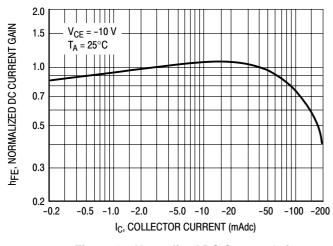


Figure 18. Current-Gain - Bandwidth Product

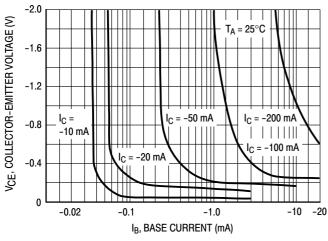
TYPICAL PNP CHARACTERISTICS — BC847 SERIES & BC848 SERIES



 $T_A = 25^{\circ}C$ -0.9 $V_{BE(sat)} @ I_C/I_B = 10$ -0.8 -0.7 V, VOLTAGE (VOLTS) -0.6 -0.5 -0.4 -0.3-0.2 $V_{CE(sat)} @ I_C/I_B = 10$ -0.1 -1.0 -0.1 -0.2 -2.0 -5.0 -50 -100 IC, COLLECTOR CURRENT (mAdc)

Figure 19. Normalized DC Current Gain

Figure 20. "Saturation" and "On" Voltages



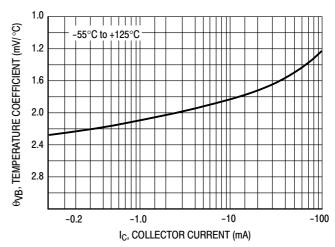
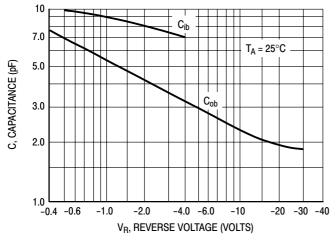


Figure 21. Collector Saturation Region

Figure 22. Base–Emitter Temperature Coefficient



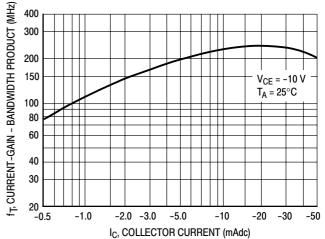


Figure 23. Capacitances

Figure 24. Current-Gain - Bandwidth Product

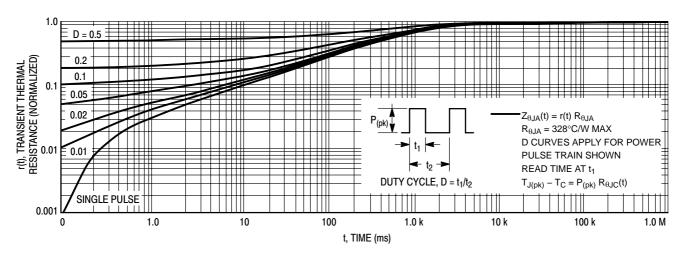


Figure 25. Thermal Response

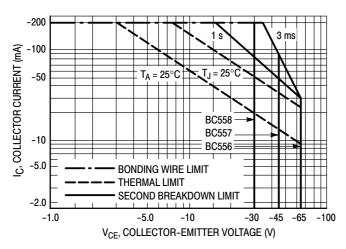


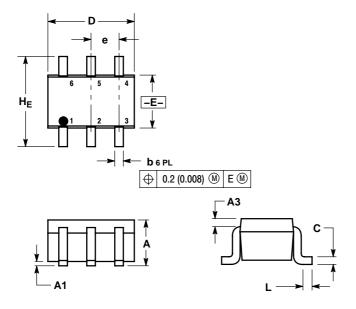
Figure 26. Active Region Safe Operating Area

The safe operating area curves indicate I_C – V_{CE} limits of the transistor that must be observed for reliable operation. Collector load lines for specific circuits must fall below the limits indicated by the applicable curve.

The data of Figure 26 is based upon $T_{J(pk)} = 150^{\circ}C$; T_C or T_A is variable depending upon conditions. Pulse curves are valid for duty cycles to 10% provided $T_{J(pk)} \le 150^{\circ}C$. $T_{J(pk)}$ may be calculated from the data in Figure 25. At high case or ambient temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by the secondary breakdown.

PACKAGE DIMENSIONS

SOT-363/SC-88 CASE 419B-02 ISSUE V

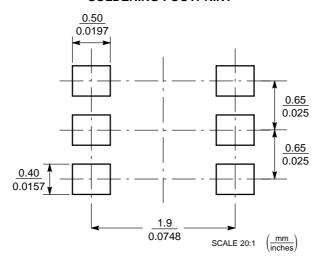


- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
- 3. 419B-01 OBSOLETE, NEW STANDARD 419B-02.

	MILLIMETERS			INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.80	0.95	1.10	0.031	0.037	0.043	
A1	0.00	0.05	0.10	0.000	0.002	0.004	
А3		0.20 REF			0.008 REF		
b	0.10	0.21	0.30	0.004	0.008	0.012	
С	0.10	0.14	0.25	0.004	0.005	0.010	
D	1.80	2.00	2.20	0.070	0.078	0.086	
Е	1.15	1.25	1.35	0.045	0.049	0.053	
е	0.65 BSC			0	.026 BS	С	
L	0.10	0.20	0.30	0.004	0.008	0.012	
HE	2.00	2.10	2.20	0.078	0.082	0.086	

- STYLE 1:
 PIN 1. EMITTER 2
 2. BASE 2
 3. COLLECTOR 1
 4. EMITTER 1
 5. BASE 1
 6. COLLECTOR 2

SOLDERING FOOTPRINT*



SC-88/SC70-6/SOT-363

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 61312, Phoenix, Arizona 85082–1312 USA Phone: 480–829–7710 or 800–344–3860 Toll Free USA/Canada Fax: 480–829–7709 or 800–344–3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800–282–9855 Toll Free USA/Canada

Japan: ON Semiconductor, Japan Customer Focus Center 2–9–1 Kamimeguro, Meguro–ku, Tokyo, Japan 153–0051 Phone: 81–3–5773–3850

ON Semiconductor Website: http://onsemi.com

Order Literature: http://www.onsemi.com/litorder

For additional information, please contact your local Sales Representative.

BC846BPDW1T1/D