



PDA143X/123J/143Z/114Y/124XQB series

50 V, 100 mA PNP resistor-equipped transistors

Rev. 1 — 28 September 2021

Product data sheet

1. General description

100 mA PNP Resistor-Equipped Transistor (RET) family in an ultra small DFN1110D-3 (SOT8015) leadless Surface-Mounted Device (SMD) plastic package with side-wettable flanks.

Table 1. Product overview

Type number	R1	R2	Package		NPN complement:
	k Ω	k Ω	Nexperia	JEDEC	
PDA143XQB	4.7	10	SOT8015	MO-340BA	PDTC143XQB
PDA123JQB	2.2	47			PDTC123JQB
PDA143ZQB	4.7	47			PDTC143ZQB
PDA114YQB	10	47			PDTC114YQB
PDA124XQB	22	47			PDTC124XQB

2. Features and benefits

- 100 mA output current capability
- Built-in resistors
- Simplifies circuit design
- Reduces component count
- Reduces pick and place costs
- Low package height of 0.5 mm
- Suitable for Automatic Optical Inspection (AOI) of solder joint

3. Applications

- Digital applications
- Cost saving alternative for BC857 series in digital applications
- Controlling IC inputs
- Switching loads

4. Quick reference data

Table 2. Quick reference data

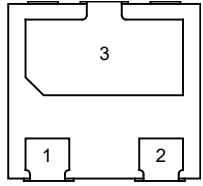
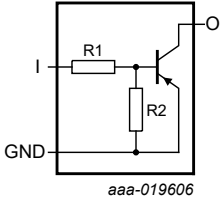
$T_{amb} = 25\text{ }^{\circ}\text{C}$ unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	-50	V
I_O	output current		-	-	-100	mA

nexperia

5. Pinning information

Table 3. Pinning

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	I	input (base)	 Transparent top view	 aaa-019606
2	GND	GND (emitter)		
3	O	output (collector)		

6. Ordering information

Table 4. Ordering information

Type number	Package		
	Name	Description	Version
PDTA143XQB	DFN1110D-3	plastic leadless extremely thin small outline package with side-wettable flanks (SWF); 3 terminals; 0.65 mm pitch; body: 1.1 x 1.0 x 0.48 mm	SOT8015
PDTA123JQB			
PDTA143ZQB			
PDTA114YQB			
PDTA124XQB			

7. Marking

Table 5. Marking

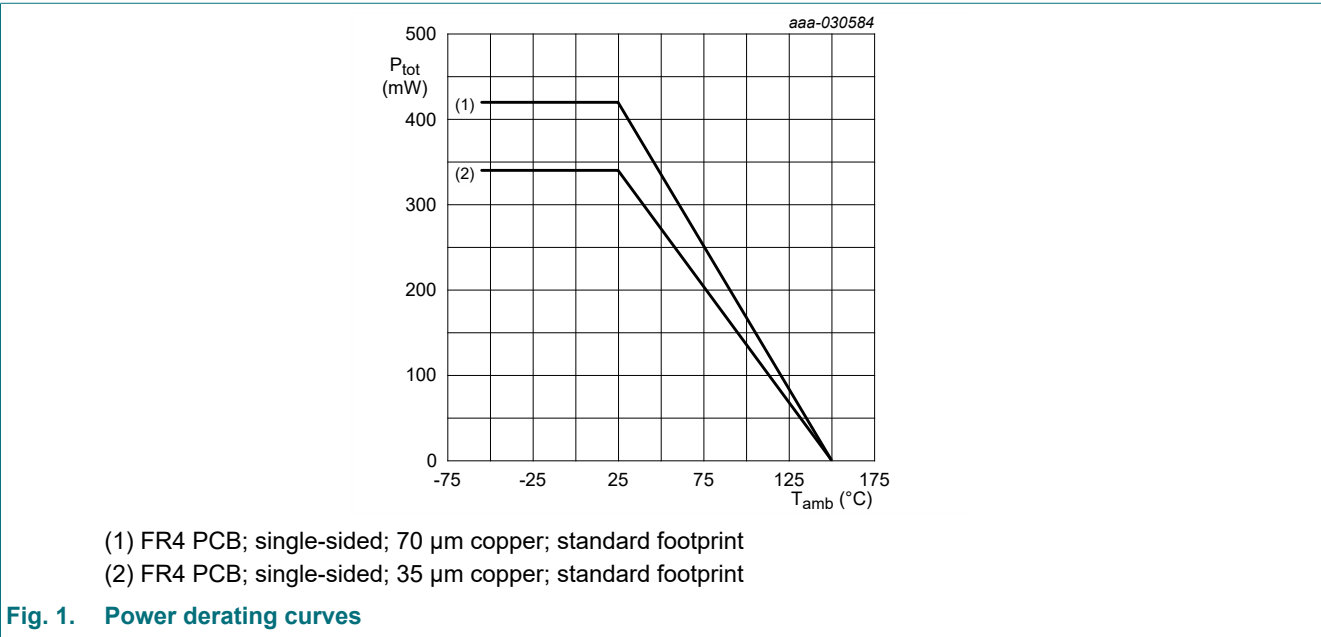
Type number	Marking code
PDTA143XQB	D6
PDTA123JQB	D2
PDTA143ZQB	D7
PDTA114YQB	C9
PDTA124XQB	D4

8. Limiting values

Table 6. Limiting values
In accordance with the Absolute Maximum Rating System (IEC 60134).
 $T_{amb} = 25\text{ °C}$ unless otherwise specified.

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter	-	-50	V
V_{CEO}	collector-emitter voltage	open base	-	-50	V
V_{EBO}	emitter-base voltage				
	PDTA143XQB	open collector	-	-7	V
	PDTA123JQB		-	-5	V
	PDTA143ZQB		-	-5	V
	PDTA114YQB		-	-6	V
	PDTA124XQB		-	-7	V
V_I	input voltage				
	PDTA143XQB		-30	+7	V
	PDTA123JQB		-12	+5	V
	PDTA143ZQB		-30	+5	V
	PDTA114YQB		-40	+6	V
	PDTA124XQB		-40	+7	V
I_O	output current		-	-100	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25\text{ °C}$	[1] -	340	mW
			[2] -	420	mW
T_j	junction temperature		-	150	°C
T_{amb}	ambient temperature		-55	150	°C
T_{stg}	storage temperature		-65	150	°C

- [1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided; 35 μm copper; tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB; single-sided; 70 μm copper; tin-plated and standard footprint.



9. Thermal characteristics

Table 7. Thermal characteristics

$T_{amb} = 25\text{ }^{\circ}\text{C}$ unless otherwise specified.

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	368	K/W
			[2]	-	-	298	K/W

- [1] Device mounted on an FR4 PCB; single-sided; 35 μm copper; tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB; single-sided; 70 μm copper; tin-plated and standard footprint.

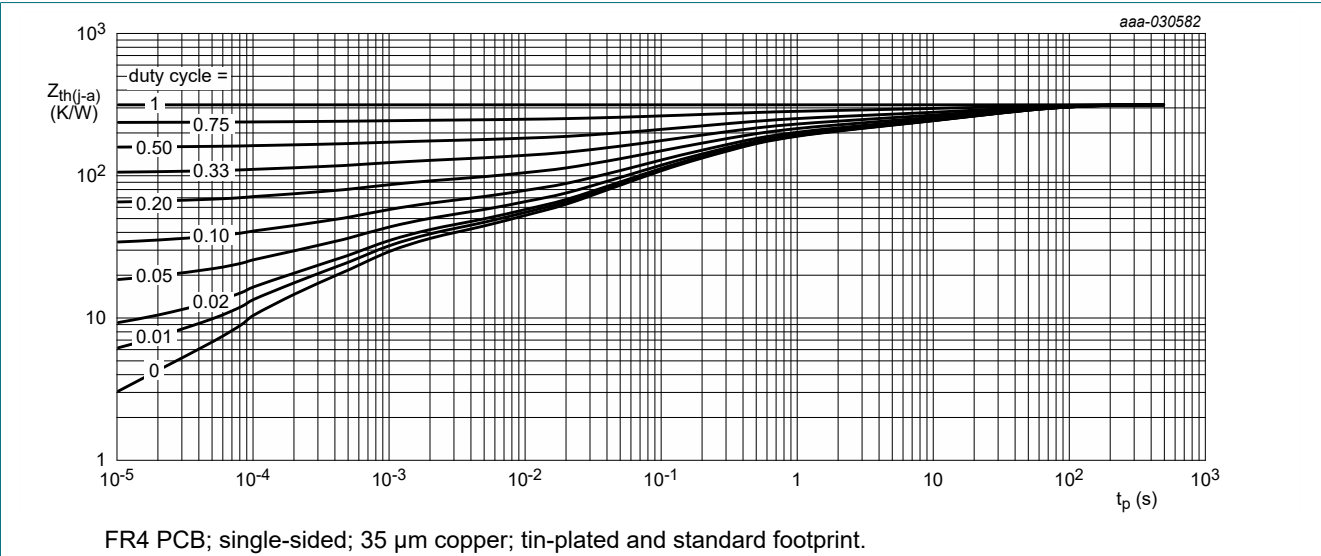


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

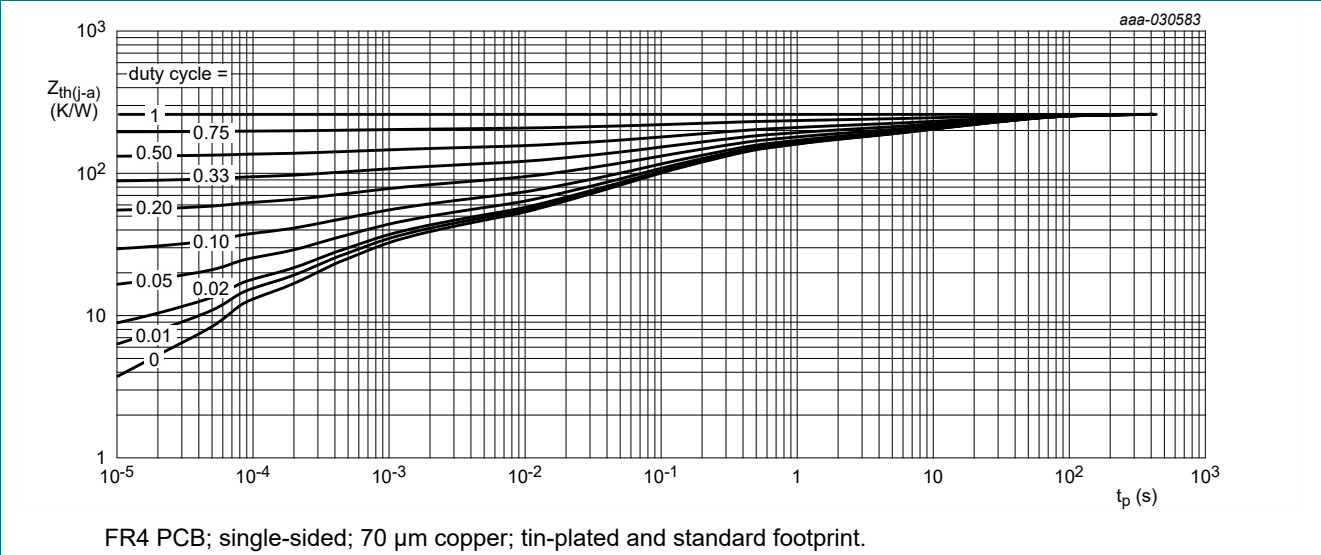


Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 8. Characteristics

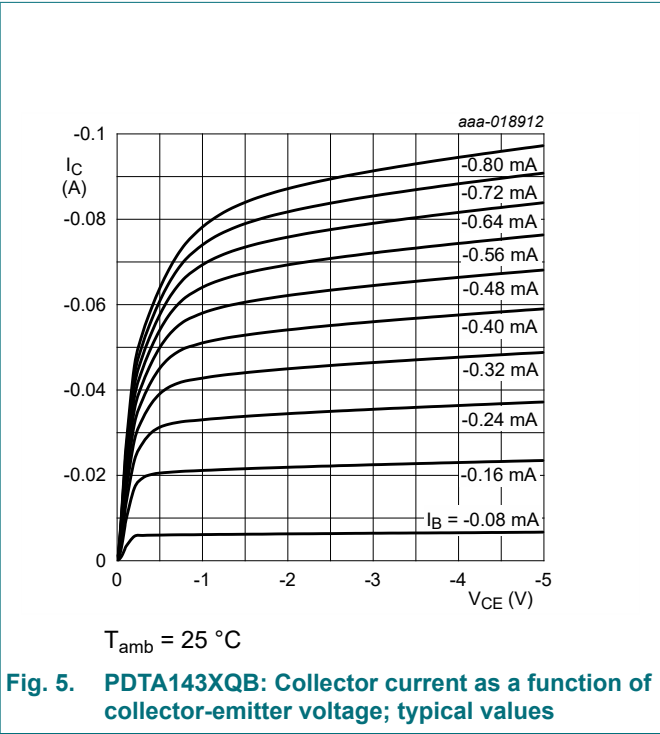
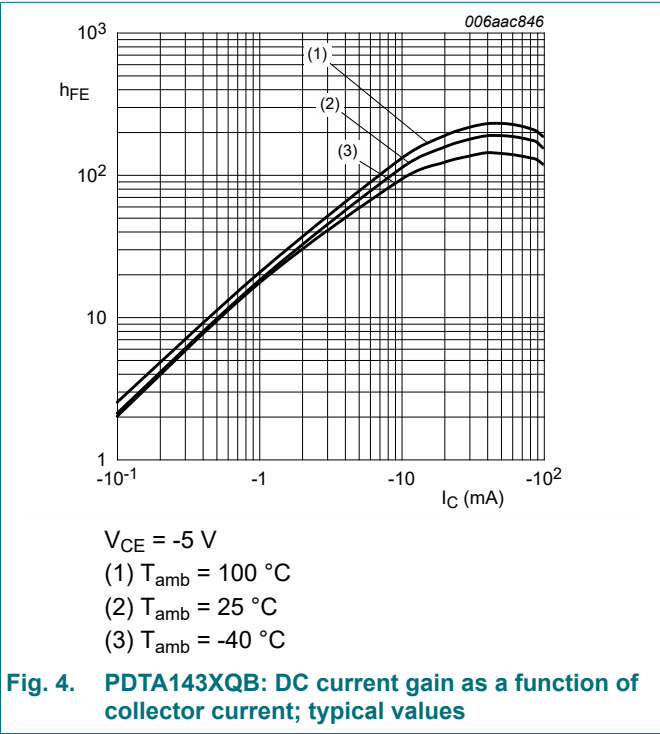
$T_{amb} = 25\text{ }^{\circ}\text{C}$ unless otherwise specified.

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	I _C = -100 μA; I _E = 0 A		-50	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = -2 mA; I _B = 0 A		-50	-	-	V
I _{CBO}	collector-base cut-off current	V _{CB} = -50 V; I _E = 0 A		-	-	-100	nA
I _{CEO}	collector-emitter cut-off current	V _{CE} = -30 V; I _B = 0 A		-	-	-100	nA
		V _{CE} = -30 V; I _B = 0 A; T _j = 150 °C		-	-	-5	μA
I _{EBO}	emitter-base cut-off current						
	PDTA143XQB	V _{EB} = -5 V; I _C = 0 A		-	-	-600	μA
	PDTA123JQB			-	-	-180	μA
	PDTA143ZQB			-	-	-170	μA
	PDTA114YQB				-150	μA	
	PDTA124XQB				-120	μA	
h _{FE}	DC current gain						
	PDTA143XQB	V _{CE} = -5 V; I _C = -10 mA		50	-	-	
	PDTA123JQB			100	-	-	
	PDTA143ZQB			100	-	-	
	PDTA114YQB	V _{CE} = -5 V; I _C = -5 mA		100	-	-	
	PDTA124XQB			80	-	-	
V _{CEsat}	collector-emitter saturation voltage						
	PDTA143XQB	I _C = -10 mA; I _B = -0.5 mA		-	-	-100	mV
	PDTA123JQB	I _C = -5 mA; I _B = -0.25 mA		-	-	-100	mV
	PDTA143ZQB			-	-	-100	mV
	PDTA114YQB			-	-	-100	mV
	PDTA124XQB	I _C = -10 mA; I _B = -0.5 mA		-	-	-100	mV
V _{I(off)}	off-state input voltage						
	PDTA143XQB	V _{CE} = -5 V ; I _C = -100 μA		-	-0.9	-0.3	V
	PDTA123JQB			-	-0.6	-0.5	V
	PDTA143ZQB			-	-0.6	-0.5	V
	PDTA114YQB			-	-0.7	-0.5	V
	PDTA124XQB			-	-0.8	-0.5	V
V _{I(on)}	on-state input voltage						
	PDTA143XQB	V _{CE} = -0.3 V ; I _C = -20 mA		-2.5	-1.5	-	V
	PDTA123JQB	V _{CE} = -0.3 V ; I _C = -5 mA		-1.1	-0.75	-	V
	PDTA143ZQB	V _{CE} = -0.3 V ; I _C = -5 mA		-1.3	-0.9	-	V
	PDTA114YQB	V _{CE} = -0.3 V ; I _C = -1 mA		-1.4	-0.8	-	V
	PDTA124XQB	V _{CE} = -0.3 V ; I _C = -2 mA		-2	-1.1	-	V

50 V, 100 mA PNP resistor-equipped transistors

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
R1	bias resistor 1 (input)						
	PDTA143XQB		[1]	3.3	4.7	6.1	kΩ
	PDTA123JQB			1.54	2.2	2.86	kΩ
	PDTA143ZQB			3.3	4.7	6.1	kΩ
	PDTA114YQB			7	10	13	kΩ
	PDTA124XQB			15.4	22	28.6	kΩ
R2/R1	bias resistor ratio						
	PDTA143XQB		[1]	1.7	2.13	2.6	
	PDTA123JQB			17	21	26	
	PDTA143ZQB			8	10	12	
	PDTA114YQB			3.7	4.7	5.7	
	PDTA124XQB			1.7	2.13	2.6	
f _T	transition frequency	V _{CE} = -5 V; I _C = -10 mA; f = 100 MHz	[2]	-	180	-	MHz
C _c	collector capacitance	V _{CB} = -10 V; I _E = i _e = 0 A; f = 1 MHz		-	-	3	pF

- [1] See "Section 11: Test information" for resistor calculation and test conditions
[2] Characteristics of built-in transistor



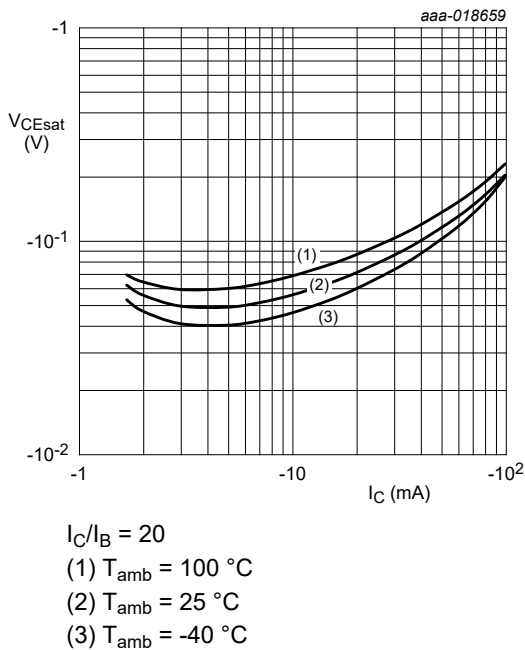


Fig. 6. PDTA143XQB: Collector-emitter saturation voltage as a function of collector current; typical values

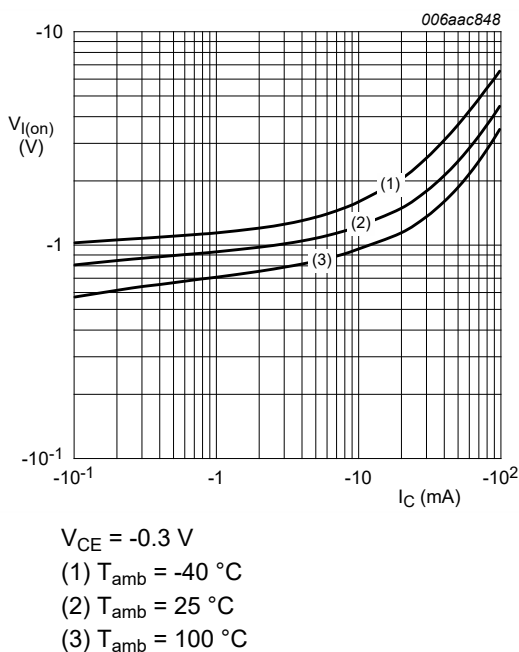


Fig. 7. PDTA143XQB: On-state input voltage as a function of collector current; typical values

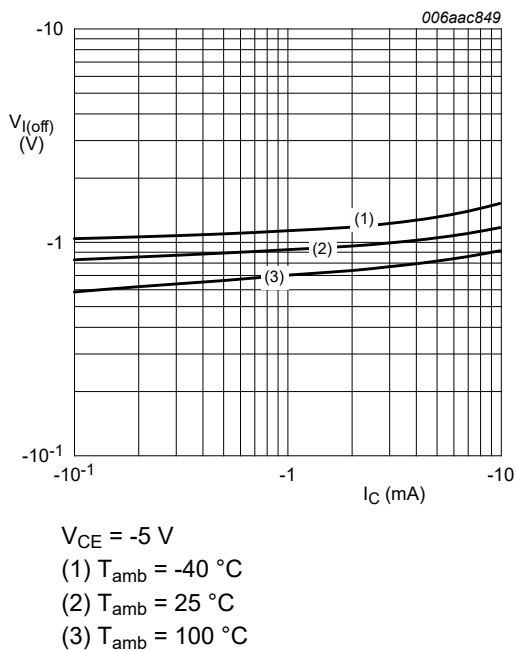


Fig. 8. PDTA143XQB: Off-state input voltage as a function of collector current; typical values

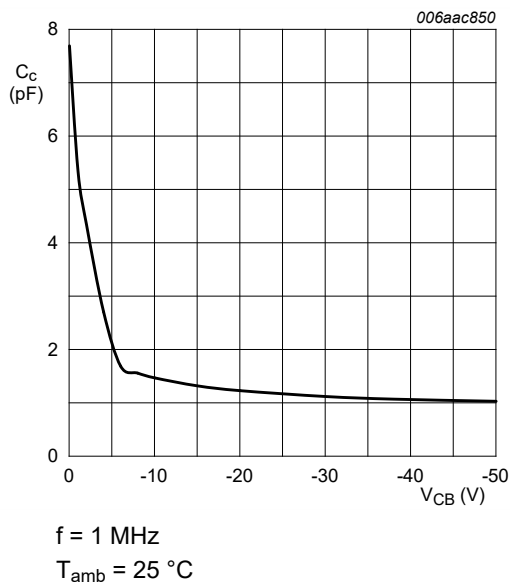


Fig. 9. PDTA143XQB: Collector capacitance as a function of collector-base voltage; typical values

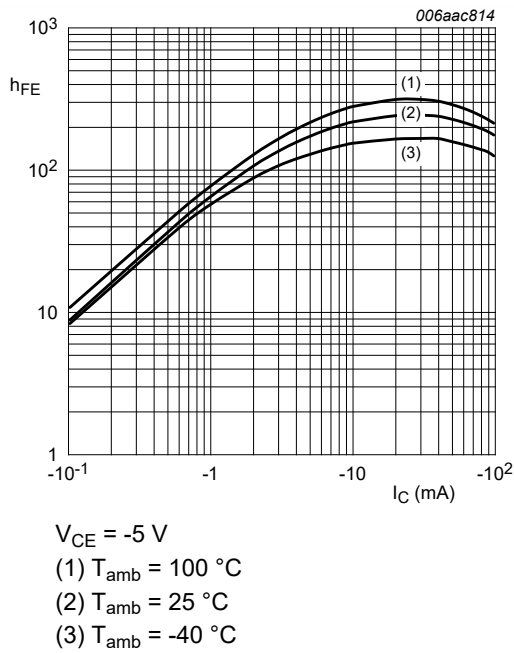


Fig. 10. PDTA123JQB: DC current gain as a function of collector current; typical values

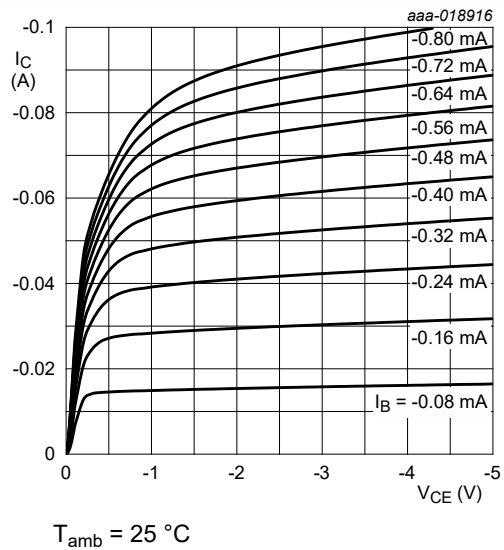


Fig. 11. PDTA123JQB: Collector current as a function of collector-emitter voltage; typical values

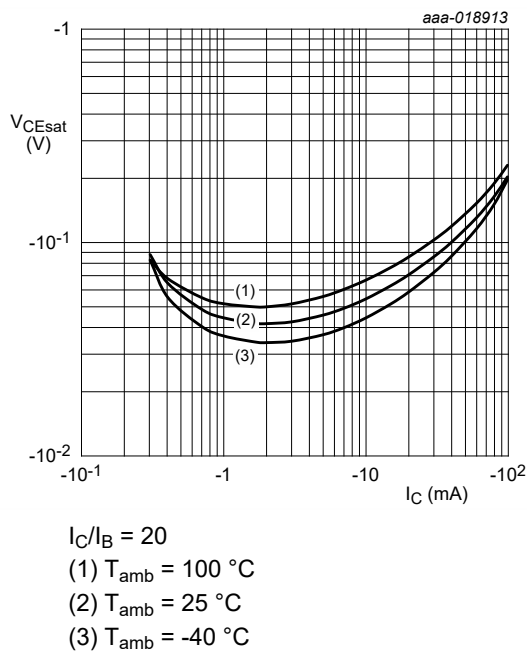


Fig. 12. PDTA123JQB: Collector-emitter saturation voltage as a function of collector current; typical values

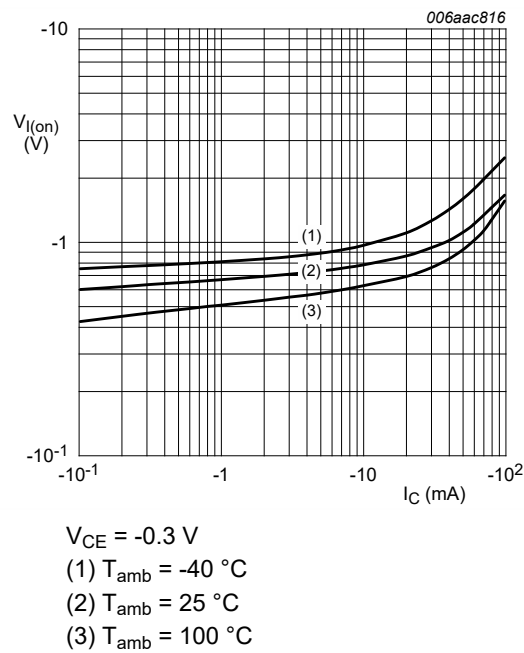


Fig. 13. PDTA123JQB: On-state input voltage as a function of collector current; typical values

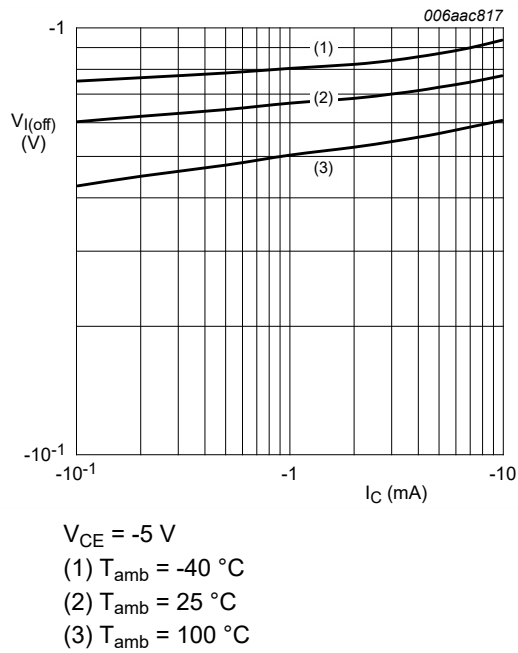


Fig. 14. PDTA123JQB: Off-state input voltage as a function of collector current; typical values

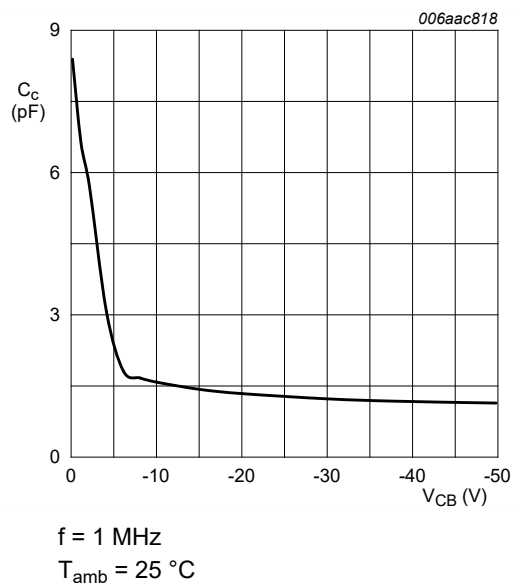


Fig. 15. PDTA123JQB: Collector capacitance as a function of collector-base voltage; typical values

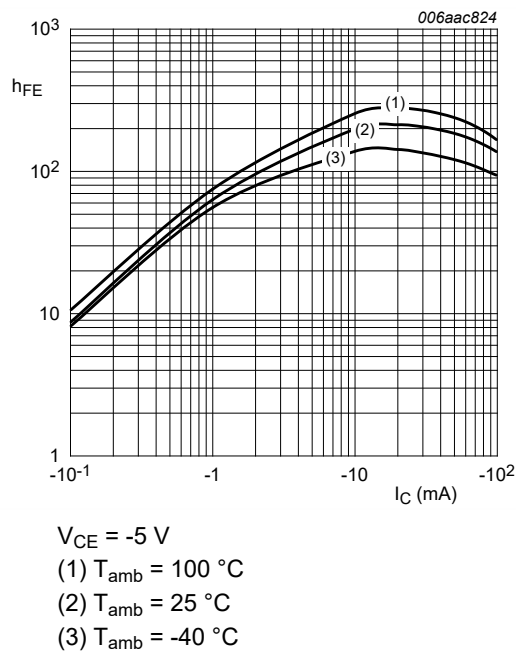


Fig. 16. PDTA143ZQB: DC current gain as a function of collector current; typical values

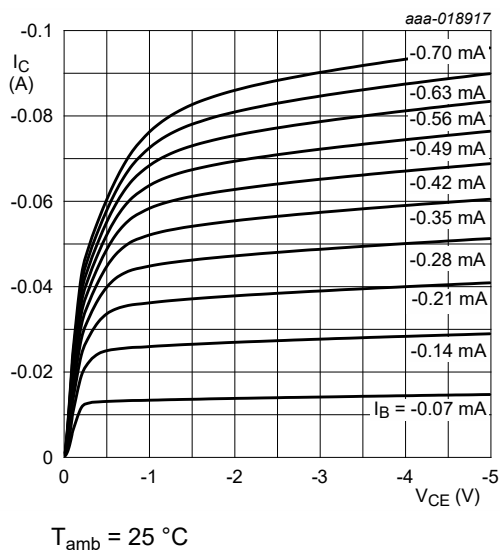


Fig. 17. PDTA143ZQB: Collector current as a function of collector-emitter voltage; typical values

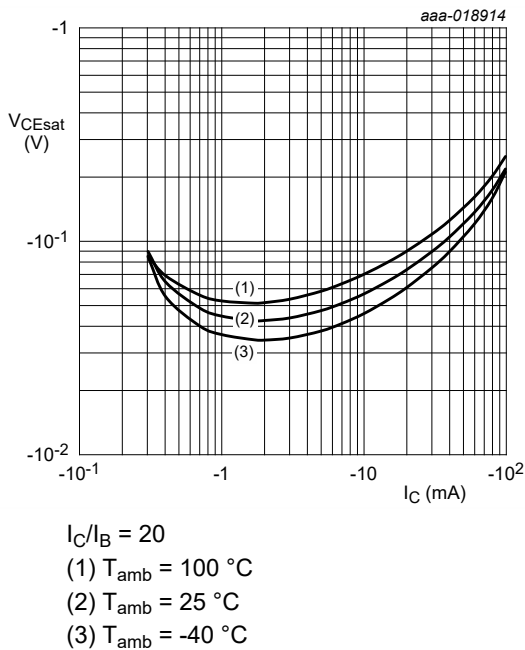


Fig. 18. PDTA143ZQB: Collector-emitter saturation voltage as a function of collector current; typical values

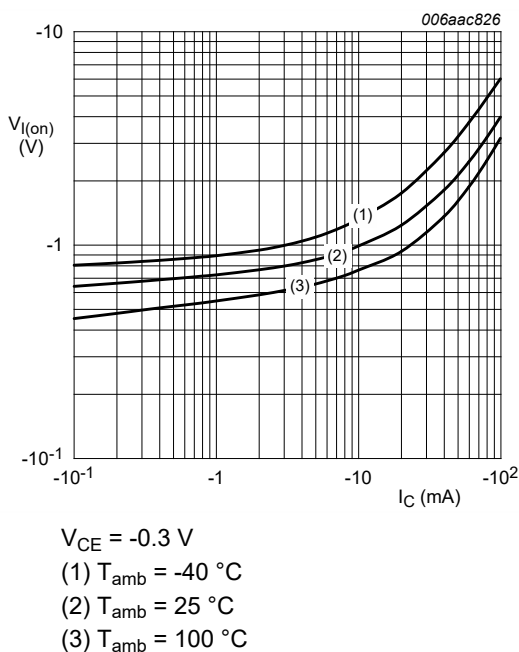


Fig. 19. PDTA143ZQB: On-state input voltage as a function of collector current; typical values

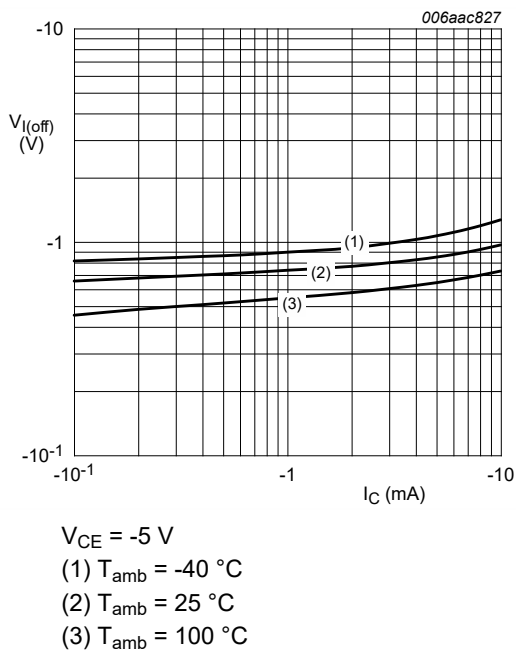


Fig. 20. PDTA143ZQB: Off-state input voltage as a function of collector current; typical values

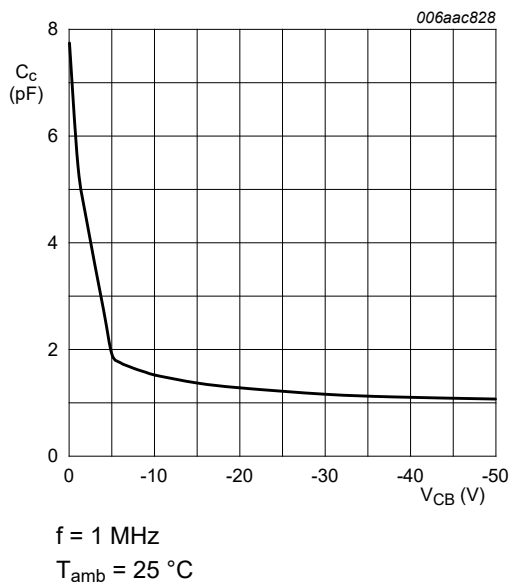


Fig. 21. PDTA143ZQB: Collector capacitance as a function of collector-base voltage; typical values

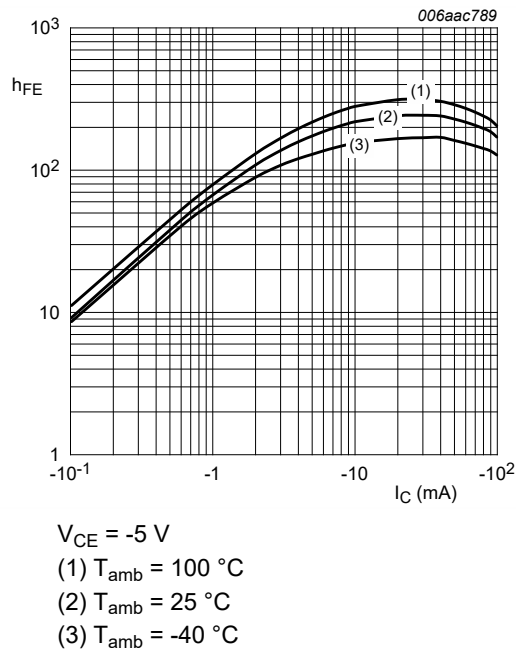


Fig. 22. PDTA114YQB: DC current gain as a function of collector current; typical values

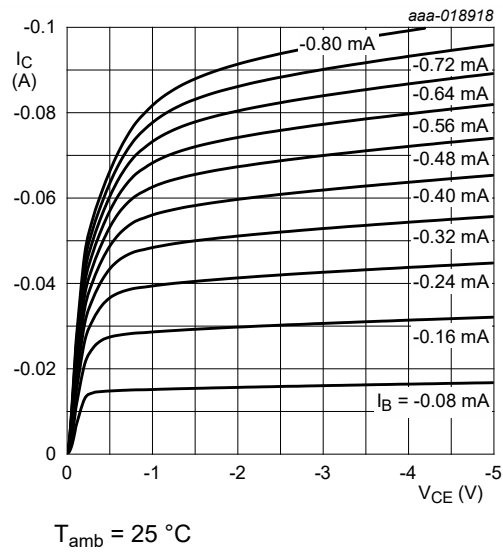


Fig. 23. PDTA114YQB: Collector current as a function of collector-emitter voltage; typical values

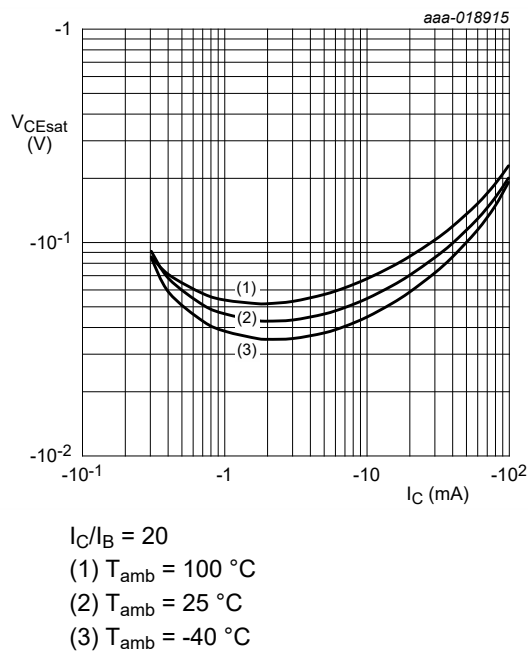


Fig. 24. PDTA114YQB: Collector-emitter saturation voltage as a function of collector current; typical values

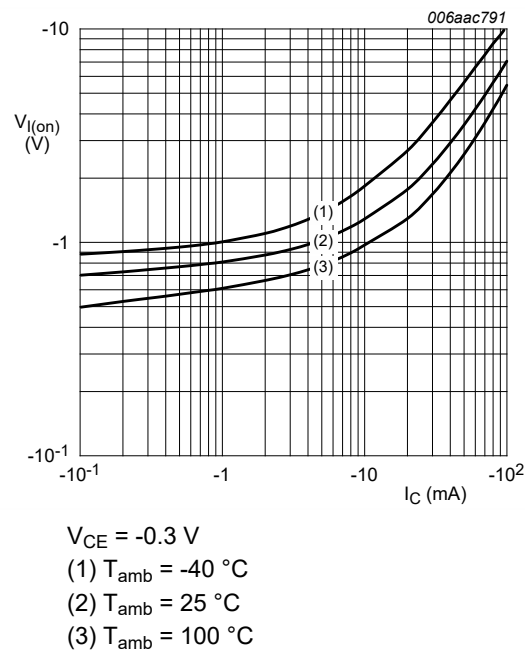


Fig. 25. PDTA114YQB: On-state input voltage as a function of collector current; typical values

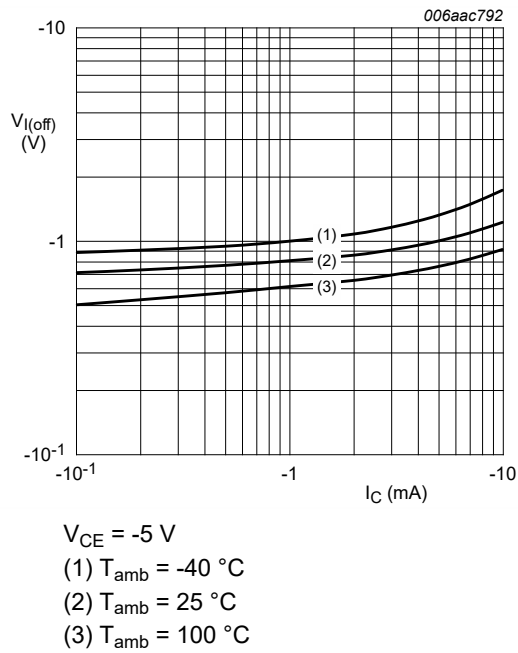


Fig. 26. PDTA114YQB: Off-state input voltage as a function of collector current; typical values

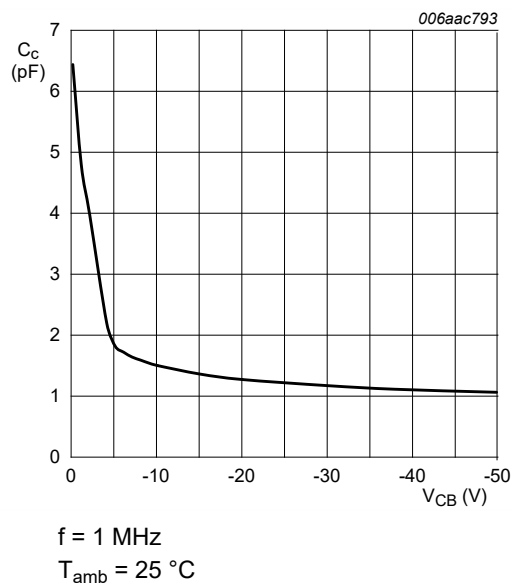


Fig. 27. PDTA114YQB: Collector capacitance as a function of collector-base voltage; typical values

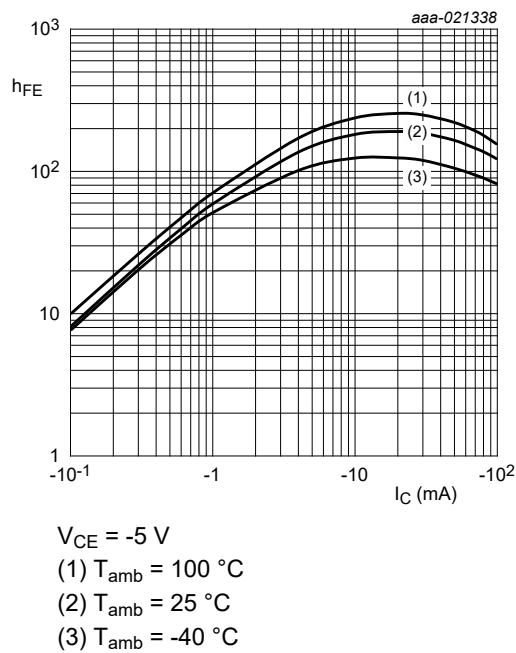


Fig. 28. PDTA124XQB: DC current gain as a function of collector current; typical values

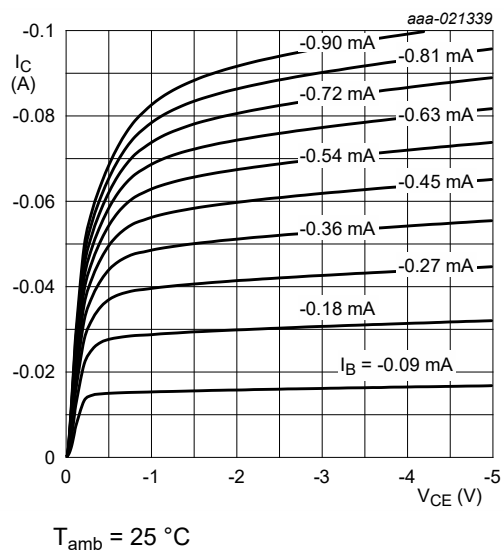


Fig. 29. PDTA124XQB: Collector current as a function of collector-emitter voltage; typical values

50 V, 100 mA PNP resistor-equipped transistors

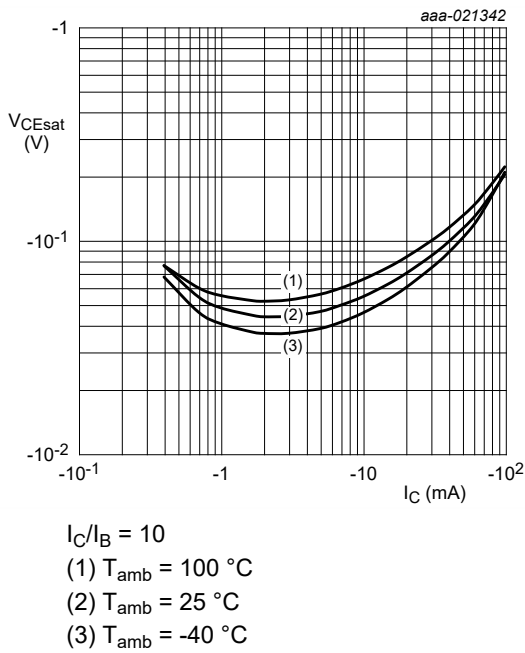


Fig. 30. PDTA124XQB: Collector-emitter saturation voltage as a function of collector current; typical values

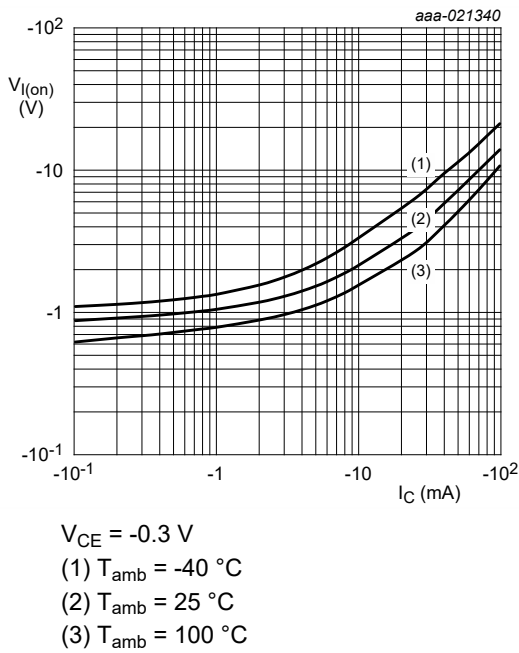


Fig. 31. PDTA124XQB: On-state input voltage as a function of collector current; typical values

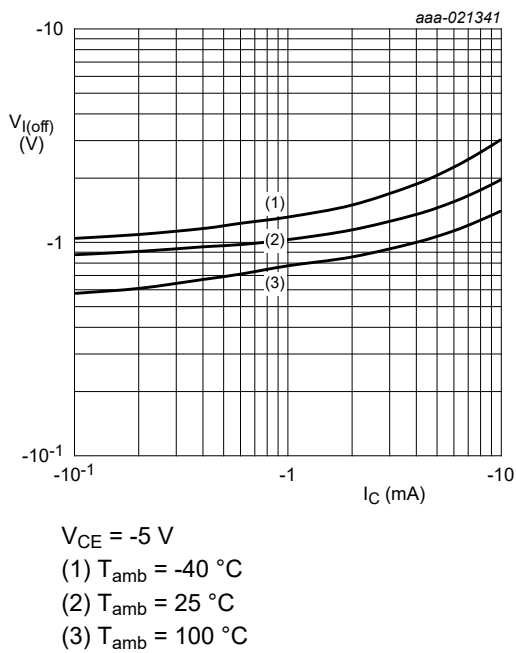


Fig. 32. PDTA124XQB: Off-state input voltage as a function of collector current; typical values

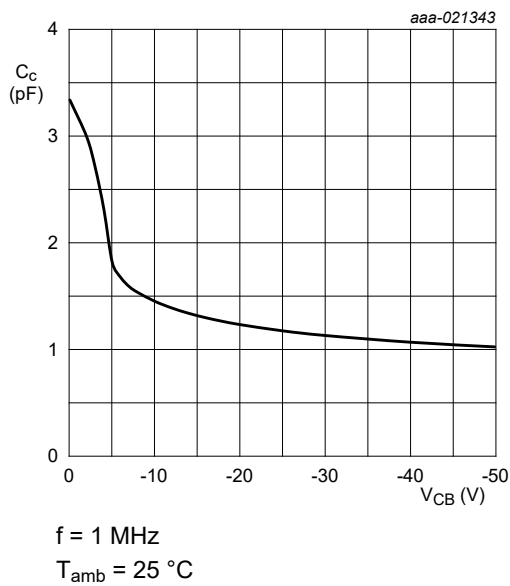
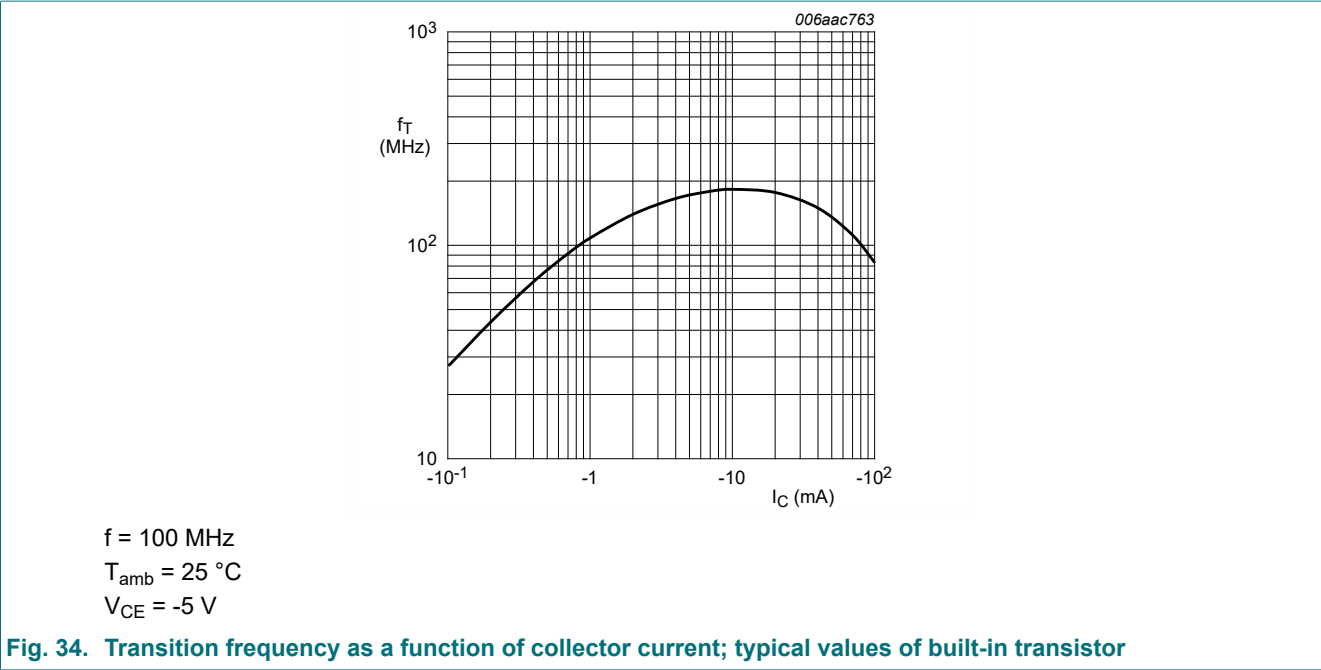


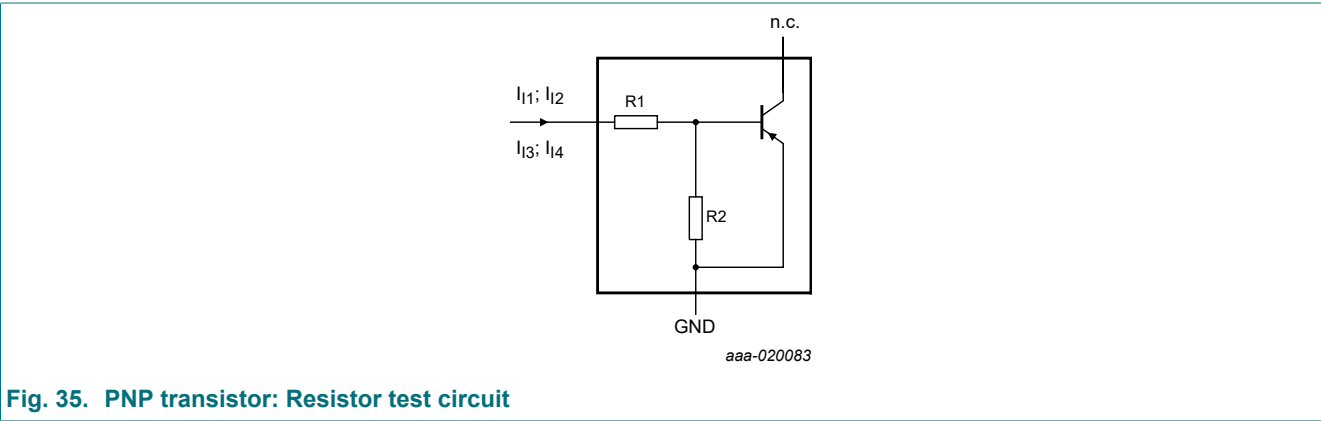
Fig. 33. PDTA124XQB: Collector capacitance as a function of collector-base voltage; typical values



11. Test information

Resistor calculation

- Calculation of bias resistor 1 (R1)
$$R1 = \frac{V(I12) - V(I11)}{I12 - I11}$$
- Calculation of bias resistor ratio (R2/R1)
$$\frac{R2}{R1} = \frac{V(I14) - V(I13)}{R1 \cdot (I14 - I13)} - 1$$



Resistor test conditions

Table 9. Resistor test conditions

Type number	R1 (kΩ)	R2 (kΩ)	Test conditions			
			I11	I12	I13	I14
PDTA143XQB	4.7	10	-350 μA	-450 μA	350 μA	450 μA
PDTA123JQB	2.2	47	-90 μA	-140 μA	55 μA	105 μA
PDTA143ZQB	4.7	47	-90 μA	-140 μA	55 μA	105 μA
PDTA114YQB	10	47	-90 μA	-140 μA	55 μA	105 μA
PDTA124XQB	22	47	-55 μA	-105 μA	55 μA	105 μA

12. Package outline

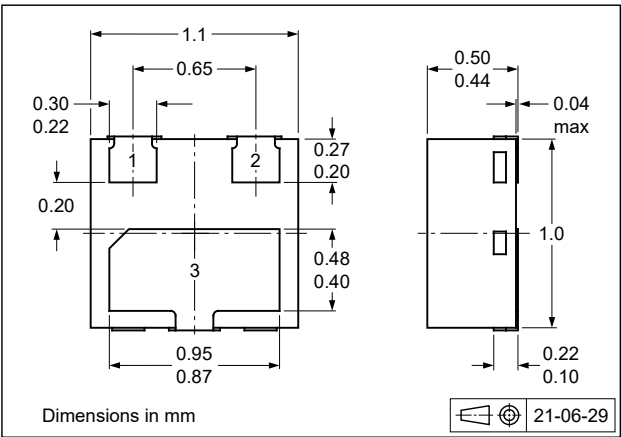


Fig. 36. Package outline DFN1110D-3 (SOT8015)

13. Soldering

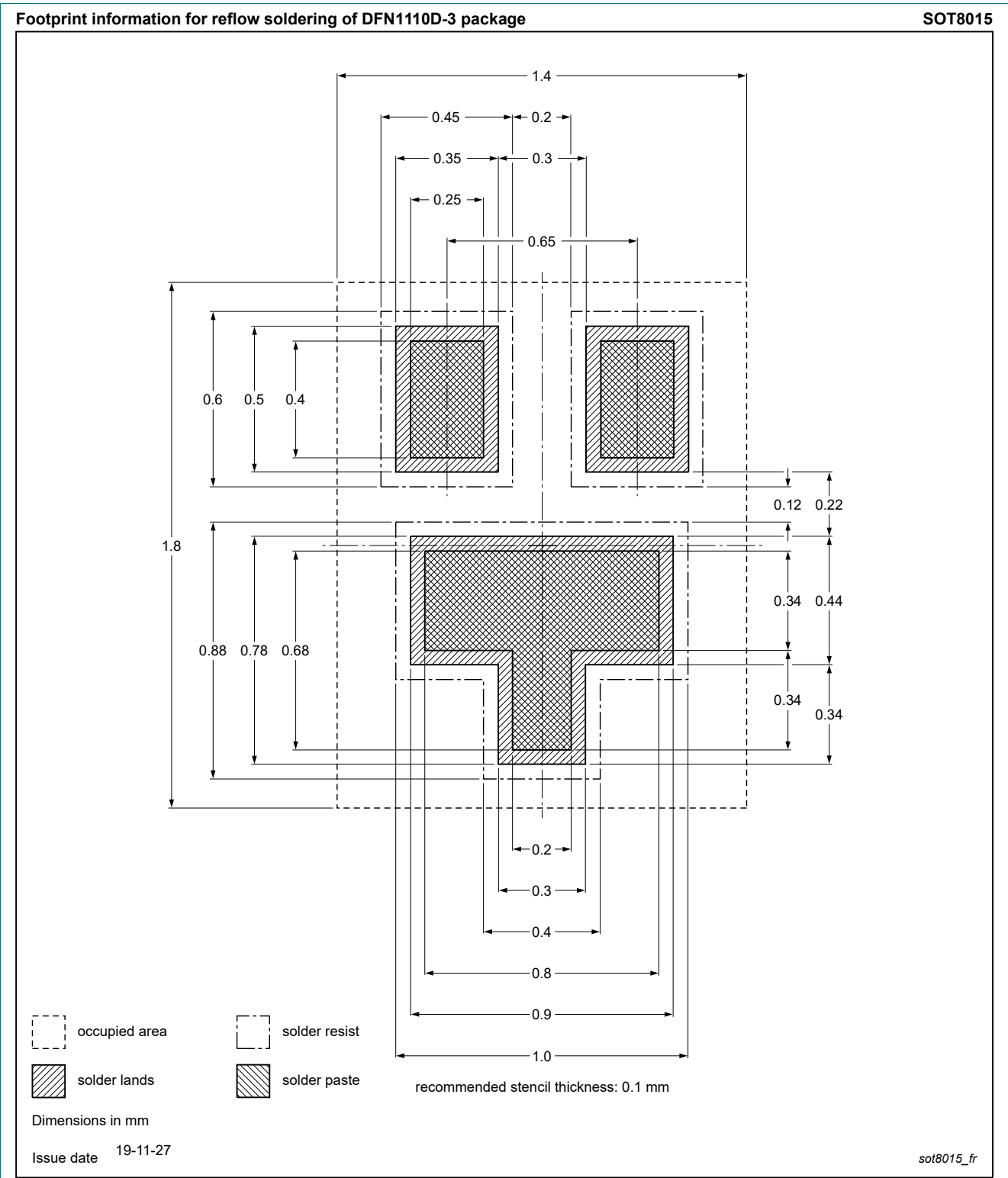


Fig. 37. Reflow soldering footprint DFN1110D-3 (SOT8015)

14. Revision history

Table 10. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PDTA143X_to_124XQB_SER v.1	20210928	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

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50 V, 100 mA PNP resistor-equipped transistors

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Date of release: 28 September 2021