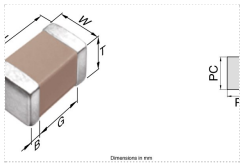
Multilayer Ceramic Chip Capacitors

C5750X7R1C226M280KA

RoHS | REACH SVHC-Free | Halogen Free | Lead Free

TDK item description	C5750X7R1C226MT****
Applications	Commercial Grade
Feature	General (Up to 50V)
Series	C5750 [EIA 2220]
Status	Production (Not Recommended for New Design)



Images are for reference only and show exemplary products.

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Documents
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- RoHS Certificate
- SVHC/REACH Certificate
- Characterization Sheet
- Promotion Video
- Sample Kits

Technical Support Tools
- S-parameter
- SPICE Netlist (Simple)
- SPICE Netlist (Precision)
- Equivalent Circuit Model
- DC Bias/Superimpotision Model (HSPICE)

Pages related to the series to which this product belongs
- Description of icons | Environment

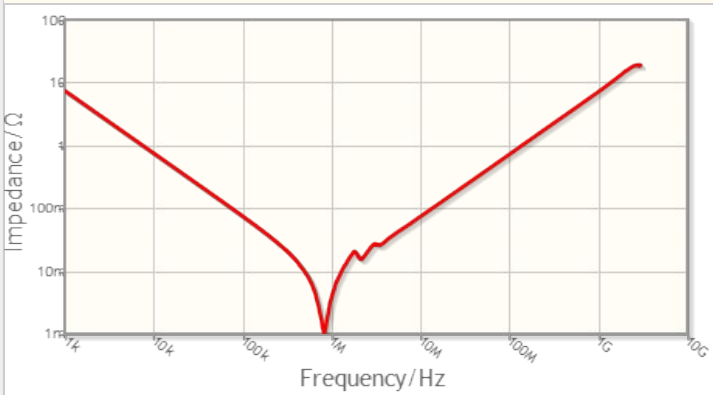
Size	
Length(L)	5.70mm ±0.40mm
Width(W)	5.00mm ±0.40mm
Thickness(T)	2.80mm ±0.30mm
Terminal Width(B)	0.20mm Min.
Terminal Spacing(G)	
Recommended Land Pattern (PA)	4.10mm to 4.80mm
Recommended Land Pattern (PB)	1.20mm to 1.40mm
Recommended Land Pattern (PC)	4.00mm to 5.00mm

Electrical Characteristics	
Capacitance	22µF ±20%
Rated Voltage	16VDC
Temperature Characteristic	X7R(±15%)
Dissipation Factor (Max.)	5%
Insulation Resistance (Min.)	4MΩ

Other	
Soldering Method	Reflow
AEC-Q200	No
Packing	Blister (Plastic)Taping [180mm Reel]
Package Quantity	500pcs

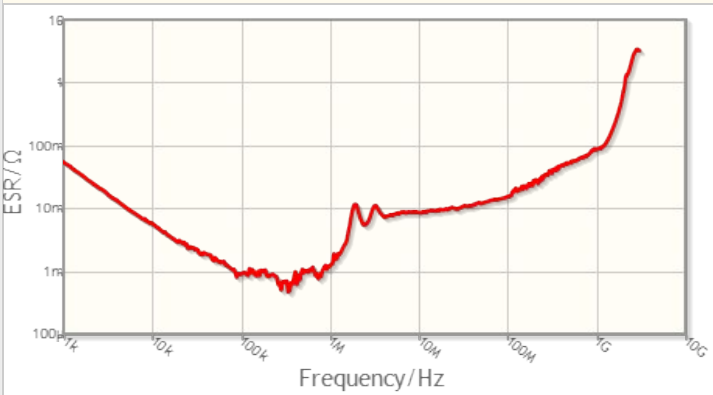
Characteristic Graph (This is reference data, and does not guarantee the products characteristics.)

Impedance



C5750X7R1C226M280KA

ESR



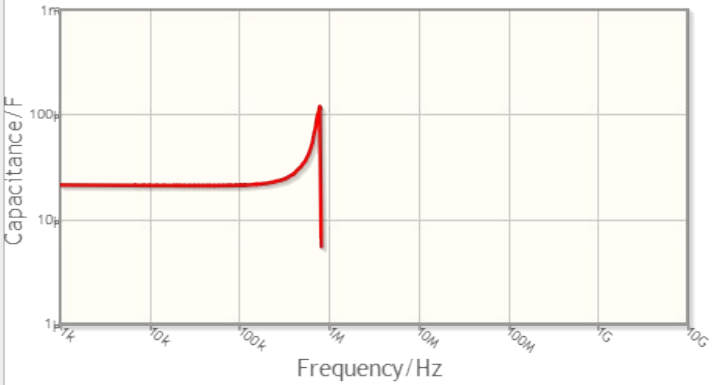
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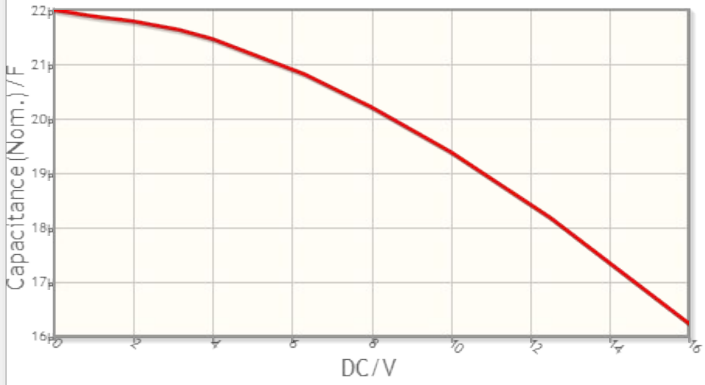
Capacitance



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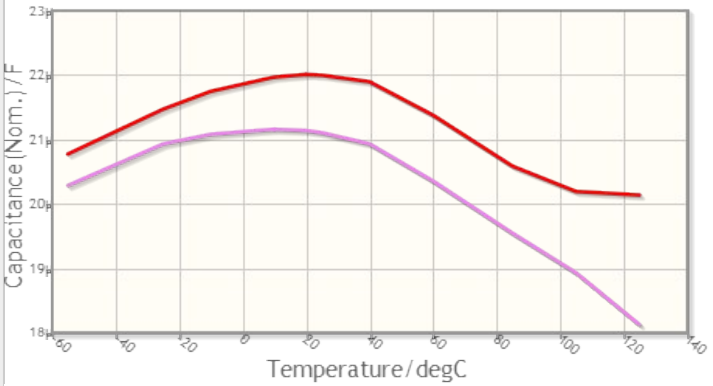
DC Bias Characteristic



C5750X7R1C226M280KA

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Temperature Characteristic

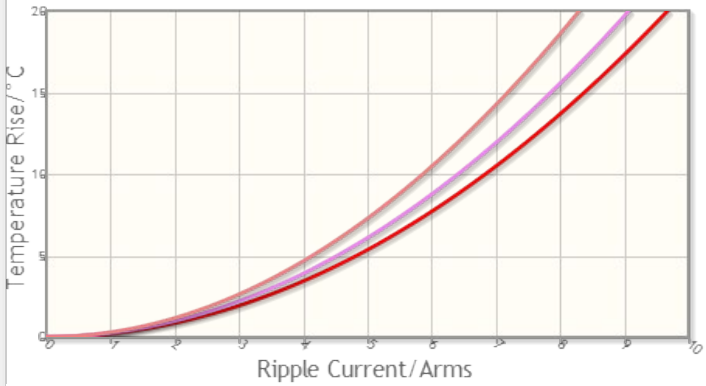


C5750X7R1C226M280KA(No Bias)

C5750X7R1C226M280KA(DC Bias = 8V)

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Ripple Temperature Rising



C5750X7R1C226M280KA(100kHz)

C5750X7R1C226M280KA(500kHz)

C5750X7R1C226M280KA(1MHz)

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