TOSHIBA TPD1053F

Toshiba Intelligent Power Device Silicon Monolithic Power MOS Integrated Circuit

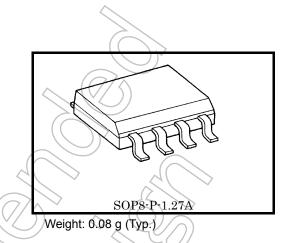
TPD1053F

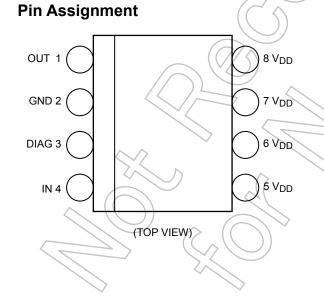
Motor, Solenoid, Lamp Drivers High-side Power Switch

The TPD1053F is a monolithic power IC for high-side switches. The IC has a vertical MOSFET output which can be directly driven from a CMOS or TTL logic circuit (e.g., an MPU). The device offers intelligent self-protection and diagnostic functions.

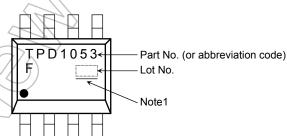
Features

- A monolithic power IC with a structure combining a control block (Bi-CMOS) and a vertical power MOSFET on a single chip.
- One side of load can be grounded to a high-side switch.
- Can directly drive a power load from a microprocessor.
- Built-in protection against over temperature and load short-circuiting.
- Incorporates a diagnosis function that allows diagnosis output to be read externally at load short-circuiting, opening, or over temperature.
- Up to -16(Typ.) of counter electromotive force from an inductance load can be applied.
- Low on-resistance : $R_{DS(ON)}=0.12\Omega(Max)$ (@ $V_{DD}=12$ V, $T_{ch}=25$ °C, $I_{O}=2$ A)
- 8-pin SOP package for surface mounting that can be packed in tape.





Marking



• on the lower left of the marking indicates Pin 1.

* Weekly code : (Three digits)

Week of manufacture
(01 for first week of year, continuing up to 52 or 53)

Year of manufacture

(The last digit of the calendar year)

Note 1 : A line under a Lot No. indentifies the indication of product Labels.

Not underlined : [[Pb]]/INCLUDES > MCV

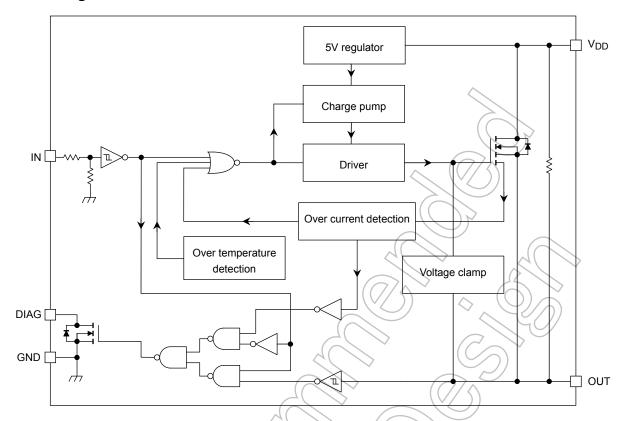
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS[[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

Due to its MOS structure, this product is sensitive to static electricity.

Start of commercial production 2011-05

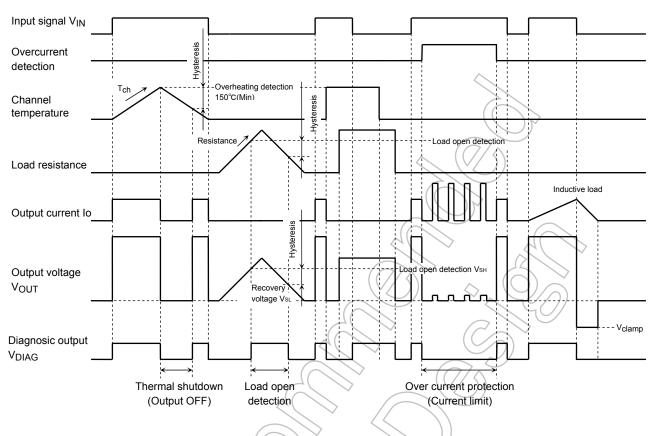
Block Diagram



Pin Description

Pin No	Symbol	Function
1	OUT	Output pin. When the load is short-circuited and current in excess of the detection current (3A min) flows to the output pin, the output automatically turns on or off.
2	GND	Ground pin.
3	DIAG	Self-diagnosis detection pin. Goes low when over temperature is detected or when output is short circuit with input on (high). N-channel open drain.
4	IN	Input pin. Input is CMOS compatible, with pull down resistor connected. Even if the input is open, output will not accidentally turn on.
5,6,7,8	√ y p̀D	Power pin.

Timing Chart



Truth Table

Input signal V _{IN}	Output voltage V _{OUT}	Output state	Operating state
L	L	off	Normal
Н	Н ((// on	Nonlai
L	H (Note 2)	Off	(Vand onen
Н	((H)	on <	Load open
L	7	off	
Н		Current limit	Load short
		(Switching)	
L	\\ L	off	Over temperature
Н		off	Over temperature

Note 2: Internal voltage in TPD1053F and external voltage decide this output voltage.

Input signal V _{IN}	Output voltage VOUT	Diagnosis V _{DIAG}	Diagnosis state
L	V _{OUT} < V _{SL}	L	Normally off
	V _{OUT} ≥ V _{SH}	Н	Load open
Н	$V_{OUT} \ge V_{DD} - V_{OC}$	Н	Normally on
	V _{OUT} < V _{DD} -V _{OC}	L	Over current(Load short), Over temperature

 $V_{SH}, V_{SL}: Schmitt \ inverter \ threshold \ voltage (V_{SH}=2.2V(Typ.), \ V_{SL}=1.8V(Typ.) \\ @V_{DD}=12V, \ T_{ch}=25^{\circ}C)$

 $V_{\mbox{OC}}$: Over current detection threshold voltage

Absolute Maximum Ratings ($T_a = 25$ °C)

Characteristics	3	Symbol	Rating	Unit
Drain-source voltage		V _{DS}	60	V
O	DC	V _{DD(1)}	-0.5 to 25	V
Supply voltage	Pulse	V _{DD(2)}	60(R _S =1Ω,τ=250ms)	V
	DC	V _{IN(1)}	-0.5 to 12	V
Input voltage	Pulse	ulse V _{IN(2)} V _{DD(1)} +1.5(t=100ms)		V
Diagnosis output voltage		V_{DIAG}	-0.5 to 25	٧
Output current		Io	Internally Limited	A(()
Input current		I _{IN}	±10	mA
Diagnosis current		I _{DIAG}	5	mA
Power dissipation (Note 3-a)		P _{D(1)}	1.1))>//
Power dissipation (Note 3-b)		P _{D(2)}	0.425	⇒ w
Operating temperature		Topr	-40 to 125	°C 🔇
Channel temperature		T _{ch}	150	°C
Storage temperature		T _{stg}	-55 to 150	°C (

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

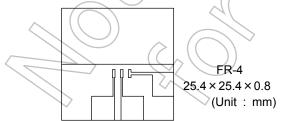
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Resistance

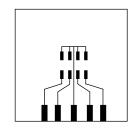
Characteristic	Symbol	Rating	Unit	
The amount of the second of th	7PIL(A)	113.5 (Note 3a)	°C /W	
Thermal resistance	R _{th(ch-a)}	294.0 (Note 3b)	C/W	

Note 3:

3-a: Glass epoxy board (a)



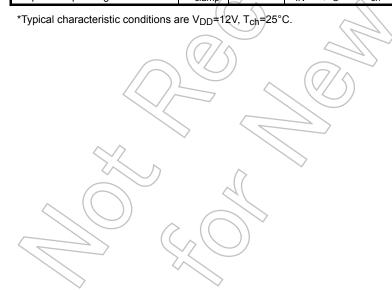
3-b : Glass epoxy board (b)



FR-4 $25.4 \times 25.4 \times 0.8$ (Unit: mm)

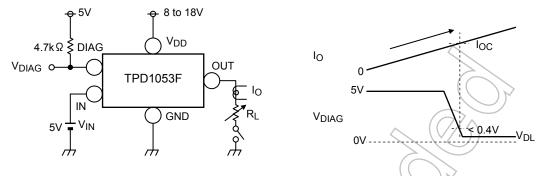
Electrical Characteristics (Unless otherwise specified, T_{ch} = -40 to 125 °C, V_{DD} = 5 to 18 V)

Characteristics		Symbol	Test circuit	Test condition	Min	Тур.	Max	Unit
Operating supply voltage		V _{DD(opr)}	-	-	5	12	18	V
Supply current		I _{DD(off)}	-	V _{IN} =0V, Output open		0.8	4.5	mA
		I _{DD(on)}	-	V _{IN} =5V, Output open		1.4	5	mA
Input voltage		V _{IH}	-	V _{DD} =8 to 18V 3.5		-	-	٧
		V _{IL}	-	V _{DD} =8 to 18V) /-	1.5	٧
Input current		I _{IN(1)}	-	V _{IN} =5V		25	200	μΑ
		I _{IN(2)}	-	V _{IN} =0V	(1)	-	1	μΑ
On resistance		R _{DS(ON)}	-	V _{DD} =8 to 18V, I _O =2A, T _{ch} =25°C	> -	0.09	0.12	Ω
Output leakage current		l _{OL}	-	V _{IN} =0V, V _{OUT} =0V		0.2	2	mA
Diagnosis output voltage	"L"-level	V _{DL}	-	I _{DIAG} =1mA	-		0.4	٧
Diagnosis output current	"H"-level	I _{DH}	-	V _{DIAG} =18V	> - (5)-6	10	μΑ
Over current detection		loc	1, 2	V _{DD} =8 to 18V	3	6	9	Α
Over temperature	Temperature	T _{OT}	-		150	170	200	°C
detection	Hysteresis	ΔT_{OT}	-		(-1)	10		°C
Load open detection		R _{OP}	3	V _{DD} =8 to 18V, V _{IN} =0V	0.5	15	250	kΩ
Switching time		t _{on}	t _{on} 4	V _{DD} =12V, R _L =10Ω, T _{ch} =25°C	<i>))</i> -	50	100	μs
		t _{off}		VDD 12 V, 11L 1032, 1CH 23 0	-	40	80	μs
Diagnosis delay time		t _{DLH}	5	V _{DD} =12V, R _L =10Ω, T _{ch} =25°C	-	40	-	μs
. ,		t _{DHL}			-	40	-	μs
Output clamp voltage		V _{clamp}		V _{IN} =0V, I _O =1A, T _{ch} =25°C	-25	-16	-10	V



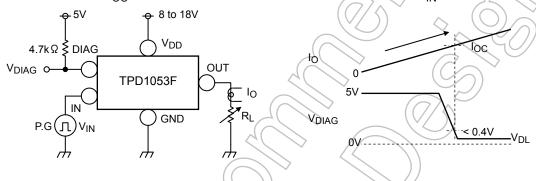
Test Circuit 1

Over current detection I_{OC} : Over current detection when load current is increased while V_{IN} = "H".



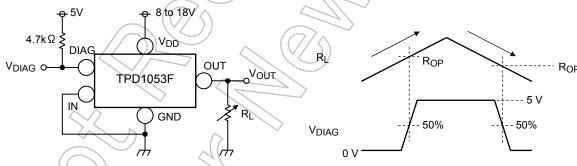
Test Circuit 2

Over current detection I_{OC} : Over current detection when load is short circuit and V_{IN} = "L" \rightarrow "H



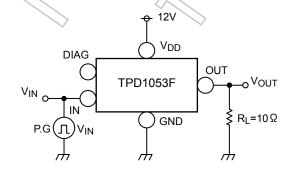
Test Circuit 3

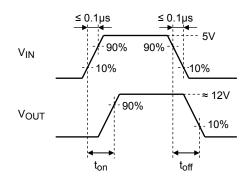
Load open detection R_{OP}



Test Circuit 4

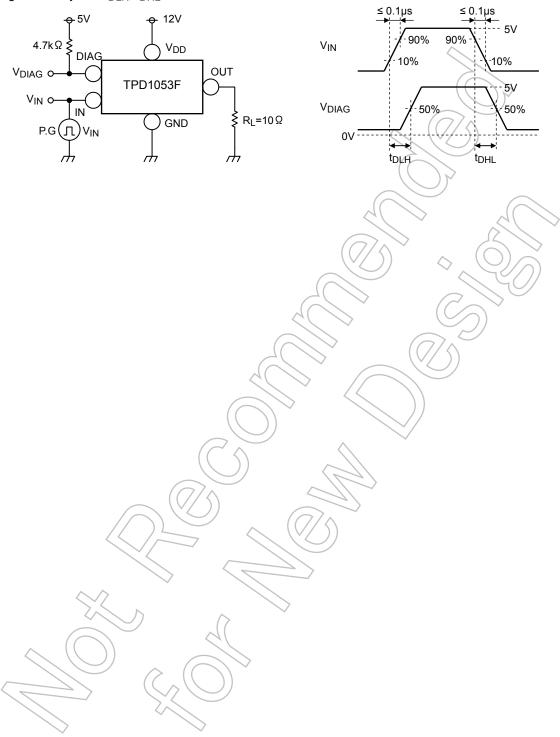
Switching times ton, toff

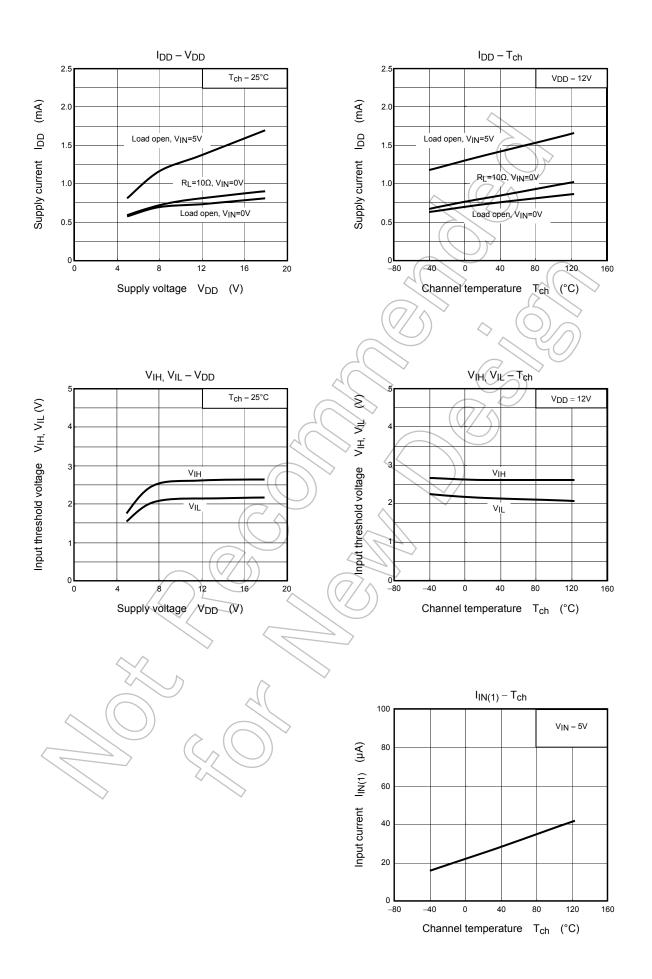


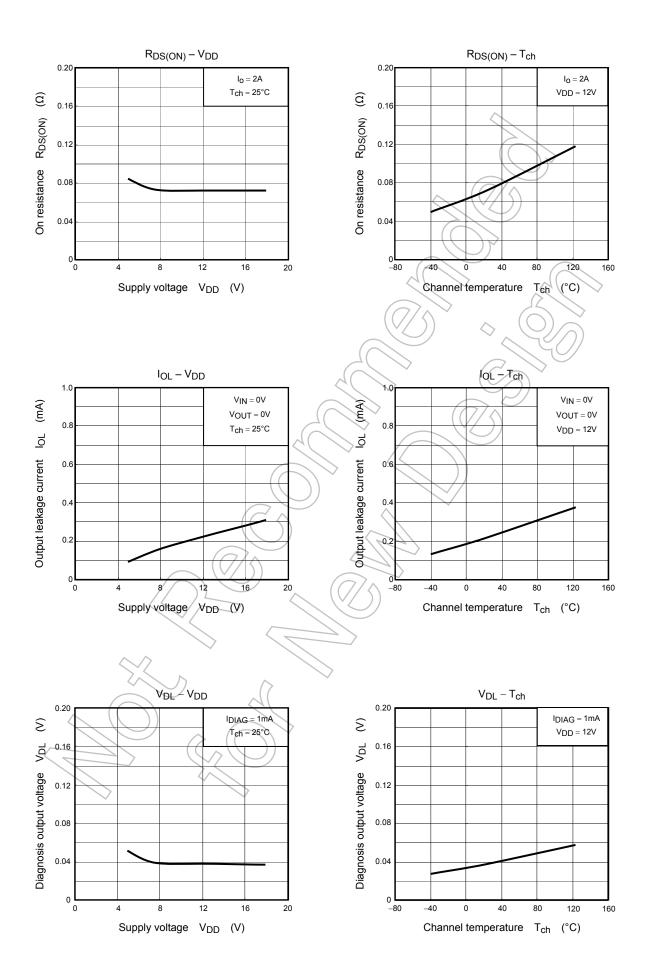


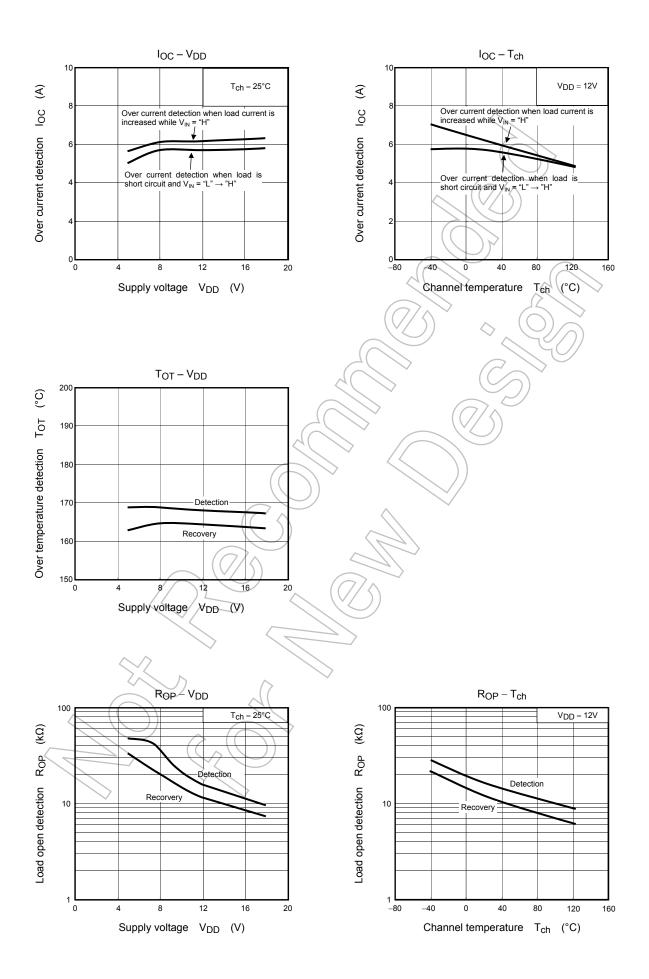
Test Circuit 5

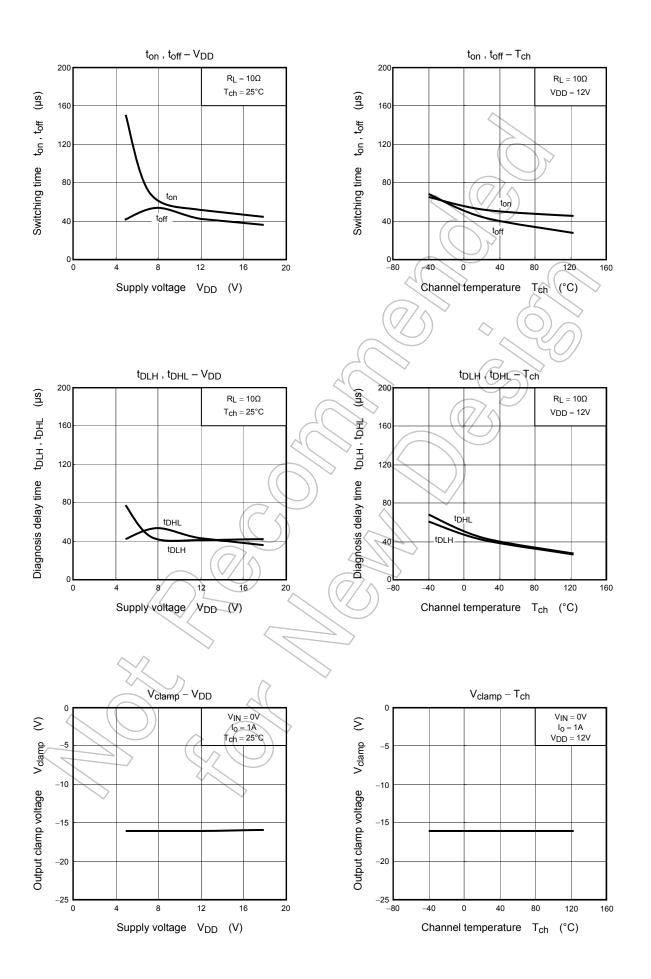
Diagnosis delay time t_{DLH}, t_{DHL}

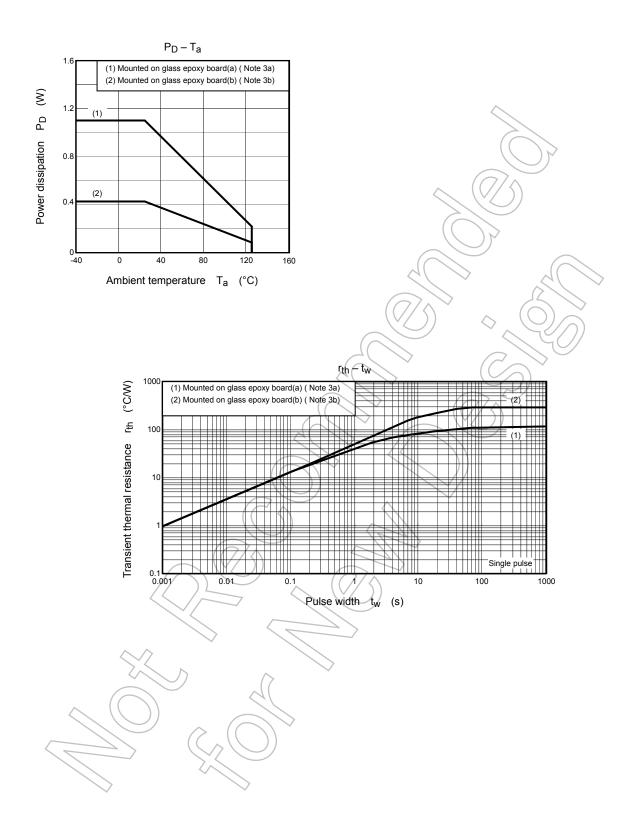






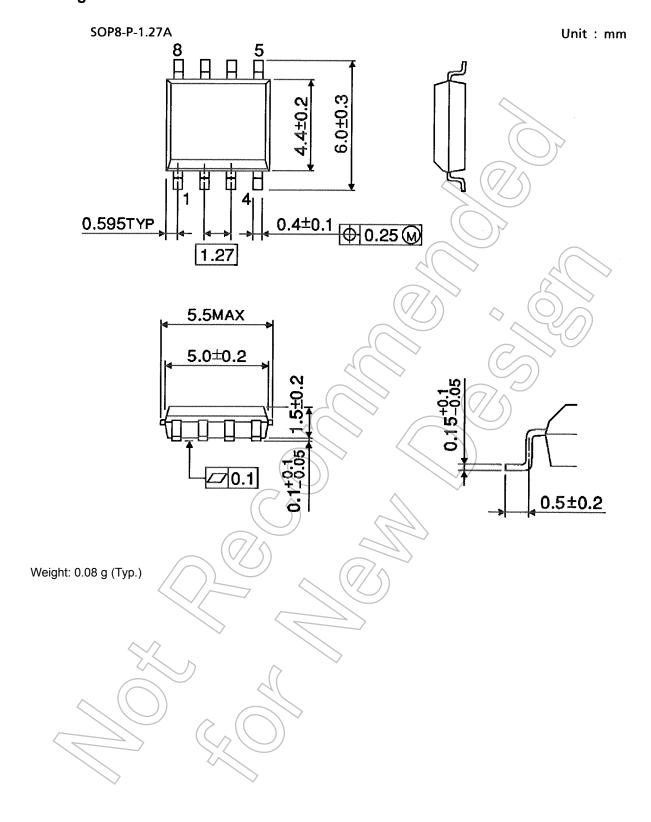








Package Dimensions



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